

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.02mm	SM-001	Solder Mask	GTS
Lead-Free	Top Surface Finish	0.02mm		Surface Finish	
CF-004	Top Layer	0.04mm		Signal	GTL
Core		1.53mm	Core-043	Dielectric	
CF-004	Bottom Layer	0.04mm		Signal	GBL
Lead-Free	Bottom Surface Finish	0.02mm		Surface Finish	
Surface Material	Bottom Solder	0.02mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.68mm					

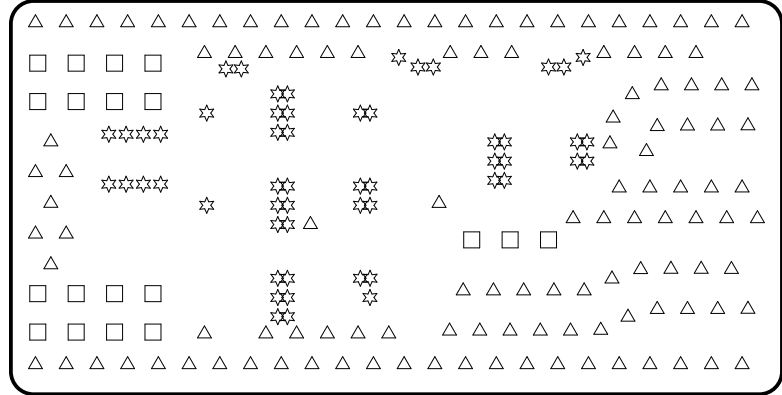
Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Wide Trace Width	Narrow Trace Width	Gap	Reference layers	Substack
1	Coated Coplanar Waveguide With Ground	50	49.76	Top Layer	50.00mil	50.00mil	12.00mil	Bottom Layer	Board Layer Stack
2	Coated Coplanar Waveguide With Ground	50	49.76	Bottom Layer	50.00mil	50.00mil	12.00mil	Top Layer	Board Layer Stack

Bill Of Materials

Line #	Designator	Comment	Quantity
1	C1, C4, C5, C8, C10, C11, C13, C15	100nF	8
2	C2, C3, C6, C7, C9, C12, C14, C16	10µF	8
3	P1, P2	Pin Header 2x4	2
4	P3	Pin List 1x3	1
5	P4, P5	SMA	2
6	R1, R2, R3, R4	0.8R	4
7	R5, R8	1k	2
8	R6, R7	10k	2
9	R9, R10	150R	2
10	R11, R14	560R	2
11	R12, R13	47R	2
12	R15, R17, R18, R19	680R	4
13	R16	33R	1
14	U1, U2, U3	THS3001ID	3
15	U4	THS4631D	1

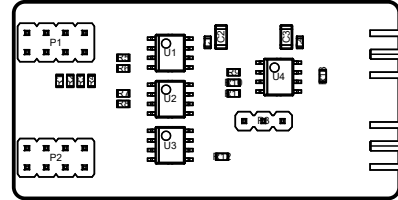
Drill Drawing View (Scale 2:1)



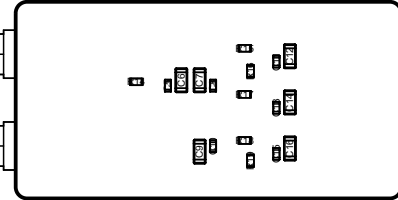
Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	55	0.30mm	Plated	
△	121	0.50mm	Plated	
□	19	0.90mm	Plated	
195 Total				

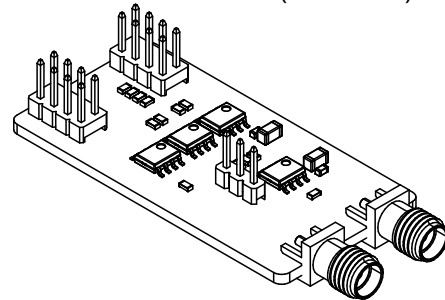
View from Top side (Scale 1:1)



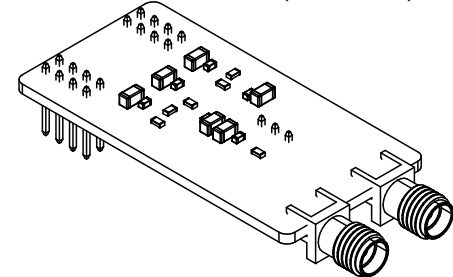
View from Bottom side (Scale 1:1)



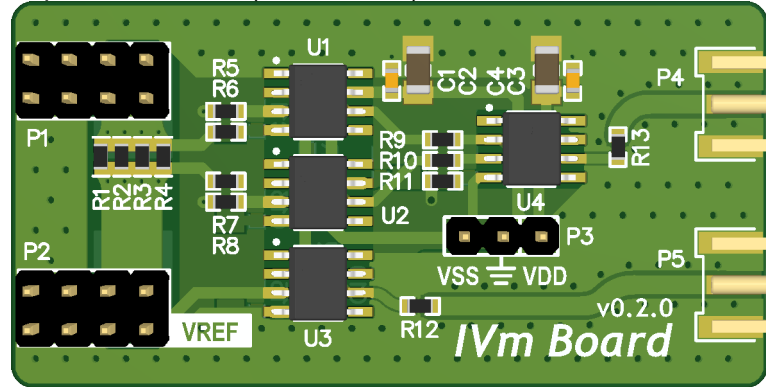
View from Front side (Scale 1:1)



View from Back side (Scale 1:1)



Top Realistic View (Not to Scale)



Bottom Realistic View (Not to Scale)

