

Designed by
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The schematic diagram illustrates a 10-bit DAC circuit. It features a 10-bit digital input bus labeled **DIN** at the top, which branches into two 5-bit buses: **DIN[4:0]** and **DIN[9:5]**. The **DIN[4:0]** bus is connected to a network of resistors **R1** through **R10** and capacitors **C1** through **C5**. The **DIN[9:5]** bus is connected to a network of resistors **R11** through **R19** and capacitors **C6** through **C10**. The circuit is powered by a **VREF** source and a **VIN** source. The output of the DAC is labeled **VOUT**. A battery symbol is also present in the lower right corner of the diagram.

