

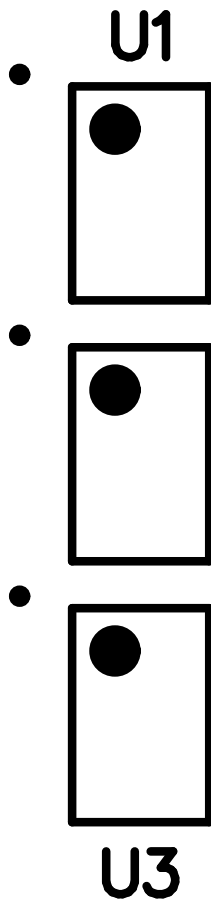
P1

R1
R2
R3
R4

P2



VREF



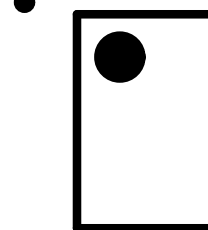
U1

U3



R9
R10
R11

U2



U4



VSS \equiv VDD

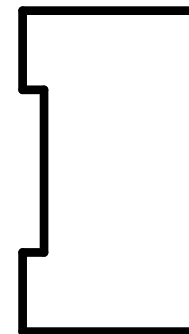


R12

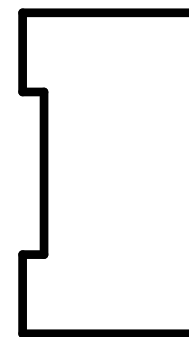


R13

P4



P5



v0.2.0

IVm Board

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