

1

2

3

4

A

A

# Master Project: "IVm Board"

B


B

C

C

D

D

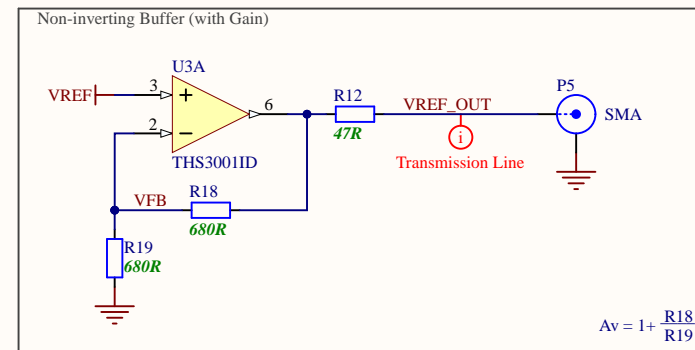
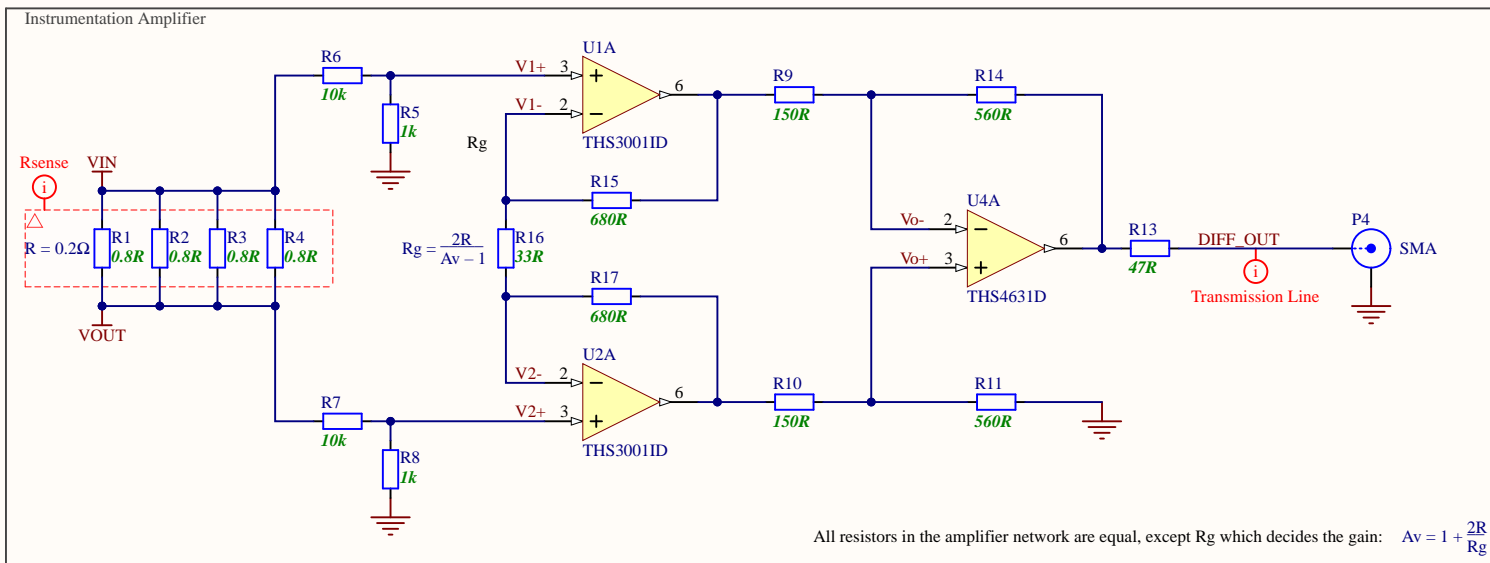
Title: <i>Cover and Information</i>			
Version: 0.2.0	Status: <a href="#">Prototype</a>	Sheet 1 of 3	
Drawn by: <a href="#">Eirik Hovde Skanke</a>		Size: A4	
Date: <a href="#">26.01.2021</a>		File: <a href="#">sheet1_cover.SchDoc</a>	

1

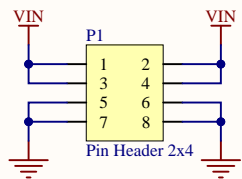
2

3

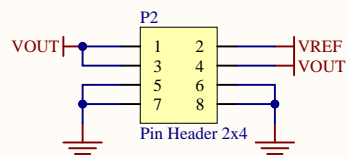
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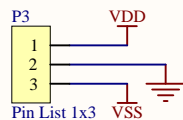
Input Voltage Pin Header



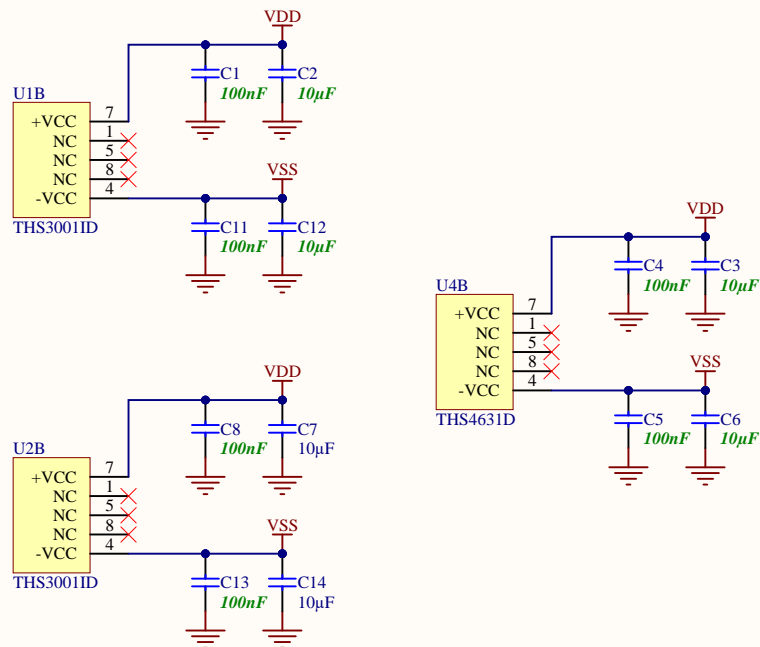
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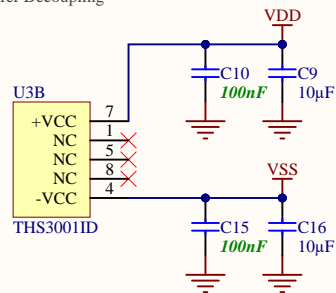
Power Rail Pin List



Instrumentation Amplifier Decoupling



Buffer Decoupling

Title: **Power**

Version: 0.2.0

Status: Prototype

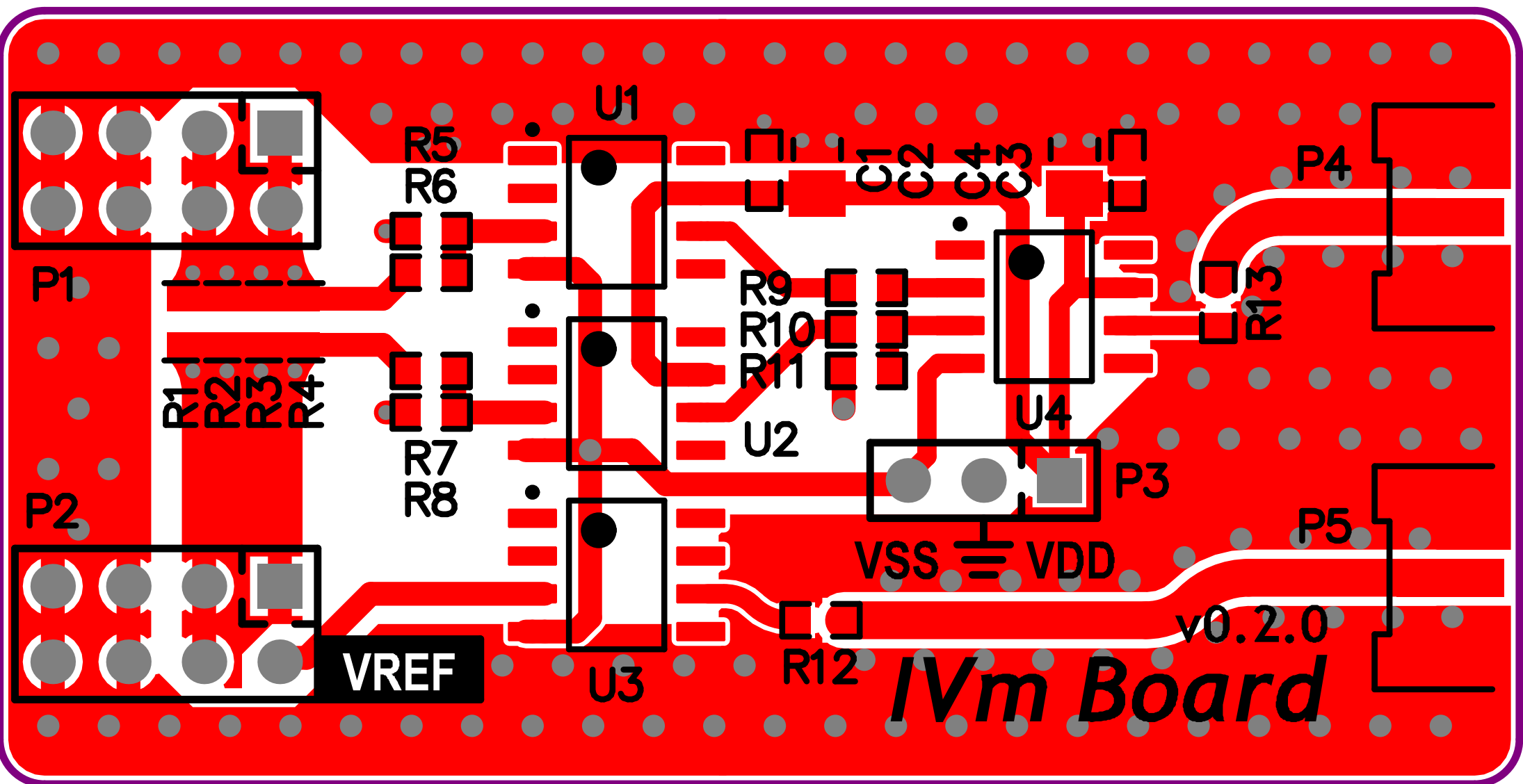
Sheet 3 of 3

Drawn by: Eirik Hovde Skanke

Size: A4

Date: 26.01.2021

File: sheet3\_power.SchDoc



*IVm Board*

v0.2.0

Designed by  
Eirik Hovde Skauke

