Sequence Domain SISO Equivalent Models of a Grid-Tied Voltage Source Converter System for Small-Signal Stability Analysis

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Abstract—This paper presents a generalized method for converting multi-input and multi-output (MIMO) dq impedance model of a grid-tied voltage source converter system into its sequence domain single-input and single-output (SISO) equivalents. As a result, two types of SISO impedance models were derived, one of which was derived from relatively strong and dq symmetric grid assumption (reduced SISO model) and the other was based on closed-loop equivalent (accurate SISO model). It was proven that the accurate SISO model has the same marginal stability condition as the MIMO model. Accuracy of these models is assessed with respect to the measured impedances in PSCAD/EMTDC simulations, their effects on stability are analyzed as well. Findings show that the accurate SISO model presents identical stability conclusions as the MIMO model. However, the reduced SISO model may lead to inaccurate results if the system is highly dq asymmetric, e.g., VSC with fast phase-locked loop or an actively controlled grid.

Index Terms—Couplings, PLL, sequence impedance, stability analysis, voltage source converter.

I. INTRODUCTION

NOWADAYS, voltage source converters (VSC) have become widely used in grid-integrated renewable energies [1] and flexible power transmission systems [2]. Oscillations at both low [3] and high frequencies [4] were observed in VSC-based systems, particularly in weak grid conditions [5]. Such types of small-signal stability issues can be effectively assessed by impedance-based analysis. Impedance models of three-phase VSCs [6]–[9], single-phase VSCs [10], and modular multi-level converters [11], among others, have been developed rigorously in recent literature.

For typical two-level and three-phase grid-tied VSCs, the impedance can be extracted either in dq synchronously rotating frame [7] or in three-phase stationary frame [8]. In dq frame, the grid-tied VSC system is time invariant if grid is three-phase balanced. This setup allows for direct linearization; thus, performing Laplace transformation on the resultant linear time invariant (LTI) model yields dq impedances [9]. However, if applied to three-phase stationary frame, the grid-tied VSC inherently varies by time. Therefore, the harmonic linearization method from a previous study [12] is applied to obtain sequence impedances [8]. Generally, linearizing the time-varying system along a steady periodic trajectory yields a linear time periodic (LTP) system. To transform LTP systems into frequency domain, the harmonic balance approach [13] can be adopted.

Despite the different models in dq and sequence domains, both are coupled because of the off-diagonal terms in impedance matrices are nonzero. Recent research has presented interest in the interpretation of these couplings and their consequences during stability assessment. Previous works [14], [15] established that frequency couplings can be identified in their sequence domain (i.e., positive and negative sequences are coupled and separated by twice fundamental frequency); and their impacts on low-frequency stability were also emphasized. This interesting property of VSC was also identified from dq impedance and introduced as dq asymmetry in [16]. Moreover, the relationship between dq and sequence impedances were thoroughly investigated in [17], and findings showed that dq impedances can be transformed into its modified sequence domain equivalents by means of symmetrical decomposition [18]. On the other hand, a complex space vector method [16] is used to directly derive VSC impedance in stationary frame [19].

However, frequency couplings in the foregoing reviews (e.g., [14]–[17]) were in single-frequency coupling form (i.e., single-frequency perturbation induces a single-frequency coupling that separated by twice the fundamental frequency). This condition is true if either the converter or the grid impedance is dq asymmetric [16] or equivalently contains the mirror frequency coupling effect [17]. If the system is three-phase unbalanced, there will be multiple frequency couplings. To include these couplings with full accuracy, the harmonic-state space [20] as well as the harmonic transfer function method [13] should be adopted.

Currently, both cases on single- and multiple-frequency couplings can only be captured by matrix-based impedances, which are multi-input and multi-output (MIMO) systems by nature; therefore, the generalized Nyquist criterion (GNC) [21] should be adopted for stability analysis. Furthermore, finding the
single-input and single-output (SISO) equivalents of grid-tied VSC system is appealing due to their simplicity and convenience for physical interpretation.

This paper aims to develop a generalized method for converting MIMO dq impedance into its sequence domain SISO equivalents by exploring the properties of single-frequency coupling system. The rest of the paper is organized as follows: In Section II, the method for converting the dq impedance into its MIMO sequence domain equivalents is introduced. System blocks of a grid-tied VSC system are modeled based on this method. In Section III, sequence domain MIMO model of grid-tied VSC system is established by assembling the blocks in Section II. And its SISO equivalents are found by performing closed-loop analysis of the entire system, instead of viewing them as subsystems. A detailed comparison of SISO models with measured impedances in PSCAD/EMTDC is presented. Section IV discussed the performance of proposed SISO models in predicting small signal stability. Finally, Section V draws the conclusions.

II. MODELING OF GRID-TIED VSC IN MODIFIED SEQUENCE DOMAIN

A. Topology and Control Scheme of the Grid-Tied VSC

Fig. 1 presents the system analyzed in this paper. It constitutes a typical two-level VSC, an L-type filter, and a Thévenin-equivalent grid.

Only current controller and phase-locked loop (PLL) are considered, mainly to achieve simplicity of subsequent property analysis. It will not affect the generality of proposed method as will be presented later. Grid voltage feedforwards have a great impacts on both transient [6] and small-signal response [5] of VSC, if the bandwidths of these feedforwards are not carefully chosen. In this regard, feedforwards are viewed as impedance-shaping method, and will not be discussed in this paper because the focus is on modeling.

B. Symmetrical Decomposition of a dq Impedance

Taking a dq impedance model in [9] as an example,

\[
\begin{bmatrix}
U_d(s) \\
U_q(s)
\end{bmatrix} =
\begin{bmatrix}
Z_{dd}(s) & Z_{dq}(s) \\
Z_{qd}(s) & Z_{qq}(s)
\end{bmatrix}
\begin{bmatrix}
I_d(s) \\
I_q(s)
\end{bmatrix}
\]

Expression (1) is a LTI system and a complex exponential input (e.g., \(e^{st}\)) leads to an output with the same formation [13]. Thus, the variables for \(dq\) currents and voltages in (1) can be written explicitly with variable \(s\), as shown below.

\[
\begin{bmatrix}
U_d \\
U_q
\end{bmatrix} e^{st} =
\begin{bmatrix}
Z_{dd}(s) & Z_{dq}(s) \\
Z_{qd}(s) & Z_{qq}(s)
\end{bmatrix}
\begin{bmatrix}
I_d \\
I_q
\end{bmatrix} e^{st}, \forall s \rightarrow j\omega \quad (2)
\]

where \(s \rightarrow j\omega\) is translated from \(s\)-domain to frequency-domain. \(I_d, I_q\) and \(U_d, U_q\) are the current and voltage phasors at frequency \(\omega\) respectively, and they can be decomposed as:

\[
\begin{bmatrix}
U_p \\
U_n
\end{bmatrix} =
\begin{bmatrix}
1 & 1 \\
1 & -1
\end{bmatrix}
\begin{bmatrix}
U_d \\
U_q
\end{bmatrix} = A \begin{bmatrix}
U_d \\
U_q
\end{bmatrix}
\]

(3)

Applying matrix \(A\) and its inverse \(A^{-1}\) to (2) yields:

\[
\begin{bmatrix}
U_p \\
U_n
\end{bmatrix} = A \begin{bmatrix}
Z_{dd}(s) \\
Z_{dq}(s)
\end{bmatrix} Z_{pp}(s) A^{-1} \begin{bmatrix}
I_p \\
I_n
\end{bmatrix}
\]

\[
= Z_{pp}(s) \begin{bmatrix}
I_p \\
I_n
\end{bmatrix}, \forall s \rightarrow j\omega \quad (4)
\]

where elements in \(Z_{pp}(s) = \begin{bmatrix}
Z_{dd}^{\text{est}}(s) & Z_{dq}^{\text{est}}(s) \\
Z_{qd}^{\text{est}}(s) & Z_{qq}^{\text{est}}(s)
\end{bmatrix}\) are generally complex transfer functions. This method makes it possible to obtain the modified sequence impedance directly from well-developed dq impedance as discussed in [17] (i.e., the same authors of this paper). The term “modified” denotes the specific frequency notion used in [17], where the frequency of sequence impedances is referred to dq frame. This notation is adopted in the present paper as well, and the term “modified” will be omitted for brevity in subsequent analysis. However, other recent works e.g., [14] and [19] use a different frequency notation, which are referred to phase domain.

C. Sequence Domain System Blocks of Grid-Tied VSC

Adopting the decomposition method in Section II-B, system blocks of a grid-tied VSC system in \(dq\) format (e.g., [9]) can be transformed into their sequence domain equivalents.

For passive circuit elements, e.g., filter:

\[
\begin{bmatrix}
R_l + sL_l & -\omega_sL_l \\
\omega_sL_l & R_l + sL_l
\end{bmatrix} \rightarrow \begin{bmatrix}
Z_{lp}(s) \\
Z_{ln}(s)
\end{bmatrix} = \frac{Z_{lp}^{\text{est}}(s)}{Z_{pp}^{\text{est}}(s)} \begin{bmatrix}
0 \\
0
\end{bmatrix}
\]

(5)

where \(Z_{lp}^{\text{est}}(s) = R_l + sL_l + j\omega_sL_l, Z_{ln}^{\text{est}}(s) = Z_{lp}^{\text{est}}(s).\) The upper line on the latter denotes complex-conjugate operator on the function (i.e., the coefficients not the Laplace variable “\(s\”).

For a typical Thévenin grid, its sequence impedances are similarly as the filter, which are \(Z_{pp}^{\text{est}}(s) = R_s + sL_s + j\omega_sL_s\) and \(Z_{pp}^{\text{est}}(s) = Z_{lp}^{\text{est}}(s)\) respectively.

For variables perturbed by abc to \(dq\) transformation e.g., converter output currents:

\[
\begin{bmatrix}
0 \\
0
\end{bmatrix} \rightarrow \begin{bmatrix}
L_0 \quad -L_0 \\
L_0 \quad -L_0
\end{bmatrix} = \frac{T_{pl}(s)}{U_0}\]

\[
\begin{bmatrix}
L_0 \quad -L_0 \\
L_0 \quad -L_0
\end{bmatrix}
\]

(6)

where \(T_{pl}(s) = \frac{U_p}{s+U_0H_{pl}(s)}\) is the closed-loop system of a typical PLL as in Fig. 1. \(U_0\) is the voltage at PLL sampling point.
The converter output voltage can be obtained similarly as:
\[
\frac{U_{o}(s)}{I_{o}(s)} = Y_{pp}(s) - \frac{T_{pl}(s)}{L_{0}}, \quad U_{o} = U_{c0} + jU_{q0}
\]

The complex-valued current in steady.

For current controller it has:
\[
\begin{bmatrix}
H_{c}(s) & 0 \\
0 & H_{c}(s)
\end{bmatrix}
\begin{bmatrix}
dq-pn
\end{bmatrix}
\rightarrow
\begin{bmatrix}
H_{pp}(s) & 0 \\
0 & H_{nn}(s)
\end{bmatrix}
\begin{bmatrix}
dq-pn
\end{bmatrix}
\]

Generally, all the system blocks in \( dq \) domain can be transformed into their sequence domain equivalents, e.g., VSC with PQ controller, DC voltage controller etc.

### D. Sequence Impedance Model of the Grid-Tied VSC System

The sequence domain MIMO model of a grid-tied VSC system can be established by assembling system blocks derived in Section II-C.

For a load (VSC) subsystem, its admittance is:
\[
\begin{bmatrix}
i_{L}^p \\
i_{L}^{\bar{p}}
\end{bmatrix} =
\begin{bmatrix}
Y_{pp}^p(s) & Y_{pn}^p(s) \\
Y_{np}^p(s) & Y_{nn}^p(s)
\end{bmatrix}
\begin{bmatrix}
u_{L}^p \\
u_{L}^{\bar{p}}
\end{bmatrix}
\]

For a generalized source (grid) subsystem, its impedance is:
\[
\begin{bmatrix}
u_{S}^p \\
u_{S}^{\bar{p}}
\end{bmatrix} =
\begin{bmatrix}
Z_{pp}^p(s) & Z_{pn}^p(s) \\
Z_{np}^p(s) & Z_{nn}^p(s)
\end{bmatrix}
\begin{bmatrix}
i_{S}^p \\
i_{S}^{\bar{p}}
\end{bmatrix}
\]

where
\[
Y_{pp} = \frac{1 - G_{pl}}{H_{c} + Z_{c}}, \quad Y_{pn} = Y_{pp}, \quad Y_{np} = -G_{pl}, \quad Y_{nn} = \frac{G_{pl}}{H_{c} + Z_{c}}
\]

Note that the line on the letter e.g., \( Y_{pp}^p \) is conjugate operator on the function, if the full complex conjugate operator \( \overline{\cdot} \) is used, it has \( (Y_{pp}^p)^{\star} = Y_{pp}(s) \). The derived MIMO model as (8) and (9) can be used directly to assess small-signal stability with the help of GNC [14]. A previous study [17] proved that the GNC based on this model leads to identical results, as the GNC based on \( dq \) impedance.

The sequence equivalent circuits can be plotted as Fig. 2 on the basis of (8)–(11). In Fig. 2, positive and negative sequence circuits are coupled via two dependent current sources, which are voltage controlled. This intrinsic binding between positive and negative sequence circuits will be explored further in next section for finding their SISO equivalents.

### III. SISO Equivalent Models of a Grid-Tied VSC System

#### A. Analysis of Coupled Sequence Loops

In order to reveal the sequence coupling in a more intuitive way, manipulating the system blocks (5)–(7) with electrical system configuration in Fig. 1 yields the following diagram.

Fig. 3 clearly identifies the positive and negative sequence loops coupled via six paths, which are all caused by the PLL (i.e., \( T_{pl}(s) \)). Different paths will result in models with different accuracies, as in the following cases:

**Case 1:** By neglecting all paths, the simplest model with decoupled positive and negative sequences is obtained. Although this case may not be effective for stability analysis, it is useful for identifying the intrinsic properties of the grid-VSC system (e.g., resonant point), and the coupling effects of PLL can be introduced as additional damping sources to the intrinsic resonant point [22].

**Case 2:** By isolating the paths of \( 1 \, 2 \, 3 \, 6 \), an other popular decoupled sequence model as in [8] is obtained. The positive and negative loop impedance from perturbation voltage to the current response can be calculated directly from Fig. 3; i.e., \( 1/Y_{pp}^p + Z_{pp}^p \) and \( 1/Y_{nn}^p + Z_{nn}^p \). Note that the obtained loop impedance is equivalent to neglect the off-diagonal terms in the converter admittance. This condition is satisfied if the grid is relatively strong and \( dq \) symmetric. See the proof in the subsequent analysis as in (18), (19).

The foregoing analysis presents two decoupled models for the grid-tied VSC, which are SISO systems. However, both models neglect sequence coupling to some extent. In the following section, we will develop a method for deriving an accurate SISO model with no assumptions and reductions.
B. Accurate and Reduced SISO Models of the Grid-Tied VSC

In this subsection, we regard VSC and grid as a closed-loop system, not as subsystems, perturbed by independent sources. Due to linearity, closed-loop analysis under positive and negative independent perturbations can be analyzed separately.

Taking the positive sequence as an example, the positive sequence loop impedance can be obtained by solving the linear system in Fig. 4:

\[
Z_{\text{Loop}}^p(s) = \frac{u_{\text{ptb}}^p}{i_{\text{ptb}}^p} = \frac{1}{C(Z_{11}^p(s) + Z_{22}^p(s))^{-1}B}
\]  (12)

where \( Z_{11}^p(s) = (Y_{L}^{PN}(s))^{-1} \). It should be noted that the derived loop impedance is one dimension, i.e., a SISO system.

Substituting elements as in (8) and (9) into (12) yields:

\[
Z_{\text{Loop}}^p(s) = Z_{SS}^p + Z_{PP}^p - \frac{Z_{PN}^p + Z_{NP}^p}{Z_{nn}^p + Z_{pp}^p}
\]  (13)

This method is applied to find the negative sequence loop impedance. Replacing the matrix \( B = [0 \ 1]^T \), \( C = [0 \ 1] \) and \( u_{\text{ptb}}^p \rightarrow u_{\text{ptb}}^n \) yields:

\[
Z_{\text{Loop}}^n(s) = Z_{SS}^n + Z_{PP}^n - \frac{Z_{PN}^n + Z_{NP}^n}{Z_{nn}^p + Z_{pp}^p}
\]  (14)

Expressions (13) and (14) is defined as the \textit{accurate SISO model}, and \( Z_{\text{Loop}}^p(s) \) (or \( Z_{\text{Loop}}^n(s) \)) still holds, i.e., if we have the analytical model of the positive sequence, the negative sequence model is determined accordingly. In addition, during the derivation, no assumption for \( dq \) symmetry was made, therefore this method is general for any LTI systems.

The physical interpretation of this method is: the negative sequence circuit in Fig. 2 is augmented into the positive sequence network (and vice versa) via the voltage-controlled dependent current source. Consequently, the effects of sequence coupling are included in this model intrinsically.

In order to proof the validity of the method, a previous work in [17], where the sequence impedance is derived for source and load subsystem is compared. Taking the positive sequence model for example, in [17] it has:

\[
Z_L^p = -\frac{u^p_L}{i^p_L} = Z_L^{pp} - \frac{Z_{21}^p(Z_{22}^p + Z_{pp}^p)}{Z_{22}^p + Z_{nn}^p}
\]  (15)

\[
Z_S^p = \frac{u^p_S}{i^p_S} = Z_S^{pp} - \frac{Z_{12}^p(Z_{11}^p + Z_{pp}^p)}{Z_{11}^p + Z_{nn}^p}
\]  (16)

\[
Z_L^n(s) = \frac{Z_L^n}{s}
\]

\[
Z_S^n(s) = \frac{Z_S^n}{s}
\]  (17)

We can clearly observe that \( Z_L^n + Z_S^n = Z_{\text{Loop}}^p \) and \( Z_L^n + Z_S^n = Z_{\text{Loop}}^n \). (15) and (16) are equivalent to (33) in [17], but are written in a more compact form with slightly different notation.

Furthermore, if considering a \( dq \) symmetric and relatively strong grid, it has conditions as: \( Z_{pp}^p = Z_{pp}^n = 0 \), \( |Z_{pp}^p| \ll |Z_{pp}^n| \), \( \forall \omega \) and \( |Z_{nn}^n| \ll |Z_{nn}^p| \), \( \forall \omega \). Hence, (13) and (14) can be reduced to:

\[
Z_{\text{Loop}}^{p,n}(s) = Z_{pp}^p + \frac{\det[ Z_{PP}^{p,n} ]}{Z_{nn}^p + Z_{pp}^p} = Z_{pp}^n + \frac{\det[ Z_{PP}^{p,n} ]}{Z_{nn}^p + Z_{pp}^p}
\]  (18)

\[
Z_{\text{Loop}}^{p,n}(s) = Z_{pp}^p + \frac{\det[ Z_{PP}^{p,n} ]}{Z_{nn}^p + Z_{pp}^p} = Z_{pp}^n + \frac{\det[ Z_{PP}^{p,n} ]}{Z_{nn}^p + Z_{pp}^p}
\]  (19)

Expressions (18) and (19) is defined as the \textit{reduced SISO model}, which is widely applied in previous research [8]. However, a frequency translation to phase domain is needed since this paper uses a \( dq \) frequency notation.

C. Proof of Identical Marginal Stability Condition

This subsection will prove that the accurate SISO model is consistent with the MIMO model in terms of marginal stability condition. The marginal stability condition is defined as the case when the eigenvalue loci of a MIMO or SISO system cross the \((-1, 0)\) point on the basis of GNC or NC.

For MIMO-based model, the marginal stability condition is:

There is \( s \) that \( \text{eig} ( Z_{11}^{PN} \cdot Y_{11}^{LN} ) \) equals \(-1 + 0 \cdot j\) (20)

where \( Z_{11}^{PN}, Y_{11}^{LN} \) are given in (8) and (9). After some calculations, we have the equality as:

\[
det Z_{11}^{PN} + \det Z_{11}^{LN} + Z_{12}^{pp}Z_{12}^{nn} + Z_{11}^{pp}Z_{11}^{nn} - Z_{12}^{pp}Z_{12}^{nn} = 0
\]  (21)

For SISO-based model, the marginal stability condition is:

There is \( s \) that \( \text{eig} ( Z_{11}^{LP} / \bar{Z}_{11}^{LP} ) \) equals \(-1 + 0 \cdot j\) (22)

where \( Z_{11}^{LP}, \bar{Z}_{11}^{LP} \) are given in (15) and (16).

(22) is equivalent to \( \text{det} \left( \frac{Z_{11}^{LP}}{\bar{Z}_{11}^{LP}} \right) = 0 \rightarrow Z_{\text{Loop}}^{p} = 0 \), thus the equality given by (13) is:

\[
( Z_{11}^{PP} + Z_{11}^{nn} ) ( Z_{12}^{PP} + Z_{12}^{nn} ) - ( Z_{11}^{pp} + Z_{11}^{np} ) ( Z_{12}^{pp} + Z_{12}^{np} ) = 0
\]  (23)

Expanding (23), then substitute \( \text{det} Z_{11}^{PN} = Z_{12}^{pp}Z_{12}^{nn} - Z_{12}^{pp}Z_{12}^{nn} \) and \( \text{det} Z_{11}^{LN} = Z_{12}^{pp}Z_{12}^{nn} - Z_{12}^{pp}Z_{12}^{nn} \) into (23) can prove that (21) equals (23), i.e., the accurate SISO model has the same marginal stability condition as the MIMO model. Therefore it can be used with accuracy in stability analysis. Furthermore, any modifications on SISO model will lead to a marginal stability condition different from (23) e.g., the reduced SISO model. Note that the same proof applies to the negative sequence.

D. Comparison of SISO Models with Measurements

The accurate SISO model as in (13) and (14), and the reduced SISO model as in (18) and (19), will be compared under conditions of a) \( dq \) symmetric and b) \( dq \) asymmetric grid.

Impedance measurements conducted in PSCAD/EMTDC with the system in Fig. 1 (see Appendix A for detailed
Fig. 5. Loop impedance comparison under dq symmetric grid. (a) SCR = 4, CC = 200 Hz, PLL = 5 Hz, current is 0.5 p.u. (flow out). (b) SCR = 4, CC = 200 Hz, PLL = 200 Hz, current is 0.5 p.u. (flow out).

Fig. 6. Control scheme for asymmetric grid emulation.

system parameters. The multi-run module in PSCAD is used. At each run, a single-tone harmonic voltage is injected into the grid. The frequency is varied from 0 Hz to 100 Hz with an increment of 2 Hz. The sampling frequency and sampling window used for Fourier analysis are 1 kHz and 0.5 s respectively. All data and figures are post-processed in MATLAB.

1) dq Symmetric Grid Cases: As shown in Fig. 5(a), both accurate and reduced SISO models achieved a good match with the measured impedances under a slow PLL configuration. However, if PLL bandwidth is increased to a relatively large value, the shapes of the reduced model would differ from the measurements, particularly for the negative sequence impedances, as shown in Fig. 5(b). By contrast, the accurate SISO model tracks the measured impedances accurately in Fig. 5(b). It proves that the accurate SISO model is superior to the reduced SISO model in capturing the details of impedance characteristics.

2) dq Asymmetric Grid Cases: In this paragraph, an actively controlled grid is introduced to emulate the asymmetric behavior in source subsystem.

The control scheme is shown as below:

In Fig. 6, $\omega_s = 2\pi \cdot 50$ is constant, $U_{ref}(s)$ is the voltage amplitude set point of the active grid. $H_v(s) = k_v p + k_i s$ is the voltage regulator. The sequence impedance of the actively controlled grid is asymmetric and can be found in Appendix. B ((A.1) and (A.2)).

Comparing Fig. 7(a) with Fig. 6(a) we can identify that, the good accuracy of reduced SISO model under symmetric grid as
well as slow PLL configuration is violated if $d_q$ asymmetric grid is presented. The inaccuracy of reduced SISO model can be identified clearly in Fig. 7(b) as well, where a fast PLL is adopted. On the contrary, the accurate SISO model still presents good accuracy in all conditions.

IV. SMALL-SIGNAL STABILITY ANALYSIS

This section will further analyze the validity of the proposed SISO models in terms of small-signal stability, particularly for the marginal stability condition in Section III-C. By acquiring the advantages of SISO properties, the proposed model can be used in combination with classic Nyquist criterion (NC) [23].

A. Numerical Stability Analysis

Three model and criterion combinations are considered:

1) Reduced SISO with NC. (For comparison)

2) Accurate SISO with NC. (For comparison)

3) MIMO model with GNC. (For Reference).

In a, the eigenvalue loci is obtained straightforward as

$$
\lambda_P(s) = Z_{pp}^S \cdot Y_{pp}^L \quad \text{and} \quad \lambda_N(s) = Z_{nn}^S \cdot Y_{pp}^L
$$

in accordance with (18) and (19).

In b, since the SISO loop impedance in (13) can be decomposed into source and load subsystems as (15) and (16). Therefore, the eigenvalue loci of minor loop gains are

$$
\lambda_P(s) = Z_{pp}^S \quad \text{and} \quad \lambda_N(s) = Z_{nn}^S
$$

where $Z_{pp}^S$, $Z_{pp}^L$, $Z_{nn}^S$, $Z_{nn}^L$ are given by (15)–(17).

In c, the eigenvalue loci can be calculated from

$$
\det(\lambda \cdot I - Z_{pp}^S Y_{pp}^{PS}(s)) = 0
$$

where $\lambda_1(s)$, $\lambda_2(s)$ are the two solutions. The abovementioned eigenvalue loci are complex transfer functions; thus, the locus for negative frequencies is not the conjugation of the locus of positive frequencies [16]. However, the eigenvalue loci of SISO systems have the property $(\lambda_N(j\omega))^* = \lambda_N(-j\omega) = \lambda_P(-j\omega)$. Hence, the negative frequency plots can be obtained by conjugating the negative sequence locus.

Fig. 8 illustrates the stability comparisons of three model and criterion combinations under a $d_q$ asymmetric grid condition. By varying PLL bandwidth in three steps from slow to fast, the system is stable, marginal stable and unstable respectively. The accurate SISO model with NC has the same stability conclusion as the MIMO model with GNC. Particularly, the eigenvalue loci of accurate SISO model and MIMO model cross the $(-1, 0)$ point simultaneously, indicating that the proof of marginal stability condition in Section III-C is correct. On the other hand, the reduced SISO model fails to give the correct marginal stability condition, as well as the stability conclusion, identified in Figs. 8(b) and (c) respectively.

Therefore, it is not safe to use the reduced SISO model if the converter and grid is highly $d_q$ asymmetric. On the contrary, the accurate SISO model is effective for stability analysis in this respect.

The marginal stability can also be analyzed physically by finding the damping characteristic at resonances of loop impedance, e.g., by passivity analysis in [24]. The following time domain study will provide more physical insights into the oscillatory behavior lies in the grid-tied VSC system.

B. Simulation Study

The physical interpretation of marginally stable condition is that the loop impedance has approximately zero damping at a resonance frequency. By plotting the real and imaginary parts
of loop impedance, the resonances can be found at frequencies where the imaginary part cross zero axis, meanwhile damping at these resonances can be determined according to the sign of real parts.

As shown in Fig. 9(a), the positive sequence loop impedance has a resonance at 10 Hz, while the negative sequence loop impedance has a resonance at 60 Hz, this findings is consistent with the analytical calculation of resonant points in [22]. Furthermore, the damping at 10 Hz resonance is negative with small value, indicating a marginally unstable condition, on the contrary a positive damping characteristic is presented at 60 Hz, indicating a stable resonance. It is again emphasized that the resonance frequencies are referred to dq frame in the above analysis.

Time domain simulations in PSCAD/EMTDC also draw similar conclusions in terms of stability. The VSC output currents gradually become unstable during a long simulation time in Fig. 9(b), this is due to the fact that negative damping at 10 Hz is small.

Furthermore, by performing a Fourier analysis on the phase current, we can identify that two additional frequencies except the fundamental at 40 Hz and 60 Hz appears, the mirror frequency coupling effect is originated from oscillations in dq frame at 10 Hz, which again proves the correctness of above analysis. Additionally, the oscillatory behavior shown in Fig. 9(b) is also similar to the field measurements of grid-tied photovoltaic inverter systems in [25].

V. Conclusion

This paper developed a generalized method for converting dq impedance model of grid-tied VSC system into its SISO sequence domain equivalents. The converting process includes two steps: firstly converts dq impedance into its MIMO sequence domain equivalent, then converts the MIMO sequence domain equivalent into its SISO equivalent by means of closed-loop analysis method proposed in this paper. The decoupled SISO model allows the classic Nyquist Criterion to be used for stability analysis.

Two types of SISO model were given, the accurate one is directly from the consequence of conversion, and the reduced one is derived with a strong grid condition approximation. Numerical and time domain analysis shown that the reduced SISO model gives the wrong stability conclusions in cases where the system is highly dq asymmetric. On the contrary, the accurate SISO model presents a good consistence with MIMO model in terms of stability conclusions, particularly for the marginally stable condition.

The proposed method is general for any MIMO LTI systems. Therefore it is applicable to grid-tied VSC systems where a power controller or DC voltage controller is adopted. Only the marginal stability condition is proven to be identical in this work. Performance on gain and phase margin should be carefully evaluated in future works.

APPENDIX

A. Circuit Parameters Used in Stability Analysis and Simulations

| TABLE A1 | CIRCUIT PARAMETERS OF THE GRID-TIED VSC SYSTEM |
| NAME       | VALUES       | NAME       | VALUES       |
| Nominal rating | 2 MVA       | Filter inductance | 0.1 p.u. |
| Nominal voltage | 0.69 kV    | Grid inductance | 1/SCR = 0.25 p.u. |
| DC voltage  | 1.1 kV       | Current controller | k_i = 0.03, k_i = 6.1 |
| (SCR = 4)   |              | (CC = 200 Hz) | (SCR = 4) |
| Switching frequency | 2.4 kHz   | PLL controller | k_p^{RL} = 71, k_i^{RL} = 1421 |
|              |              | (PLL = 20 Hz) | k_p = 1, k_i = 100 |
\[ Z_{\text{grid}}^{dq}(s) = \begin{bmatrix} 1 + \cos \delta_H v(s) & 0 \\ -\sin \delta_H v(s) & 1 \end{bmatrix} \begin{bmatrix} sL_s + R_s & -\omega_L L_s \\ \omega_L L_s & sL_s + R_s \end{bmatrix} \]

(A.1)

where \( \delta_H \) is the steady state voltage angle difference between PCC and grid. Clearly, the \( dq \) impedance of actively controlled grid is not symmetric. Using the decomposition method in Section II-B gives a coupled sequence impedance:

\[ Z_{\text{grid}}^{PN}(s) = AZ_{\text{grid}}^{dq}(s)A^{-1} \quad \text{(A.2)} \]

### B. Modeling of Actively Controlled Grid

The \( dq \) domain grid model with control scheme in Fig. 6 is:

\[ Z_{\text{grid}}^{dq}(s) = \begin{bmatrix} 1 + \cos \delta_H v(s) & 0 \\ -\sin \delta_H v(s) & 1 \end{bmatrix} \begin{bmatrix} sL_s + R_s & -\omega_L L_s \\ \omega_L L_s & sL_s + R_s \end{bmatrix} \]

(A.1)

where \( \delta_H \) is the steady state voltage angle difference between PCC and grid. Clearly, the \( dq \) impedance of actively controlled grid is not symmetric. Using the decomposition method in Section II-B gives a coupled sequence impedance:

\[ Z_{\text{grid}}^{PN}(s) = AZ_{\text{grid}}^{dq}(s)A^{-1} \quad \text{(A.2)} \]

### C. \( dq \) Symmetric and Asymmetric

For a \( dq \) impedance matrix \( Z_{\text{grid}}^{dq}(s) \), it is said to be \( dq \) symmetric if \( Z_{\text{grid}}^{dq}(s) = Z_{\text{grid}}^{dq}(s) \) and \( Z_{\text{grid}}^{dq}(s) = -Z_{\text{grid}}^{dq}(s) \), and if the condition not satisfied, the system is referred to \( dq \) asymmetric. For a \( dq \) symmetric system, its sequence equivalent can be obtained by linear transformation using the methods in Section II. As a result, the sequence impedance is decoupled. Otherwise, the sequence impedance is coupled.

### References


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Q2. Author: Please update Ref. [25].
Sequence Domain SISO Equivalent Models of a Grid-Tied Voltage Source Converter System for Small-Signal Stability Analysis

Chen Zhang, Xu Cai, Atle Rygg, and Marta Molinas, Member, IEEE

Abstract—This paper presents a generalized method for converting multi-input and multi-output (MIMO) $dq$ impedance model of a grid-tied voltage source converter system into its sequence domain single-input and single-output (SISO) equivalents. As a result, two types of SISO impedance models were derived, one of which was derived from relatively strong and $dq$ symmetric grid assumption (reduced SISO model) and the other was based on closed-loop equivalent (accurate SISO model). It was proven that the accurate SISO model has the same marginal stability condition as the MIMO model. Accuracy of these models is assessed with respect to the measured impedances in PSCAD/EMTDC simulations, their effects on stability are analyzed as well. Findings show that the accurate SISO model presents identical stability conclusions as the MIMO model. However, the reduced SISO model may lead to inaccurate results if the system is highly $dq$ asymmetric, e.g., VSC with fast phase-locked loop or an actively controlled grid.

Index Terms—Couplings, PLL, sequence impedance, stability analysis, voltage source converter.

I. INTRODUCTION

Nowadays, voltage source converters (VSC) have become widely used in grid-integrated renewable energies [1] and flexible power transmission systems [2]. Oscillations at both low [3] and high frequencies [4] were observed in VSC-based systems, particularly in weak grid conditions [5]. Such types of small-signal stability issues can be effectively assessed by impedance-based analysis. Impedance models of three-phase VSCs [6]–[9], single-phase VSCs [10], and modular multi-level converters [11], among others, have been developed rigorously in recent literature.

For typical two-level and three-phase grid-tied VSCs, the impedance can be extracted either in $dq$ synchronously rotating frame [7] or in three-phase stationary frame [8]. In $dq$ frame, the grid-tied VSC system is time invariant if grid is three-phase balanced. This setup allows for direct linearization; thus, performing Laplace transformation on the resultant linear time invariant (LTI) model yields $dq$ impedances [9]. However, if applied to three-phase stationary frame, the grid-tied VSC inherently varies by time. Therefore, the harmonic linearization method from a previous study [12] is applied to obtain sequence impedances [8]. Generally, linearizing the time-varying system along a steady periodic trajectory yields a linear time periodic (LTP) system. To transform LTP systems into frequency domain, the harmonic balance approach [13] can be adopted.

Despite the different models in $dq$ and sequence domains, both are coupled because of the off-diagonal terms in impedance matrices are nonzero. Recent research has presented interest in the interpretation of these couplings and their consequences during stability assessment. Previous works [14], [15] established that frequency couplings can be identified in their sequence domain (i.e., positive and negative sequences are coupled and separated by twice fundamental frequency); and their impacts on low-frequency stability were also emphasized. This interesting property of VSC was also identified from $dq$ impedance and introduced as $dq$ asymmetry in [16]. Moreover, the relationship between $dq$ and sequence impedances were thoroughly investigated in [17], and findings showed that $dq$ impedances can be transformed into its modified sequence domain equivalents by means of symmetrical decomposition [18]. On the other hand, a complex space vector method [16] is used to directly derive VSC impedance in stationary frame [19].

However, frequency couplings in the foregoing reviews (e.g., [14]–[17]) were in single-frequency coupling form (i.e., a single-frequency perturbation induces a single-frequency coupling that separated by twice the fundamental frequency). This condition is true if either the converter or the grid impedance is $dq$ asymmetric [16] or equivalently contains the mirror frequency coupling effect [17]. If the system is three-phase unbalanced, there will be multiple frequency couplings. To include these couplings with full accuracy, the harmonic-state space [20] as well as the harmonic transfer function method [13] should be adopted.

Currently, both cases on single- and multiple-frequency couplings can only be captured by matrix-based impedances, which are multi-input and multi-output (MIMO) systems by nature; therefore, the generalized Nyquist criterion (GNC) [21] should be adopted for stability analysis. Furthermore, finding the
single-input and single-output (SISO) equivalents of grid-tied VSC system is appealing due to their simplicity and convenience for physical interpretation.

This paper aims to develop a generalized method for converting MIMO dq impedance into its sequence domain SISO equivalents by exploring the properties of single-frequency coupling system. The rest of the paper is organized as follows: In Section II, the method for converting the dq impedance into its MIMO sequence domain equivalents is introduced. System blocks of a grid-tied VSC system are modeled based on this method. In Section III, sequence domain MIMO model of grid-tied VSC system is established by assembling the blocks in Section II. And its SISO equivalents are found by performing closed-loop analysis of the entire system, instead of viewing them as subsystems. A detailed comparison of SISO models with measured impedances in PSCAD/EMTDC is presented. Section IV discussed the performance of proposed SISO models in predicting small signal stability. Finally, Section V draws the conclusions.

II. MODELING OF GRID-TIED VSC IN MODIFIED SEQUENCE DOMAIN

A. Topology and Control Scheme of the Grid-Tied VSC

Fig. 1 presents the system analyzed in this paper. It constitutes a typical two-level VSC, an L-type filter, and a Thevenin-equivalent grid.

Only current controller and phase-locked loop (PLL) are considered, mainly to achieve simplicity of subsequent property analysis. It will not affect the generality of proposed method as will be presented later. Grid voltage feedforwards can have a great impacts on both transient [6] and small-signal response [5] of VSC, if the bandwidths of these feedforwards are not carefully chosen. In this regard, feedforwards are viewed as impedance-shaping method, and will not be discussed in this paper because the focus is on modeling.

B. Symmetrical Decomposition of a dq Impedance

Taking a dq impedance model in [9] as an example,

\[
\begin{bmatrix}
U_d(s) \\
U_q(s)
\end{bmatrix} = \begin{bmatrix}
Z^{rd}(s) & Z^{dq}(s) \\
Z^{qd}(s) & Z^{qq}(s)
\end{bmatrix}
\begin{bmatrix}
I_d(s) \\
I_q(s)
\end{bmatrix}
\]

Expression (1) is a LTI system and a complex exponential input (e.g., \(e^{st}\)) leads to an output with the same formation [13]. Thus, the variables for dq currents and voltages in (1) can be written explicitly with variable \(s\), as shown below.

\[
\begin{bmatrix}
U_d \\
U_q
\end{bmatrix} e^{st} = \begin{bmatrix}
Z^{rd}(s) & Z^{dq}(s) \\
Z^{qd}(s) & Z^{qq}(s)
\end{bmatrix}
\begin{bmatrix}
I_d \\
I_q
\end{bmatrix} e^{st}, \forall s \rightarrow j\omega
\]

where \(s \rightarrow j\omega\) is translated from \(s\)-domain to frequency-domain. \(I_d, I_q\) and \(U_d, U_q\) are the current and voltage phasors at frequency \(\omega\) respectively, and they can be decomposed as:

\[
\begin{bmatrix}
U_p \\
U_n
\end{bmatrix} = \begin{bmatrix}
1 & j \\
1 & -j
\end{bmatrix}
\begin{bmatrix}
I_d \\
I_q
\end{bmatrix} = A
\begin{bmatrix}
I_d \\
I_q
\end{bmatrix}
\]

Applying matrix \(A\) and its inverse \(A^{-1}\) to (2) yields:

\[
\begin{bmatrix}
U_p \\
U_n
\end{bmatrix} = A
\begin{bmatrix}
Z^{pd}(s) \\
Z^{qd}(s)
\end{bmatrix} \begin{bmatrix}
I_p \\
I_n
\end{bmatrix} = Z^{PN}(s)
\begin{bmatrix}
I_p \\
I_n
\end{bmatrix}, \forall s \rightarrow j\omega
\]

where elements in \(Z^{PN}(s) = \frac{Z^{pd}(s)}{Z^{pd}(s)}, \frac{Z^{qd}(s)}{Z^{qd}(s)}\) are generally complex transfer functions. This method makes it possible to obtain the modified sequence impedance directly from well-developed dq impedance as discussed in [17] (i.e., the same authors of this paper). The term “modified” denotes the specific frequency notation used in [17], where the frequency of sequence impedances is referred to dq frame. This notation is adopted in the present paper as well, and the term “modified” will be omitted for brevity in subsequent analysis. However, other recent works e.g., [14] and [19] use a different frequency notation, which are referred to phase domain.

C. Sequence Domain System Blocks of Grid-Tied VSC

Adopting the decomposition method in Section II-B, system blocks of a grid-tied VSC system in dq format (e.g., [9]) can be transformed into their sequence domain equivalents.

For passive circuit elements, e.g., filter,

\[
\begin{bmatrix}
R_l + sL_f & -\omega_sL_f \\
\omega_sL_f & R_l + sL_f
\end{bmatrix}
\begin{bmatrix}
Z^{lp}(s) \\
Z^{ln}(s)
\end{bmatrix}
\Rightarrow
\begin{bmatrix}
0 \\
0
\end{bmatrix}
= Z^{pn}(s)
\]

where \(Z^{lp}(s) = R_l + sL_f + j\omega_sL_f, Z^{ln}(s) = Z^{pn}(s)\). The upper line on the latter denotes complex-conjugate operator on the function (i.e., the coefficients not the Laplace variable “s”). For a typical Thevenin grid, its sequence impedances are similarly as the filter, which are \(Z^{pn}(s) = R_s + sL_s + j\omega_sL_s\) and \(Z^{pn}(s) = Z^{pn}(s)\) respectively.

For variables perturbed by abc to dq transformation e.g.,

\[
\begin{bmatrix}
0 \\
0
\end{bmatrix}
= \frac{T_{abc}(s)}{s + \frac{1}{sU_0}}
\begin{bmatrix}
\frac{L_0}{U_0} \\
-L_0
\end{bmatrix}
\]

where \(T_{abc}(s) = \frac{U_0H_{abc}(s)}{s + U_0H_{abc}(s)}\) is the closed-loop system of a typical PLL as in Fig. 1, \(U_0\) is the voltage at PLL sampling point.
The converter output voltage can be obtained similarly as:

\[ \bar{U}_{c0} = U_{e0} \]

where \( U_{e0} \) is the complex-valued terminal voltage.

For current controller it has:

\[
\begin{bmatrix}
H_e(s) & 0 \\
0 & H_e(s)
\end{bmatrix}
\begin{bmatrix}
d_{pq-mn} \n
\end{bmatrix}
\rightarrow
\begin{bmatrix}
H_{pp}^e(s) & 0 \\
0 & H_{pp}^e(s)
\end{bmatrix}
\]

(7)

Generally, all the system blocks in \( dq \) domain can be transformed into their sequence domain equivalents, e.g., VSC with PQ controller, DC voltage controller etc.

### D. Sequence Impedance Model of the Grid-Tied VSC System

The sequence domain MIMO model of a grid-tied VSC system can be established by assembling system blocks derived in Section II-C.

For a load (VSC) subsystem, its admittance is:

\[
\begin{bmatrix}
\bar{I}_L^P \\
\bar{I}_L^N
\end{bmatrix} = \begin{bmatrix}
Y_{pp}^L(s) & Y_{pn}^L(s) \\
Y_{np}^L(s) & Y_{nn}^L(s)
\end{bmatrix} \begin{bmatrix}
\bar{U}_L^P \\
\bar{U}_L^N
\end{bmatrix}
\]

(8)

For a generalized source (grid) subsystem, its impedance is:

\[
\begin{bmatrix}
\bar{I}_S^P \\
\bar{I}_S^N
\end{bmatrix} = \begin{bmatrix}
Z_{pp}^S(s) & Z_{pn}^S(s) \\
Z_{np}^S(s) & Z_{nn}^S(s)
\end{bmatrix} \begin{bmatrix}
\bar{U}_S^P \\
\bar{U}_S^N
\end{bmatrix}
\]

(9)

\[
\bar{I}_S^P = \bar{I}_S^N, \quad \bar{U}_S^P = \bar{U}_S^N
\]

(10)

\[
\bar{U}_S^P + \bar{U}_{ptb} = \bar{U}_L^P, \quad \bar{U}_S^N = \bar{U}_L^N
\]

(11)

where \( Y_{pp}^L = \frac{1-G_{lli}}{H_s^L+Z_c^L} \), \( Y_{nn}^L = \frac{1+G_{lli}}{H_s^L+Z_c^L} \), \( Y_{pp}^S = \frac{1+G_{lli}}{H_s^S+Z_c^S} \), \( Y_{nn}^S = \frac{1-G_{lli}}{H_s^S+Z_c^S} \).

\[ Y_{pp}^L, Y_{nn}^L, Y_{pp}^S, Y_{nn}^S \]

are voltage to the current response can be calculated directly from the system configuration in Fig. 1 yields the following diagram.

Fig. 3 clearly identifies the positive and negative sequence loops coupled via six paths, which are all caused by the PLL (i.e., \( T_{pll}(s) \)). Different paths will result in models with different accuracies, as in the following cases:

**Case 1:**

By neglecting all paths, the simplest model with decoupled positive and negative sequences is obtained. Although this case may not be effective for stability analysis, it is useful for identifying the intrinsic properties of the grid-VSC system (e.g., resonant point), and the coupling effects of PLL can be introduced as additional damping sources to the intrinsic resonant point [22].

**Case 2:**

By isolating the paths of \( 1 \) \( 3 \) \( 5 \) \( 7 \) \( 9 \) \( 11 \), another popular decoupled sequence model as in [8] is obtained. The positive and negative loop impedance from perturbation voltage to the current response can be calculated directly from Fig. 3; i.e., \( 1/Y_{pp}^L + Z_{pp}^p \) and \( 1/Y_{nn}^L + Z_{nn}^n \). Note that the obtained loop impedance is equivalent to neglect the off-diagonal terms in the converter admittance. This condition is satisfied if the grid is relatively strong and \( dq \) symmetric. See the proof in the subsequent analysis as in (18), (19).

The foregoing analysis presents two decoupled models for the grid-tied VSC, which are SISO systems. However, both models neglect sequence coupling to some extent. In the following section, we will develop a method for deriving an accurate SISO model with no assumptions and reductions.

---

**This is an excerpt from a technical document discussing the analysis of grid-tied VSC systems. It focuses on sequence domain modeling, including impedance and admittance calculations, and the derivation of SISO models. The text uses mathematical expressions and diagrams to explain the system behavior under different conditions.**
B. Accurate and Reduced SISO Models of the Grid-Tied VSC

In this subsection, we regard VSC and grid as a closed-loop system, not as subsystems, perturbed by independent sources. Due to linearity, closed-loop analysis under positive and negative independent perturbations can be analyzed separately. Taking the positive sequence as an example, the positive sequence loop impedance can be obtained by solving the linear system in Fig. 4:

\[ Z_{\text{Loop}}^P(s) = -\frac{u_{\text{pib}}^P}{i_{\text{L}}} = \frac{1}{C(Z_{4n}^N(s) + Z_{4p}^N(s))^{-1}B} \]  

where \( Z_{4n}^N(s) = (Y_{4n}^P(s))^{-1} \). It should be noted that the derived loop impedance is one dimension, i.e., a SISO system.

Substituting elements in (8) and (9) into (12) yields:

\[ Z_{\text{Loop}}^P(s) = Z_{S}^P + Z_{L}^P - \left(\frac{Z_{np}^P + Z_{sp}^P}{Z_{nn}^P + Z_{np}^P}\right)Z_{nn}^P \]  

This method is applied to find the negative sequence loop impedance. Replacing the matrix \( B = [0, 1]^T, C = [0, 1] \) and \( u_{\text{pib}}^P \rightarrow u_{\text{pib}}^N \) yields:

\[ Z_{\text{Loop}}^N(s) = Z_{S}^N + Z_{L}^N - \left(\frac{Z_{np}^N + Z_{sp}^N}{Z_{nn}^N + Z_{np}^N}\right)Z_{nn}^N \]  

Expressions (13) and (14) is defined as the accurate SISO model, and \( Z_{\text{Loop}}^P(s) = Z_{\text{Loop}}^N(s) \) still holds, i.e., if we have the analytical model of the positive sequence, the negative sequence model is determined accordingly. In addition, during the derivation, no assumption for \( d\bar{q} \) symmetry was made, therefore this method is general for any LTI systems.

The physical interpretation of this method is: the negative sequence circuit in Fig. 2 is augmented into the positive sequence network (and vice versa) via the voltage-controlled dependent current source. Consequently, the effects of sequence coupling are included in this model intrinsically.

In order to proof the validity of the method, a previous work in [17], where the sequence impedance is derived for source and load subsystem is compared. Taking the positive sequence model for example, in [17] it has:

\[ Z_{P}^L = -\frac{u_{pib}^P}{i_{L}^P} = Z_{L}^P - \frac{Z_{L}^P(Z_{2p}^P + Z_{np}^P)}{Z_{nn}^P + Z_{np}^P} \]  

\[ Z_{S}^P = \frac{u_{pib}^N}{i_{L}^N} = Z_{S}^N - \frac{Z_{S}^N(Z_{2p}^N + Z_{np}^N)}{Z_{nn}^N + Z_{np}^N} \]  

\[ Z_{P}^L (s) = Z_{L}^P (s) \]  

\[ Z_{S}^N (s) = Z_{S}^P (s) \]  

We can clearly observe that \( Z_{L}^P + Z_{S}^P = Z_{L}^P \) and \( Z_{L}^N + Z_{S}^N = Z_{L}^N \). (15) and (16) are equivalent to (33) in [17], but are written in a more compact form with slightly different notation.

Furthermore, if considering a \( d\bar{q} \) symmetric and relatively strong grid, it has conditions as: \( Z_{S}^P = Z_{S}^N = 0, |Z_{L}^P| \ll |Z_{L}^N|, \forall \omega \) and \( |Z_{L}^P| \ll |Z_{L}^N|, \forall \omega \). Hence, (13) and (14) can be reduced to:

\[ Z_{\text{Loop}}^P(s) = Z_{S}^P + \frac{\det [Z_{L}^P]}{Z_{nn}^P + Z_{np}^P} = Z_{S}^P + \frac{1}{Y_{nn}^P} \]  

\[ Z_{\text{Loop}}^N(s) = Z_{S}^N + \frac{\det [Z_{L}^N]}{Z_{nn}^N + Z_{np}^N} = Z_{S}^N + \frac{1}{Y_{nn}^N} \]

Expressions (18) and (19) is defined as the reduced SISO model, which is widely applied in previous research [8]. However, a frequency translation to phase domain is needed since this paper uses a \( d\bar{q} \) frequency notation.

C. Proof of Identical Marginal Stability Condition

This subsection will prove that the accurate SISO model is consistent with the MIMO model in terms of marginal stability condition. The marginal stability condition is defined as the case when the eigenvalue loci of a MIMO or SISO system cross the \((-1, 0)\) point on the basis of GNC or NC.

For MIMO-based model, the marginal stability condition is:

There is \( s \) that \( \text{eig} \left( Z_{S}^N \cdot Y_{L}^N \right) = -1 + 0 \cdot j \) \( (20) \)

where \( Z_{S}^N, Y_{L}^N \) are given in (8) and (9). After some calculations, we have the equality as:

\[ \det Z_{S}^N + \det Z_{L}^N + Z_{S}^N Z_{L}^N + Z_{S}^N Z_{L}^N Z_{S}^N Z_{L}^N = 0 \]  

(21)

For SISO-based model, the marginal stability condition is:

There is \( s \) that \( \text{eig} \left( Z_{L}^P / Z_{L}^N \right) = -1 + 0 \cdot j \) \( (22) \)

where \( Z_{L}^P, Z_{L}^N \) are given in (15) and (16).

(22) is equivalent to \( \det \left( \frac{Z_{L}^P}{Z_{L}^N} \right) = 0 \rightarrow Z_{L}^N \text{Loop} = 0 \), thus the equality given by (13) is:

\[ (Z_{S}^N Z_{L}^N + Z_{L}^N) (Z_{S}^P Z_{L}^P + Z_{L}^P) - (Z_{S}^N Z_{L}^N + Z_{S}^N) (Z_{L}^P Z_{L}^P) = 0 \]  

(23)

Expanding (23), then substitute \( \det Z_{S}^N = Z_{S}^N Z_{S}^N Z_{S}^N Z_{S}^N \) and \( Z_{L}^N = Z_{L}^N Z_{L}^N Z_{L}^N Z_{L}^N \) into (23) can prove that (21) equals (23), i.e., the accurate SISO model has the same marginal stability condition as the MIMO model. Therefore it can be used with accuracy in stability analysis. Furthermore, any modifications on SISO model will lead to a marginal stability condition different from (23) e.g., the reduced SISO model. Note that the same proof applies to the negative sequence.

D. Comparison of SISO Models with Measurements

The accurate SISO model as in (13) and (14), and the reduced SISO model as in (18) and (19), will be compared under conditions of a) \( d\bar{q} \) symmetric and b) \( d\bar{q} \) asymmetric grid.

Impedance measurements conducted in PSCAD/EMTDC with the system in Fig. 1 (see Appendix A for detailed
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Fig. 5. Loop impedance comparison under $dq$ symmetric grid. (a) SCR = 4, CC = 200 Hz, PLL = 5 Hz, current is 0.5 p.u. (flow out). (b) SCR = 4, CC = 200 Hz, PLL = 200 Hz, current is 0.5 p.u. (flow out).

Fig. 6. Control scheme for asymmetric grid emulation.

Fig. 7. Loop impedance comparison under $dq$ asymmetric grid. (a) SCR = 4, CC = 200 Hz, PLL = 5 Hz, current is 0.5 p.u. (flow out) (Note that SCR here is only used for calculating grid passive impedance). (b) SCR = 4, CC = 200 Hz, PLL = 300 Hz, current is 0.5 p.u. (flow out) (Note that SCR here is only used for calculating grid impedance).

The multi-run module in PSCAD is used. At each run, a single-tone harmonic voltage is injected into the grid. The frequency is varied from 0 Hz to 100 Hz with an increment of 2 Hz. The sampling frequency and sampling window used for Fourier analysis are 1 kHz and 0.5 s respectively. All data and figures are post-processed in MATLAB.

1) $dq$ Symmetric Grid Cases: As shown in Fig. 5(a), both accurate and reduced SISO models achieved a good match with the measured impedances under a slow PLL configuration. However, if PLL bandwidth is increased to a relatively large value, the shapes of the reduced model would differ from the measurements, particularly for the negative sequence impedances, as shown in Fig. 5(b). By contrast, the accurate SISO model tracks the measured impedances accurately in Fig. 5(b). It proves that the accurate SISO model is superior to the reduced SISO model in capturing the details of impedance characteristics.

2) $dq$ Asymmetric Grid Cases: In this paragraph, an actively controlled grid is introduced to emulate the asymmetric behavior in source subsystem.

The control scheme is shown as below:

In Fig. 6, $\omega_s = 2\pi \cdot 50$ is constant, $U_{ref}^s$ is the voltage amplitude set point of the active grid. $H_v(s) = k_v^p + \frac{k_v}{s}$ is the voltage regulator. The sequence impedance of the actively controlled grid is asymmetric and can be found in Appendix B ((A.1) and (A.2)).

Comparing Fig. 7(a) with Fig. 6(a) we can identify that, the good accuracy of reduced SISO model under symmetric grid as...
well as slow PLL configuration is violated if \(dq\) asymmetric grid is presented. The inaccuracy of reduced SISO model can be identified clearly in Fig. 7(b) as well, where a fast PLL is adopted. On the contrary, the accurate SISO model still presents good accuracy in all conditions.

IV. SMALL-SIGNAL STABILITY ANALYSIS

This section will further analyze the validity of the proposed SISO models in terms of small-signal stability, particularly for the marginal stability condition in Section III-C. By acquiring the advantages of SISO properties, the proposed model can be used in combination with classic Nyquist criterion (NC) [23].

A. Numerical Stability Analysis

Three model and criterion combinations are considered:

1) Reduced SISO with NC. (For comparison)

2) Accurate SISO with NC. (For comparison)

3) MIMO model with GNC. (For Reference).

In a, the eigenvalue loci is obtained straightforward as

\[
\lambda_P(s) = \frac{Z_{pp}^S}{Z_{pp}^L}, \quad \lambda_N(s) = \frac{Y_{pp}^S}{Y_{pp}^L}
\]

in accordance with (18) and (19).

In b, since the SISO loop impedance in (13) can be decomposed into source and load subsystems as (15) and (16).

Therefore, the eigenvalue loci of minor loop gains are

\[
\lambda_P(s) = \frac{Z_{pp}^S}{Z_L^p}, \quad \lambda_N(s) = \frac{Z_{pp}^S}{Z_L^p}
\]

where \(Z_{pp}^S, Z_L^p, Z_{pp}^S, Z_{pp}^L\) are given by (15)–(17).

In c, the eigenvalue loci can be calculated from

\[
\det(\lambda \cdot I - Z_S^{PS}Y_{L}^{PS}(s)) = 0
\]

where \(\lambda_1(s), \lambda_2(s)\) are the two solutions. The abovementioned eigenvalue loci are complex transfer functions; thus, the locus for negative frequencies is not the conjugation of the locus of positive frequencies [16]. However, the eigenvalue loci of SISO systems have the property 

\[
(\lambda_N(j\omega))^* = \bar{\lambda}_N(-j\omega) = \lambda_P(-j\omega)
\]

Hence, the negative frequency plots can be obtained by conjugating the negative sequence locus.

Fig. 8 illustrates the stability comparisons of three model and criterion combinations under a \(dq\) asymmetric grid condition. By varying PLL bandwidth in three steps from slow to fast, the system is stable, marginal stable and unstable respectively. The accurate SISO model with NC has the same stability conclusion as the MIMO model with GNC. Particularly, the eigenvalue loci of accurate SISO model and MIMO model cross the \((-1, 0j)\) point simultaneously, indicating that the proof of marginal stability condition in Section III-C is correct. On the other hand, the reduced SISO model fails to give the correct marginal stability condition, as well as the stability conclusion, identified in Figs. 8(b) and (c) respectively.

Therefore, it is not safe to use the reduced SISO model if the converter and grid is highly \(dq\) asymmetric. On the contrary, the accurate SISO model is effective for stability analysis in this respect.

The marginal stability can also be analyzed physically by finding the damping characteristic at resonances of loop impedance, e.g., by passivity analysis in [24]. The following time domain study will provide more physical insights into the oscillatory behavior lies in the grid-tied VSC system.

B. Simulation Study

The physical interpretation of marginally stable condition is that the loop impedance has approximately zero damping at a resonance frequency. By plotting the real and imaginary parts
of loop impedance, the resonances can be found at frequencies
where the imaginary part cross zero axis, meanwhile damping
at these resonances can be determined according to the sign of
real parts.

As shown in Fig. 9(a), the positive sequence loop impedance
has a resonance at 10 Hz, while the negative sequence loop
impedance has a resonance at 60 Hz, this findings is consist-
tent with the analytical calculation of resonant points in [22].
Furthermore, the damping at 10 Hz resonance is negative with
small value, indicating a marginally unstable condition, on the
contrary a positive damping characteristic is presented at 60 Hz,
indicating a stable resonance. It is again emphasized that the
resonance frequencies are referred to dq frame in the above
analysis.

Time domain simulations in PSCAD/EMTDC also draw sim-
ilar conclusions in terms of stability. The VSC output currents
gradually become unstable during a long simulation time in
Fig. 9(b), this is due to the fact that negative damping at 10 Hz
is small.

Furthermore, by performing a Fourier analysis on the phase
current, we can identify that two additional frequencies ex-
cept the fundamental at 40 Hz and 60 Hz appears, the mir-
ror frequency coupling effect is originated from oscillations in
dq frame at 10 Hz, which again proves the correctness of
above analysis. Additionally, the oscillatory behavior shown in
Fig. 9(b) is also similar to the field measurements of grid-tied
photovoltaic inverter systems in [25].

V. CONCLUSION

This paper developed a generalized method for converting
dq impedance model of grid-tied VSC system into its SISO
sequence domain equivalents. The converting process includes
two steps: firstly converts dq impedance into its MIMO se-
quence domain equivalent, then converts the MIMO sequence
domain equivalent into its SISO equivalent by means of closed-
loop analysis method proposed in this paper. The decoupled
SISO model allows the classic Nyquist Criterion to be used for
stability analysis.

Two types of SISO model were given, the accurate one is
directly from the consequence of conversion, and the reduced
one is derived with a strong grid condition approximation. Nu-
merical and time domain analysis shown that the reduced SISO
model gives the wrong stability conclusions in cases where the
system is highly dq asymmetric. On the contrary, the accurate
SISO model presents a good consistence with MIMO model in
terms of stability conclusions, particularly for the marginally
stable condition.

The proposed method is general for any MIMO LTI systems.
Therefore it is applicable to grid-tied VSC systems where a
power controller or DC voltage controller is adopted. Only the
marginal stability condition is proven to be identical in this
work. Performance on gain and phase margin should be carefully
evaluated in future works.

APPENDIX

A. Circuit Parameters Used in Stability Analysis and
Simulations

<table>
<thead>
<tr>
<th>TABLE A1</th>
<th>CIRCUIT PARAMETERS OF THE GRID-TIED VSC SYSTEM</th>
</tr>
</thead>
<tbody>
<tr>
<td>NAME</td>
<td>VALUES</td>
</tr>
<tr>
<td>Nominal rating</td>
<td>2 MVA</td>
</tr>
<tr>
<td>Nominal voltage</td>
<td>0.69 kV</td>
</tr>
<tr>
<td>Dc voltage</td>
<td>1.1 kV</td>
</tr>
<tr>
<td>(SCR = 4)</td>
<td>(CC = 200 Hz)</td>
</tr>
<tr>
<td>Switching frequency</td>
<td>2.4 kHz</td>
</tr>
<tr>
<td>asymmetric grid controller</td>
<td></td>
</tr>
</tbody>
</table>
B. Modeling of Actively Controlled Grid

The dq domain grid model with control scheme in Fig. 6 is:

\[
Z_{\text{grid}}^{\text{dq}}(s) = \begin{bmatrix}
1 + \cos \delta_0 H_v(s) & 0 \\
-\sin \delta_0 H_v(s) & 1
\end{bmatrix}
\begin{bmatrix}
s L_n + R_n & -\omega_s L_n \\
\omega_s L_n & s L_n + R_n
\end{bmatrix}
\]

where \(\delta_0\) is the steady voltage angle difference between PCC and grid. Clearly, the dq impedance of actively controlled grid is not symmetric. Using the decomposition method in Section II-B gives a coupled sequence impedance:

\[
Z_{\text{grid}}^{\text{pN}}(s) = A Z_{\text{grid}}^{\text{dq}}(s) A^{-1}
\]

C. dq Symmetric and Asymmetric

For a dq impedance matrix \([Z_{\text{dq}}^x(s) \ Z_{\text{dq}}^y(s)]\), it is said to be dq symmetric if \(Z_{\text{dq}}^{x\text{t}}(s) = Z_{\text{dq}}^{y\text{t}}(s)\) and \(Z_{\text{dq}}^{y\text{t}}(s) = -Z_{\text{dq}}^{x\text{t}}(s)\), and if the condition not satisfied, the system is referred to dq asymmetric. For a dq symmetric system, its sequence equivalent can be obtained by linear transformation using the methods in Section II. As a result, the sequence impedance is decoupled. Otherwise, the sequence impedance is coupled.

REFERENCES


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Dear Editor,

Thank you for your review.

Regarding Q1, the reference is updated as bellow:


Regarding Q2, the reference is updated as bellow:


Besides, I add two comments in the context, please refer to line 191 and line 303 respectively.

If anything is inappropriate please let me know, thank you!

Sincerely,

Chen Zhang