Optimizing HVDC Grid Expansion and Control for Enhancing DC Stability

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Abstract—This paper analyzes the problem of the expansion of an offshore HVDC network. We assess which new HVDC link deployment ensures the highest stabilization of the DC voltage under transient conditions, in the worst-case scenario and for a given grid topology. A linear feedback controller is also designed to guarantee the minimization of the DC grid voltage deviations, while ensuring input constraints. The operation of this controller is compared to that of a standard droop regulator commonly applied in multiterminal Voltage Source Converter (VSC)-based HVDC networks.

Index terms—HVDC, expansion planning, linear matrix inequalities, transient stability

I. INTRODUCTION

The increasing growth in the number of installed offshore wind farms and the need to convey consistent amounts of intermittent wind power to load centers onshore have led to the deployment of the first point-to-point HVDC link in the North Sea [1]. The installed offshore wind capacity is expected to rise from 15.8 GW in 2017 to 70 GW by 2030 in Europe [2]. This anticipates the development of a multi-terminal, and eventually meshed, HVDC grid in the same area, which will emerge from the gradual interconnection of pre-existing and independently designed HVDC links. The design, control and operation of such a system will bring unprecedented challenges and opportunities.

The goal of this paper is to offer a framework based on optimal control to enhance DC voltage stability in grid expansion decisions.

Considering the technical and financial implications of deploying highly-interconnected offshore grids, the expansion planning stage is of paramount importance. However, many commonly adopted expansion planning approaches are based on techno-economical optimization and do not take stability and performance of the grid dynamics into sufficient consideration [3]. HVDC links however contribute significantly to the latter aspects and are often used to solve grid problems such as congestions and mitigation of power imbalances [4]. Their contribution with this respect should be properly accounted for when planning for a grid expansion [5]. Luca Furieri

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Because of the very high cost for operation and maintenance, ensuring stability of the grid is the primary concern, especially in the offshore environment. In particular, when operating multi-terminal or meshed HVDC systems a main focus is on the DC voltage stability, which is sensitive to power imbalances due to varied wind power injection, fault conditions on the connected AC terminals and so on. [6]. The DC voltage stability can be jeopardized by resonances triggered under specific modes of operation [7]. Hence, it is necessary to ensure that the DC voltage is always kept within the allowed range at all terminals and under all scenarios of operation.

This paper provides a method to assess the stabilizing effect of the installation of a new HVDC link on the surrounding DC grid during the planning phase. Our contributions are as follows. First, for a given offshore grid topology, we assess which additional HVDC link should be installed to minimize the DC voltage oscillations, assuming the worst-case scenario for the perturbations. Second, we compute the optimal linear controller achieving such minimization. Third, we include a comparison between the proposed optimal linear controller and the droop regulator, with the goal of gaining more insight into the structure of droop regulators and the features of optimal linear controller.

Although applied to a 4-terminal test grid, the proposed optimization approach is scalable to HVDC networks of higher topological complexity in a straightforward way.

This paper is structured as follows: in Section II we present the selected test-case. In Section III we introduce the mathematical formulation of the problem using Linear Matrix Inequality theory. In Section IV we show the results obtained in terms of HVDC voltage stabilization for four different configurations and their corresponding multi-objective evaluation, and we provide a sensitivity analysis in Section V. In Section VI we present the linear feedback controller ensuring highest DC voltage stabilization and its comparison with standard droop solutions.

II. CONSIDERED TEST CASE

For the sake of simplicity, the approach used in this paper is exemplified using a test case based on a 4-terminal HVDC grid



Fig. 1 4-terminal HVDC test grid [8]: existing links (solid lines) and possible expansions considered (dashed lines)

configuration [8], which is shown in Fig. 1. The system parameters correspond to those of [8] and are reported in Table I. We assume the presence of two independently operated HVDC links connecting two offshore wind farms with their respective onshore substations. The links are represented in Figure 1 by the cables between converters 1-2 and 3-4 respectively. Assuming that we are given the possibility to extend the HVDC system with one additional link, we aim to assess how the different options, i.e. the candidate new lines 1-3, 1-4, 2-3 and 2-4, would affect the resulting dynamics of the system. In order to decide on the most suitable expansion, we will exploit both the above evaluation/criterion and the length of the additional link. The HVDC converters considered in this analysis are three-phase 2-level Voltage Source Converters (2L-VSC), whose basic structure is shown in Fig. 2. The VSC converter models are developed in the synchronous reference frame [9] and the entire system is represented in per-unit (see Appendix).

We select a specific operating point, which we consider to be the most representative for the system. The chosen operating condition corresponds to specific current references for the different terminals, as indicated in Table II. The proposed analysis is based on a linearized model of each grid configuration around such equilibrium point. This equilibrium point is assumed to be the same for all the grid configurations. Thus, the system can be represented through the state-space equations (1) - (2):

 TABLE I.
 CONVERTER AND GRID PARAMETERS OF THE SELECTED 4-TERMINAL HVDC TEST GRID [8]

	Converter 1, 2, 3	Converter 4				
Rated power [MVA]	900	1200				
AC grid voltage [kV]	400	400				
AC grid reactance $[\Omega]$	17.7	13.4				
AC grid resistance [Ω]	1.77	1.34				
	Cables 1-2, 1-3, 1-4,	2-3, 2-4, 3-4				
DC cable capacitance [F/km]	0.16e-6					
DC cable inductance [H/km]	0.19e-3					
DC cable resistance $[\Omega/km]$	0.0141					

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Fig. 2 Simplified model of the considered 2L-VSC

$$\Delta \dot{x}(t) = A \Delta x(t) + B \Delta u(t) \tag{1}$$

$$\Delta z(t) = C \Delta x(t). \tag{2}$$

The state vector, $\Delta x(t)$, includes the variation of the state variables corresponding to all the converter stations (i.e. *d* and *q* components of the AC-side current and the DC voltage) and the current in the DC cables linking them.

We also assume that each converter model includes a PI current controller developed in the same dq synchronous reference frame as the converter model (Figure 2). This controller, which represents the inner loop of a cascaded converter control approach, is here considered as part of the linearized system, implying the presence of two additional state variables (i.e. the controller current integral states on the dq axes, g_d and g_q) per converter terminal (n = 1...4). The state vector corresponding to the configuration with link 1-4 is reported in ¹ as an example. If another configuration were studied, the last variable in the state vector would change accordingly since it represents the DC current variation of the added link.

The output vector $\Delta z(t)$ contains the signals of interest, which are in our case the DC voltage variations at the four terminals, representing a subset of the state variables.

$$\Delta \mathbf{Z}(t) = [\Delta v_{dc1}, \Delta v_{dc2}, \Delta v_{dc3}, \Delta v_{dc4}].$$
(3)

The input vector, $\Delta u(t)$ is composed of the reference current signal variations used to control each of the terminal converters, i.e.:

$$\Delta u(t) = [\Delta i_{d,ref,1}, \Delta i_{q,ref,1}, \Delta i_{d,ref,2}, \Delta i_{q,ref,2}, \Delta i_{d,ref,3}, \Delta i_{q,ref,3}, \Delta i_{d,ref,4}, \Delta i_{q,ref,4}].$$

$$(4)$$

The consistency of the operating point is ensured by the operation of terminal 4 as a slack-bus.

 $\Delta x_{1-4} = \left[\Delta i_{d,1}, \Delta i_{q,1}, \Delta v_{dc,1}, \Delta g_{d,1}, \Delta g_{d,1}, \Delta g_{d,1}, \Delta i_{d,2}, \Delta i_{d,2}, \Delta v_{dc,2}, \Delta g_{d,2}, \Delta g_{d,2}, \Delta g_{d,2}, \Delta i_{dc,12}, \Delta i_{d,3}, \Delta i_{q,3}, \Delta v_{dc,3}, \Delta g_{d,3}, \Delta g_{d,3}, \Delta g_{d,3}, \Delta i_{d,4}, \Delta i_{d,4}, \Delta g_{d,4}, \Delta$

AC reference current	Reference value (in p.u.)	AC reference current	Reference value (in p.u.)
$I_{d,ref,1}$	-0.5	Id,ref,3	-0.7
$I_{q,ref.1}$	0	Iq,ref,3	0
$I_{d,ref,2}$	0.3	$I_{d,ref,4}$	0.9
I _{a.ref.2}	0	Ia.ref.4	0

TABLE II. SELECTED OPERATING POINT FOR THE CONVERTERS IN THE 4-TERMINAL HVDC TEST GRID

III. MATHEMATICAL PROBLEM FORMULATION

In the considered analysis, we assumed that a disturbance has caused the deviation of the initial state from the nominal operating condition and we want to assess the capability of the HVDC system to counteract this deviation, in particular by minimizing the DC voltage oscillations. We need to define a performance indicator able to quantify the contribution of any additional link to the stabilization of the DC voltage of the overall multi-terminal grid. The technique we use is based on the derivations in [10], [11], which exploit the Linear Matrix Inequality (LMI) theory to determine the best HVDC link placement, with the goal of minimizing the rotor oscillations of the generators in the interconnected AC grid. In this paper, we use a similar approach, but the scope differs from AC grid reinforcement. For a large and highly interconnected offshore HVDC network, a higher priority is given to minimizing the variations of the DC grid voltage, in the event of external disturbances.

The selected mathematical formulation addresses the physical constraints of the system. In particular, the input is subject to ellipsoidal constraints (6), which bound the squared sum of the d and q reference current components, thus reflecting the maximum AC current limitation of each converter. To this end, we correspondingly divide Δu as

$$\Delta u = \begin{bmatrix} \Delta u_1^T & \Delta u_2^T & \Delta u_3^T & \Delta u_4^T \end{bmatrix}^T,$$
 (5)

where $\Delta u_n \in R_2$, $\forall n = 1...4$ and it must satisfy:

$$\Delta u(t) \in \mathbf{U} = \left\{ \Delta u \in R_8 : \Delta u_n^T E_{u,n} \Delta u_n \le 1, \forall n = 1...4 \right\}, \quad (6)$$

 $E_{u,n} \in R_{2x2}$ being a symmetric positive definite matrix.

In a similar way, the initial state $\Delta x(0)$ is assumed to lie within an ellipsoidal set X_0 , which represents the set of possible worst-case conditions assumed by the system after any perturbation:

$$\Delta x(0) \in X_0 = \left\{ \Delta x \in R_{23} : \Delta x^T E_x \Delta x \le 1 \right\},\tag{7}$$

 $E_x > 0$ being a symmetric positive definite matrix.

The performance indicator we select to quantify the degree of stability of the DC voltage against disturbances is expressed as the time-integral of a quadratic function of the DC voltage variations vector, that is:

$$J_{perf} = \int_0^\infty z(t)^T M \ z(t) dt , \qquad (8)$$

where the matrix M is positive definite (M > 0) and acts as a weight on the signals of interest.

The goal of our study is to identify, among a set of possible grid topologies, the one that minimizes the disturbanceinduced oscillations on the DC voltage under the worst-case scenario, with the action of an appropriate controller. We consider the use of a linear feedback in order to design this DC stabilizing controller:

$$u(t) = K x(t). \tag{9}$$

As mentioned, u(t) represents the outer controller of a cascaded feedback loop, and it provides the current references to the inner current regulator that is based on standard PI controllers whose dynamics are included in our system of equations (1) and (2). Additionally, the proposed analysis allows identifying the optimal controller *K* ensuring the best performance.

IV. IDENTIFICATION OF THE OPTIMAL EXPANSION OPTION

The choice of the optimal feedback gain, K, in (9) with the highest stabilizing effect on a selected grid configuration requires the solution of the following min-max optimization problem (10):

$$J_{perf}^{*} = \min_{K} \max_{x(0) \in X_{0}} J_{perf}$$
(10)

s.t. (1), (2), (6), (7), (8), (9).

The direct computation of the above is difficult. However, under the assumption of ellipsoidal bound for input constraints (6) and initial state (7), and with the choice of a quadratic performance indicator (8), we can formulate the problem using Linear Matrix Inequalities, as shown in [10], [11]:

$$\frac{1}{J_{perf}^*} = \max_{s>0, Q>0, Y} s \tag{11}$$

s.t.

$$0 \leq \begin{bmatrix} (AQ + BY) + (AQ + BY)^T & QC^T \hat{M} \\ \hat{M}^T C Q & -I \end{bmatrix}$$
$$0 \leq Q - sE_x^{-1}$$
$$0 \leq \begin{bmatrix} Q & Y_n^T \\ Y_n & sE_{u,n}^{-1} \end{bmatrix}, \forall n = 1...4$$

where $Q \in R_{23\times23}$, $Y_n \in R_{2\times23} \forall n = 1...4$, $Y = \begin{bmatrix} Y_1^T & Y_2^T & Y_3^T & Y_4^T \end{bmatrix}^T$ and *s* is a scalar quantity whose inverse represents the upper bound of J_{perf} and \hat{M} is derived from the factorization of $M = \hat{M}\hat{M}^T$. Once the optimal solution (S^*, Q^*, Y^*) is obtained, it is possible to recover the optimal feedback gain for the worst initial condition as $K_w^* = Y^*Q^*$ and apply the corresponding optimal control input $u^*(t) = K_w^*x(t)$. Additionally, it is possible to compute the worst case initial oscillation $\Delta x_w(0)$ as $\Delta x_w(0) = \hat{E}^{-T}v_1$, where $\hat{E} = VD^{1/2}$, V contains the eigenvector of E_x , D is diagonal with the eigenvalues of E_x and v_I is the eigenvector of $\hat{E}^{-1}Q^{*-1}\hat{E}^{-T}$ associated with its largest eigenvalue.

The matrices A, B and C, correspond to the state space matrices in (1) and (2) and they will therefore differ for each of the four grid configurations considered. The matrix M is selected here as the unity matrix.



The matrices $E_{u,n}$, are selected to be diagonal matrices

enforcing (6) with the actual current constraints of each terminal. This assumes the total current to be bounded to take into account the limits on the maximum possible overcurrent that the converter switches can withstand, when operating close to nominal power, i.e.:

$$|\Delta i_{ref,n}| = \sqrt{\Delta i_{d,ref,n}^2 + \Delta i_{q,ref,n}^2} \le 0.25 \, p.u. \tag{12}$$

The matrix E_x is also a diagonal matrix, that enforces constraints (7), on maximum assumed deviations of the initial state variables (13-16). The AC current limitations reflect those imposed to the corresponding references in (7), and the DC voltage and DC current ones are associated with physical limitations of the DC cable.

$$|\Delta i_{n}| = \sqrt{\Delta i_{d,n}^{2} + \Delta i_{q,n}^{2}} \le 0.25 \, p.u.$$
(13)

$$|\Delta V_{dc,n}| \le 0.1 \, p.u. \tag{14}$$

$$|\Delta g_{n}| = \sqrt{\Delta g_{d,n}^{2} + \Delta g_{q,n}^{2}} \le 0.2 \, p.u.$$
(15)

$$|\Delta I_{dc,n}| \le 0.1 \, p.u. \tag{16}$$

The maximization problem (11) has been solved using the Yalmip toolbox [12] of Matlab® to determine J^*_{perf} for all the configurations. In the considered test case, the four possible

expansion options are assessed and compared based on the twofold criteria of: a) minimum cable length; and b) minimum DC voltage oscillations (i.e. minimum J_{perf}). Assuming that an equal weight is given to both objectives, represented on orthogonal axes in Fig.3, we can see that link 1-4 would be the preferable option, as it represents the point at minimum distance from the origin. However, it is possible to allocate different weights to each criteria based on planning phase preferences and priorities, which may result in the final selection of a different preferable configuration.

From the results obtained for the four configurations, we can notice that the J_{perf}^* index is highly dependent on the length of the added HVDC link. The higher the cable length, the lower the DC oscillation index and the better the performance under worst-case initial disturbances. This implies that the two selected objectives are conflicting. This result is, however, physically sound and can be explained by the fact that an increase of the line length, and consequently of the line capacitance, results in DC voltage ripple reduction and stability enhancement. Moreover, the better performance of links 1-4 and 2-4 compared to links 1-3 and 2-3, can be due to the operation of terminal 4 as a slack bus compensating for the network power balance while its voltage is kept constant at the reference value. The numerical results provide quantitative measure for the explanation above.

V. PARAMETER SENSITIVITY OF THE OPTIMAL CONFIGURATION

We have performed a parameter sensitivity analysis, by halving and doubling the cable equivalent kilometric capacitance, inductance and resistance (alternatively), compared to the base case, to analyze the dependence of the expansion decision on the line parameters and further confirm the physical explanation of the voltage stabilization previously proposed. From Fig. 4, we can clearly see that the cable capacitance has the biggest influence on the J_{perf}^* index, with larger deviations in the behavior of links of equal length obtained for lower capacitances. On the contrary, and according to intuition, resistors and inductors do not have significant effect on the voltage stabilization. The analysis



Fig. 4 Comparison of the considered expansion options based on cable parameters' sensitivity analysis: a) capacitor variation, 0.08, 0.16 & 0.32 uF/km (left), b) inductor variation 0.095, 0.19 & 0.38 mH/km (middle), c) resistance variation 0.007, 0.014 & 0.028 Ω/km (right) (Arrow direction shows the parameter's value increase.)



Fig. 5 System response of the DC voltage at terminal 1 (top left), terminal 2 (bottom left) terminal 3 (top right) and terminal 4 (bottom right). In all graphs, the blue trace corresponds to DC voltage evolution starting from the identified "worst-case" initial conditions, and the other curves correspond to other randomly selected initial conditions within the allowed range

shows that even considering wide ranges of variation for the cable kilometric parameters, the line characteristic having the highest impact on the reduction of DC voltage oscillation is the cable length. Even though the J_{perf}^* values vary due to cable parameters' variation, the comparison among the four grid configurations continues to indicate link 1-4 as the optimal one.

VI. OPTIMAL CONTROLLER FOR MAXIMUM DC VOLTAGE STABIILITY

The second result of the proposed study is the mathematical derivation of the linear feedback controller that allows achieving the maximum stabilization of the system voltage in the worst-case scenario.

As explained in the previous section, the algorithm allows identifying the worst-case initial condition $\Delta x_w(0)$, within the allowed region described by (7) and (12-16), which produces the highest oscillation in the DC voltages, and targets the optimal design of the controller *K* to that specific initial condition.

In the considered configurations, the identified worst case condition corresponds to a vector $\Delta x_w(0)$ representing the maximum deviations of the *d*-component of the current integral state for each of the converters $\Delta g_{d,n}$. It is interesting to note the higher sensitivity of the DC voltage stabilization index to a control variable (i.e. the integral of the state

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corresponding to the d-current) than to physical parameters depending on the network configuration.

Fig. 5 shows the free system response (i.e. the DC voltages of the terminals), starting from the worst-case initial deviation identified by the algorithm (thicker line), and four other randomly selected realistic initial conditions. It can be seen how higher DC voltage oscillations with respect to other random conditions are detected in the identified worst-case.

In order to take a closer look at the optimal controller structure, the controller obtained for the configuration including link 1-4 is reported in ², as an example. Controllers derived for the other grid configurations have similar structure.

Even rows correspond to the *q*-components of the reference currents, and, as expected, they are all equal to zero, since DC voltage oscillations indicate active power imbalances and hence should be associated to an action on *d*-current components.

Note that the linear feedback acts on the states corresponding to the DC voltage and *d*-current integrals of all the four terminals, as well as on the *d*-current of terminals 1, 3 and 4. This can be explained since 1 and 3 are directly connected terminals in this configuration and terminal 4 acts as the slack bus.

It is also worth noting that the optimal controller we computed is sparse. However, it is centralized, in the sense that each terminal requires some information from all the other terminals in order to synthesize the current references.

	-0.0003	0	0.0087	-0.3062	0	0	0	0.0044	-0.3147	0	0	-0.0004	0	0.0033	-0.3029	0	0.0002	0	0.0076	-0.3191	0	0	0]
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	-0.0003	0	0.0087	-0.3062	0	0	0	0.0044	-0.3147	0	0	-0.0004	0	0.0033	-0.3029	0	0.0002	0	0.0076	-0.3191	0	0	0
K	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
$K_{1-4} =$	-0.0003	0	0.0087	-0.3062	0	0	0	0.0044	-0.3147	0	0	-0.0004	0	0.0033	-0.3029	0	0.0002	0	0.0076	-0.3191	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	-0.0003	0	0.0087	-0.3062	0	0	0	0.0044	-0.3147	0	0	-0.0004	0	0.0033	-0.3029	0	0.0002	0	0.0076	-0.3191	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0



Fig. 6 Non-linear droop controller (black line) and its linear approximation (red dashed line) complying with the same Δi_{DC} and Δv_{DC} limits

On the other hand, a classic droop controller, which is often used as the outer loop controller in such HVDC applications, can also be represented via linear feedback in normal operating conditions. A power feedback and a current feedback, both expressed in the per-unit system, can be considered equivalent provided that the voltage is considered almost constant.

In the case of a droop controller, however, the structure of the matrix K would be much simpler. The controller would be decentralized and would have only four non-zero elements. They would be found in odd rows, on the columns corresponding to the DC-voltage at the same terminal of the *d*-current reference considered.

However, the design of a droop controller is often based on experience rather than on analytical procedures and the droop coefficients, $\Delta v_{DC}/\Delta P_{DC}$, normally used are in the range 2-10%. Considering the maximum allowed DC voltage oscillation of our case, such limited droop would imply the need of significant I_{DC} current injections by the terminals, which may, in turn, conflict with our considered DC current limitation in (16). In practical applications, droop controllers normally take into account the physical system limitations (i.e. current or power limits) by inserting proper saturations on the controlled signals (Fig. 6). This makes the representation highly nonlinear and difficult to capture by the linearized approach proposed. A linearized representation of the droop action compatible with the problem formulation of Section III, and compliant with the state constraints set in our optimization algorithm (7), encompassing the maximum deviation on both v_{DC} and i_{DC} (red points in Fig. 6) requires a higher droop slope than the actual droop coefficient used in realistic cases. Thus, due to the non-linear nature of the real droop controller a quantitative comparison with the proposed controller is not straightforward. However, based on additional tests, a qualitative comparison between the optimal controller derived analytically and the traditional droop controller is presented in Table III.

The possibility of analytically identifying the worst-case DC voltage oscillations and designing a suitable stabilizing controller, without the need of dynamic models and multiple simulations can be especially useful at the preliminary stage of expansion planning, when a multitude of parameters and

TABLE III: QUALITATIVE COMPARISON BETWEEN THE PROPOSED OPTIMAL CONTROLLER AND STANDARD DROOP CONTROLLER

Optimal linear feedback controller	Standard droop controller
Is obtained by analytical derivation considering system physical constraints	Requires case-by-case tuning with ex-post check on constraints compliance
Ensures best performance under the worst case scenario	May provide suboptimal performance in the worst case scenario
When a specific scenario is considered, it can be used to ensure the best performance under such scenario	The same droop controller can ensure good performances in most cases
Centralized solution	Decentralized solution

scenarios should be considered and detailed simulations of all of them is prohibitive. In this way, the stability enhancement contribution can be easily included as an additional parameter in a multi-objective optimization, before deciding which link to deploy. Once the grid decision is made, we can implement decentralized droop controllers, having simpler structure, independence on long-distance communication, and good performance in a plurality of realistic conditions.

As an additional point, it is interesting to highlight the high sensitivity of the DC voltage stabilization index to the current integral state variable variation $\Delta g_{d,n}$.

This is as an additional reason to support the need for an integrated analysis between control/stability aspects, and expansion planning studies. In fact, enhanced stability and controllability should be given an economic value when the deployments of new HVDC links is considered.

VII. CONCLUSIONS

We investigated the problem of assessing different expansion options in offshore multi-terminal HVDC grids. A performance index that is capable to quantify how each added DC link can contribute to the reduction of DC voltage oscillations in the system is introduced. Such index can be used as one of the decision criteria in expansion planning, in conjunction with other technical and economic metrics.

Few previous contributions included control and stability aspects in the assessment of HVDC expansion options. Unlike them, our paper has a specific focus on large off-shore systems, where the DC voltage stabilization has a higher priority than oscillation damping in the interconnected AC grids.

Moreover, we have presented a mathematical formulation that allows decoupling the boundaries of the input signals of one terminal from those of the other terminals in the grid, thus ensuring more fidelity to the real implementation than previous similar works. We have exemplified the proposed approach using a 4-terminal HVDC test grid. Nevertheless, this can be easily extended to hybrid AC/DC systems and more complex (e.g. meshed) configurations, by simply modifying the linearized dynamical system matrices accordingly.

We have discussed the merits of the proposed approach in the context of HVDC expansion planning, and analyzed the characteristics of the proposed optimal controller versus a standard droop controller, normally used for such applications.

We plan to investigate decentralized optimal control as part of our future work, with the goal of retaining the advantages of both the optimal linear feedback controller and traditional droop controllers.

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APPENDIX

BASE VALUES USED FOR THE P.U. SYSTEM

Base power, S_b :

1200 MVA $\sqrt{\frac{2}{3}} \times 400 \text{ kV}$ AC base voltage, $V_{AC,b}$:

DC base voltage, $V_{DC,b}$: $2 \times \sqrt{\frac{2}{3}} \times 400 \text{ kV}$