



Master's thesis

2017

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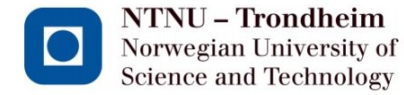
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# Design and analysis of a video scaler suited for FPGA implementation

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Design and analysis of a video scaler  
suited for FPGA implementation

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Master of Science in Electronic Systems Design  
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