



Eirik Steen-Hansen

**NTNU**  
 Norwegian University of  
 Science and Technology  
 Faculty of Information Technology,  
 Mathematics and Electrical Engineering  
 Department of Electronics and  
 Telecommunications

Eirik Steen-Hansen

# Modeling and Design of a Dual-Residue Pipelined ADC in 130nm CMOS

Trondheim, June 2012

