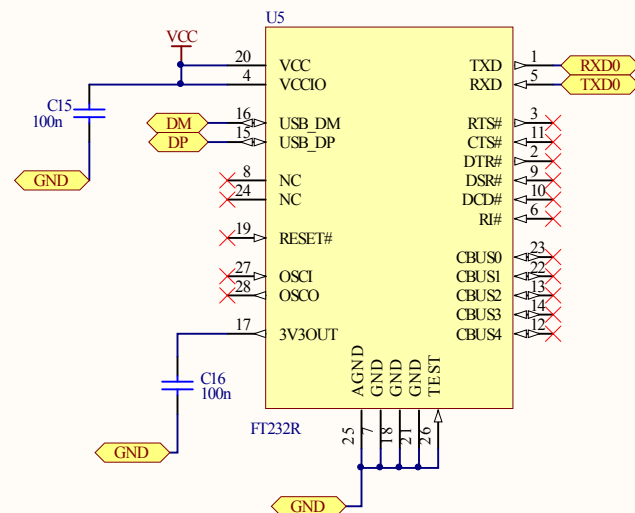


Title		
PDCP - hardware layout		
Size	Number	Revision
A4		
Date:	2012-06-04	Sheet of 1/2
File:	D:\politechnika\pdcpcb\Board.SchDoc	Drawn By: Andrzej Zamojski



Title		
PDCEP - hardware layout		
Size	Number	Revision
A4		
Date:	2012-06-04	Sheet of 1/2
File:	D:\politechnika\..pdcpBoard_2.SchDoc	Drawn By: Andrzej Zamojski

Bill of Materials

<Parameter Title not found>

Source Data From: pdcBoard.PrjPCB
Project: pdcBoard.PrjPCB
Variant: None

Creation Date: 2012-06-04 08:59:55
Print Date: 41064 41064,375

Footprint	#Column Name	#Column Name	Designator	Description	Quantity
SMDA C0805			C1, C2	Polarized Tantalum Capacitor	2
			C3, C4, C5, C6, C7, C8, C9, C14, C15, C16	Capacitor	10
C0805			C10, C11, C12, C13	Capacitor	4
SMDA			D1, D2	LED	2
USBA-G			J1	USB-A	1
SIP-4			J2	Connector	1
SIP-8			J3	Connector	1
SIP-2			J4, J6, J7	Connector	3
SIP-4			J5	Connector	1
SIP-4			J8, J10	Connector	2
IDC10			J9	JTAG Header for AVR MCU's	1
L_SMD_1			L1	L	1
R0805			R1, R5	Resistor	2
R0805			R2	Resistor	1
R0805			R3	Resistor	1
R0805			R4	Resistor	1
TACT 6x3.5mm			sw1, sw2	Przełącznik TACT 6x6mm SPSTNO wysokość7mm 1,6N	2
SMD TO-252			U1	8-Bit AVR Microcontroller with 128K Bytes In-System Programmable Flash Memory	1
64A_N			U2		1
			U3		1
SOIC18			U4		1
SOIC-8			U5		1
SSOP-28			Y1, Y2	USB UART IC Crystal Oscillator	2
SMD 12SMX A					43

Approved	Notes