



Leif Tore Rusten, Gunnar Inge Sortland

NTNU
Norwegian University of
Science and Technology
Faculty of Information Technology,
Mathematics and Electrical Engineering
Department of Computer and
Information Science

Leif Tore Rusten
Gunnar Inge Sortland

Implementing a Heterogeneous Multi-Core Prototype in an FPGA

Trondheim, July 2012

