

Project:  
Control\_card.PrjPcb

Title:  
Atmega.SchDoc

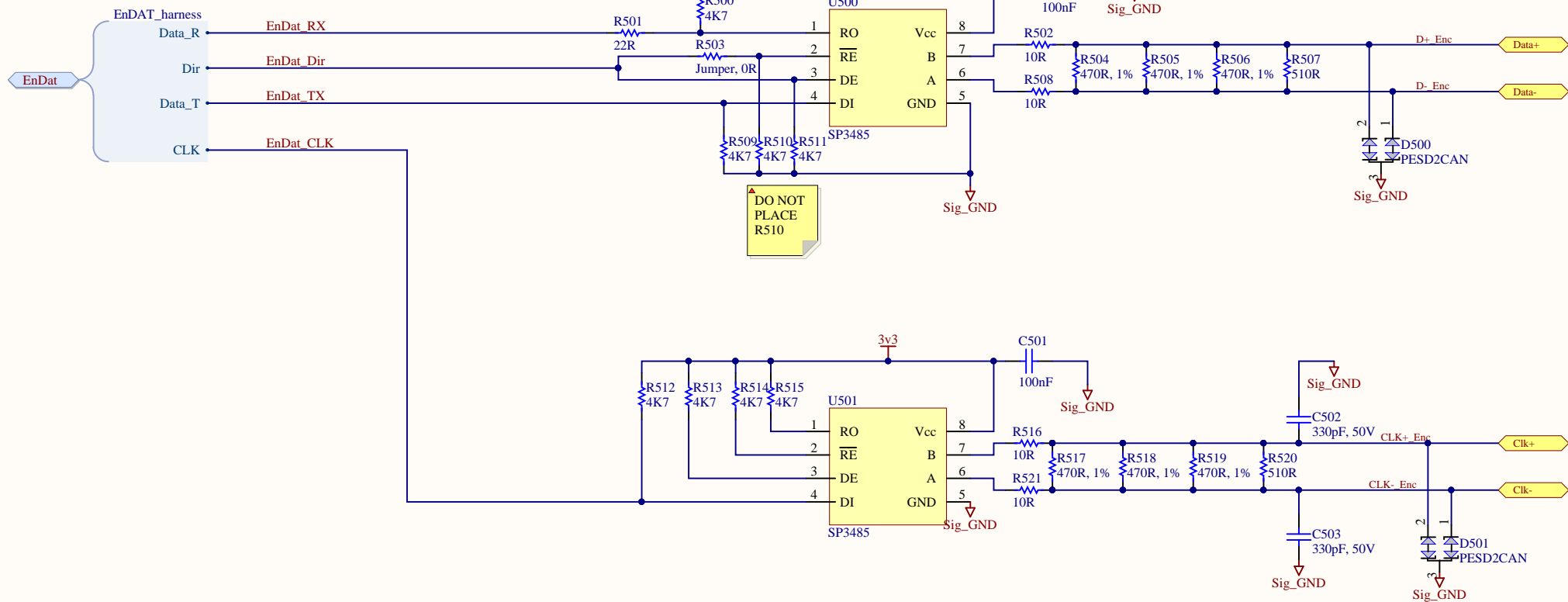
Sheet number:  
2 out of 16

Revision:  
2103

Designer:  
Anders Holter Bjørkto

Date:  
04.07.2016

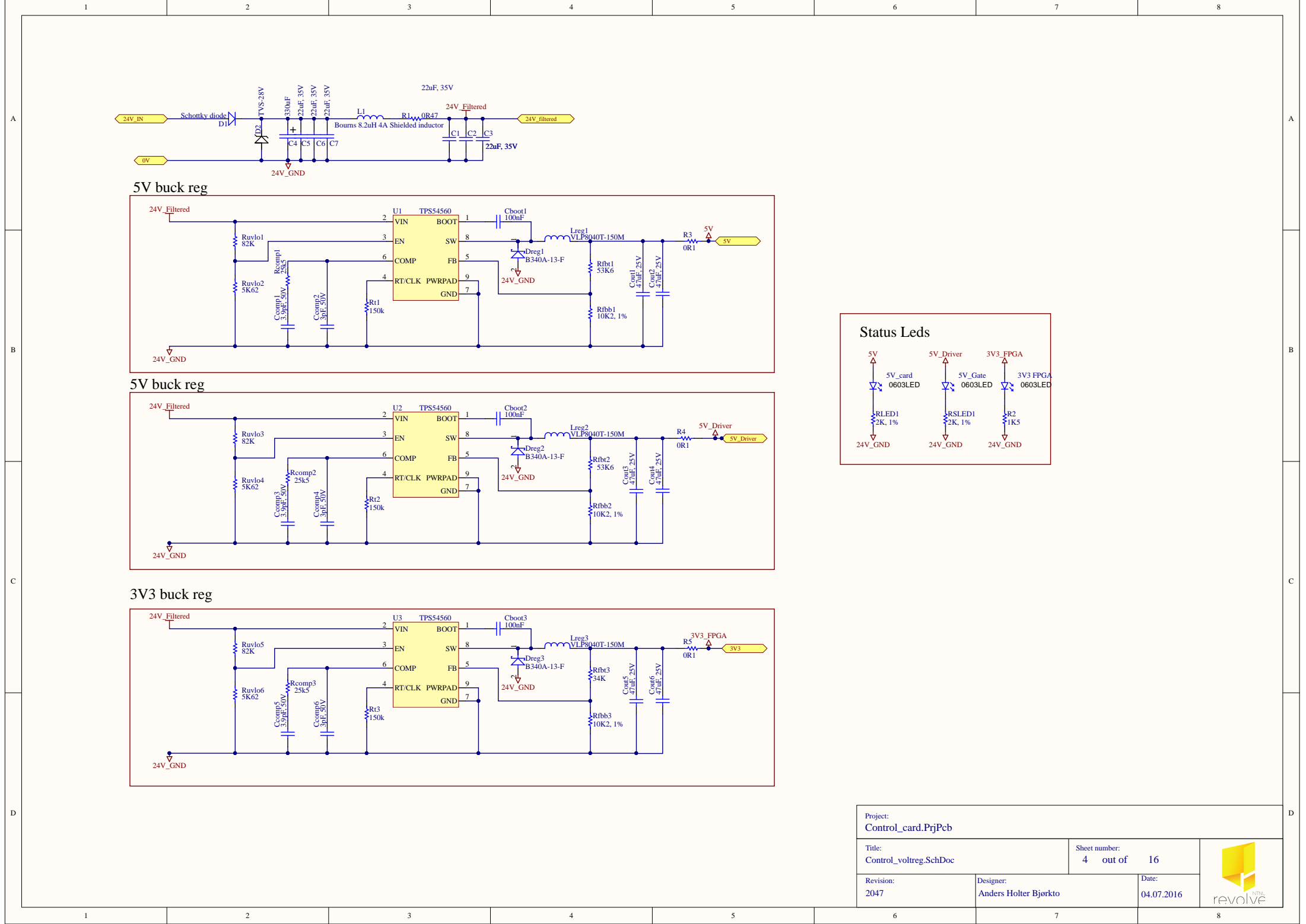




▲ Or CLK signals, and send to DI

Project: Control_card.PrjPcb		
Title: Control_encoder.SchDoc		Sheet number: 3 out of 16
Revision: 2047	Designer: Anders Holter Bjørkto	Date: 04.07.2016



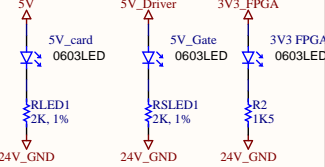


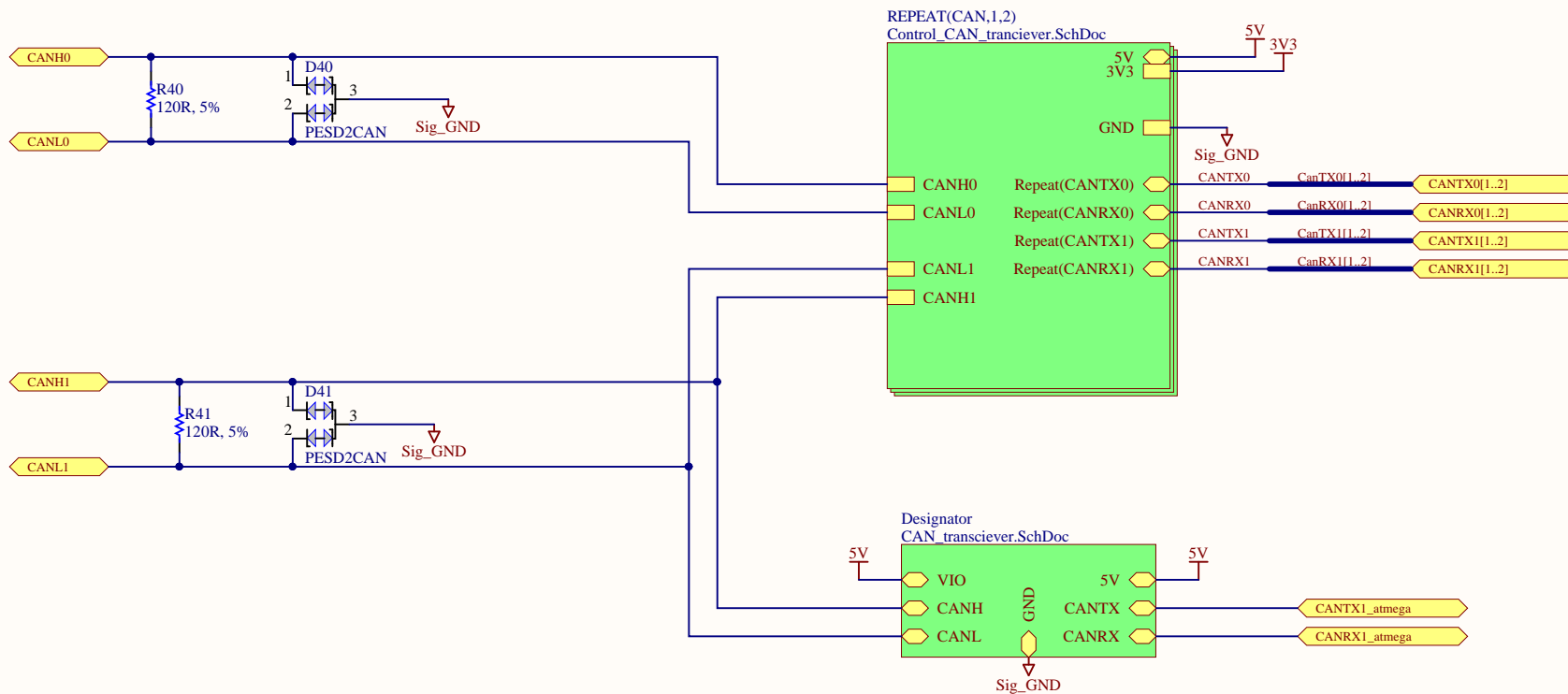
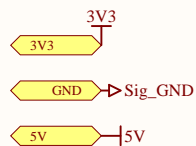
5V buck reg

5V buck reg

3V3 buck reg

Status Leds





Project:  
Control\_card.PrjPcb

Title:  
Control\_CAN\_termination.SchDoc

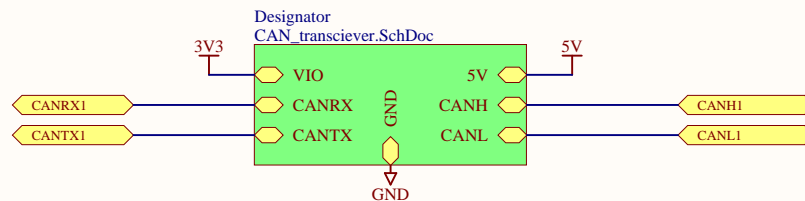
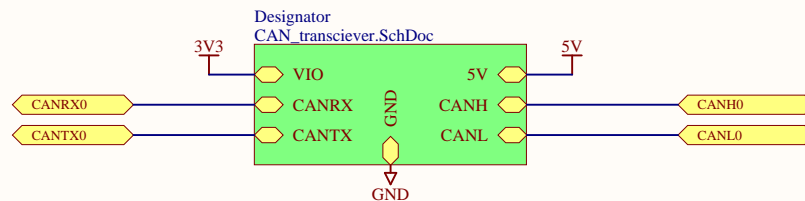
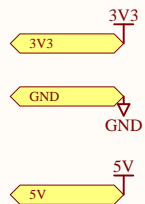
Sheet number:  
5 out of 16

Revision:  
2047

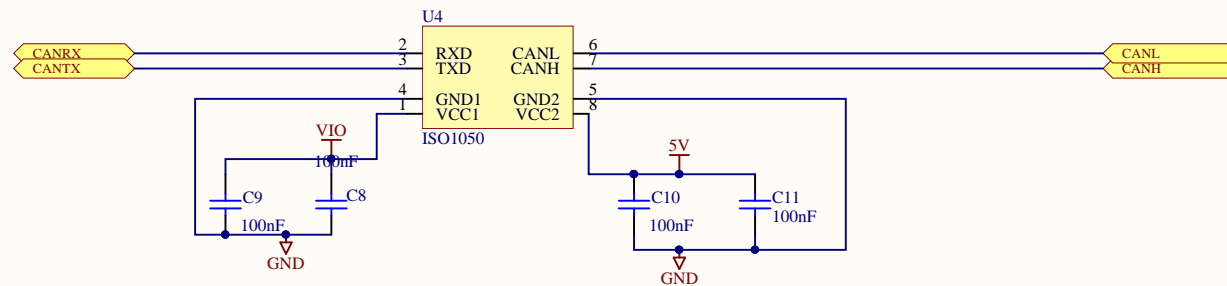
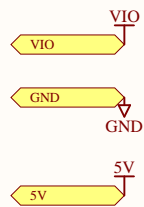
Designer:  
Anders Holter Bjørkto

Date:  
04.07.2016





Project: Control_card.PrjPcb			
Title: Control_CAN_tranciever.SchDoc		Sheet number: 6 out of 16	
Revision: 1738	Designer: Anders Holter Bjørkto		Date: 04.07.2016



Project:  
Control\_card.PrjPcb

Title:  
CAN\_transciever.SchDoc

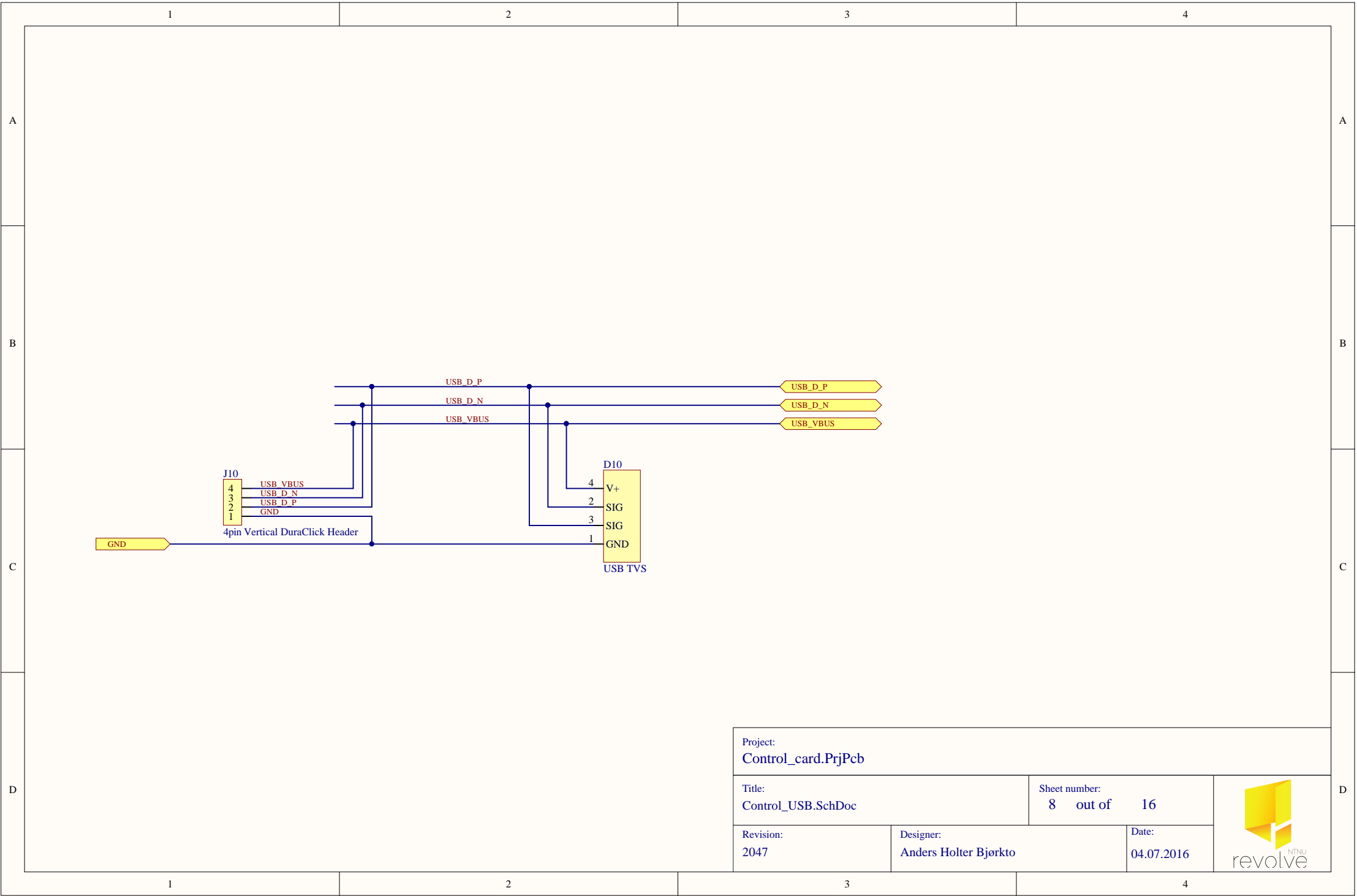
Sheet number:  
7 out of 16

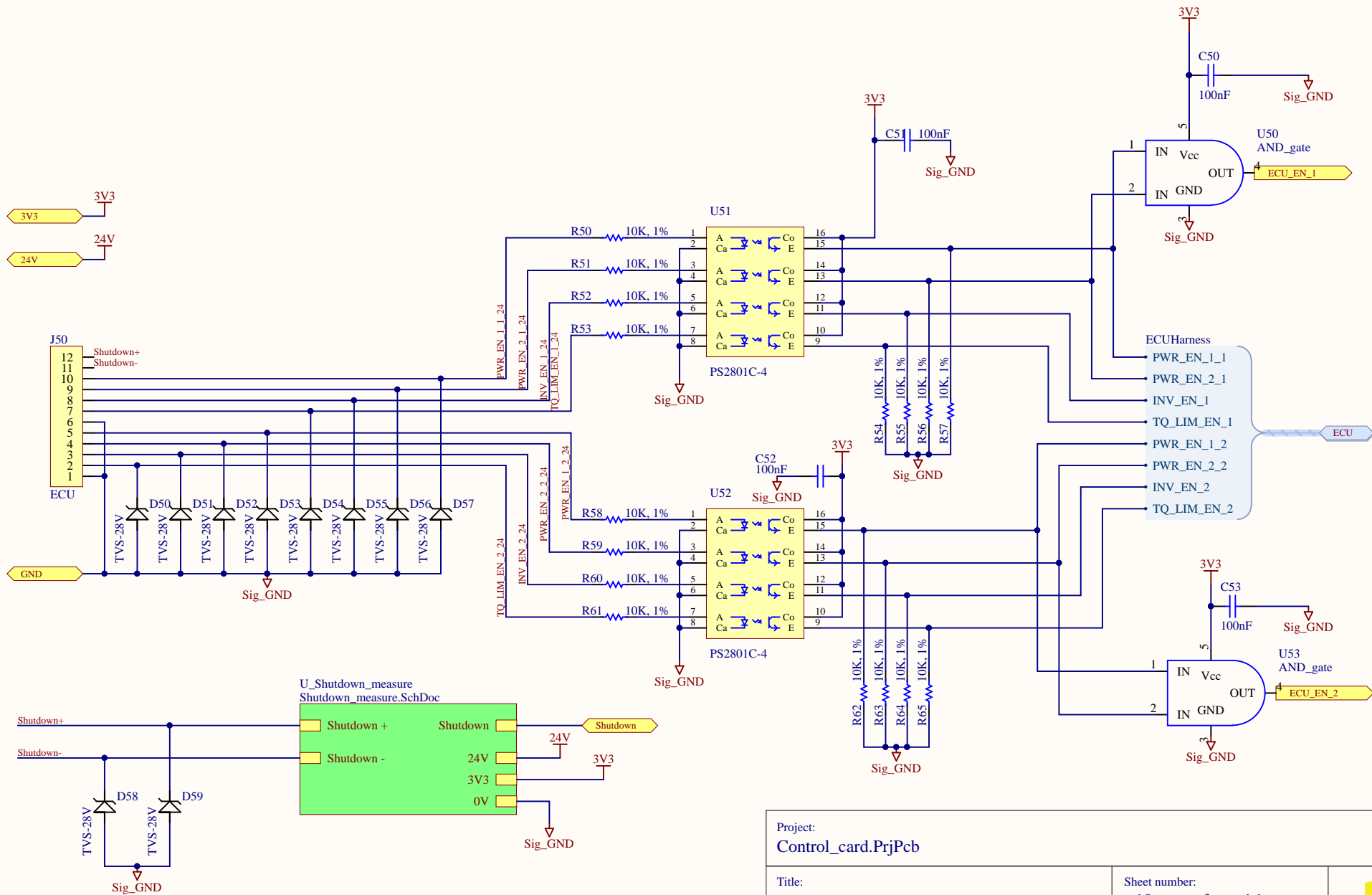
Revision:  
2047

Designer:  
Anders Holter Bjørkto

Date:  
04.07.2016







Project:  
Control\_card.PrjPcb

Title:  
Control\_ECu.SchDoc

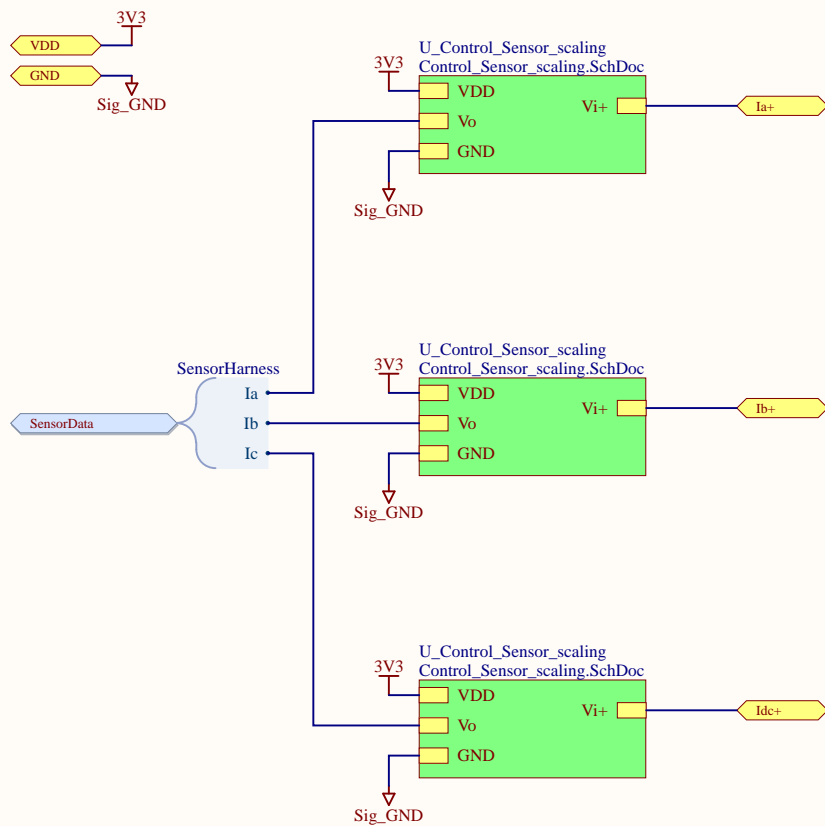
Revision:  
2047

Designer:  
Anders Holter Bjørkto

Sheet number:  
10 out of 16

Date:  
04.07.2016





Project:  
Control\_card.PrjPcb

Title:  
Control\_Sensors\_Inv.SchDoc

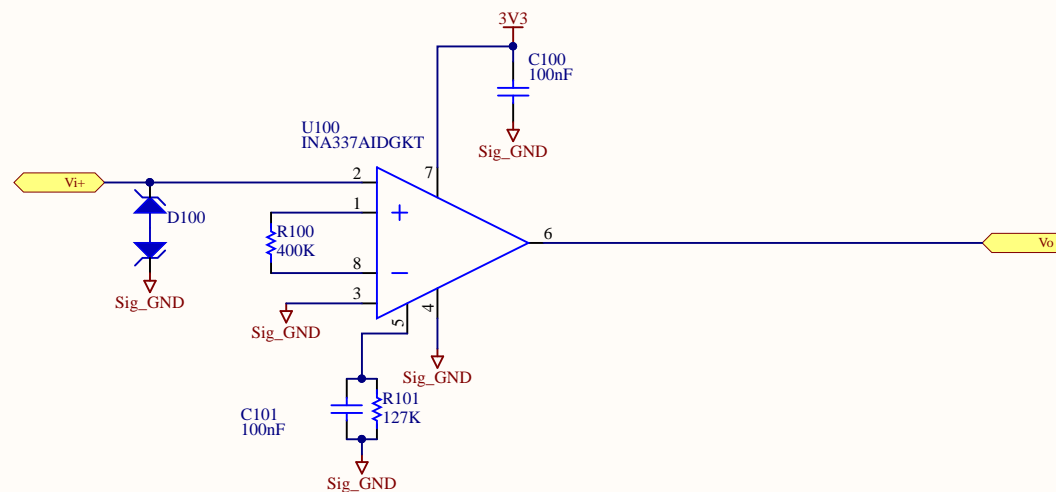
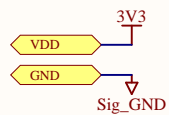
Sheet number:  
11 out of 16


Revision:  
2000

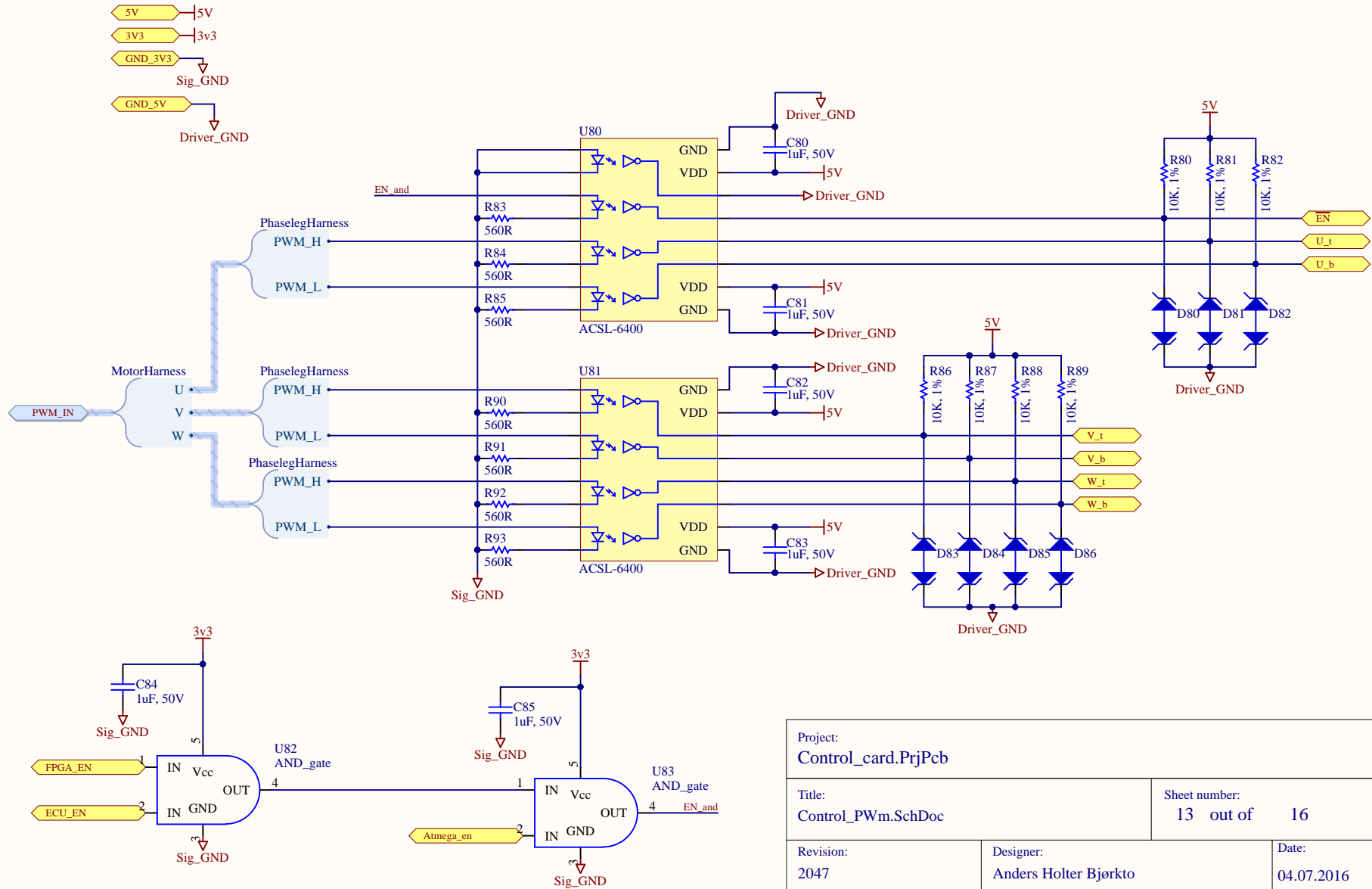
Designer:  
Anders Holter Bjørkto

Date:  
04.07.2016





Project: Control_card.PrjPcb			
Title: Control_Sensor_scaling.SchDoc		Sheet number: 12 out of 16	
Revision: 2047	Designer: Anders Holter Bjørkto	Date: 04.07.2016	



Project:  
Control\_card.PrjPcb

Title:  
Control\_PWm.SchDoc

Revision:  
2047

Designer:  
Anders Holter Bjørkto

Sheet number:  
13 out of 16

Date:  
04.07.2016





1

2

3

4

A

A

B

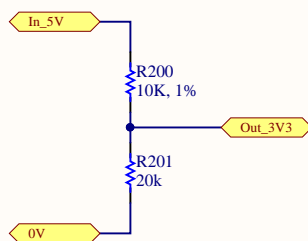
B

C

C

D

D



Project:  
Control\_card.PrjPcb

Title:  
level\_shifter.SchDoc

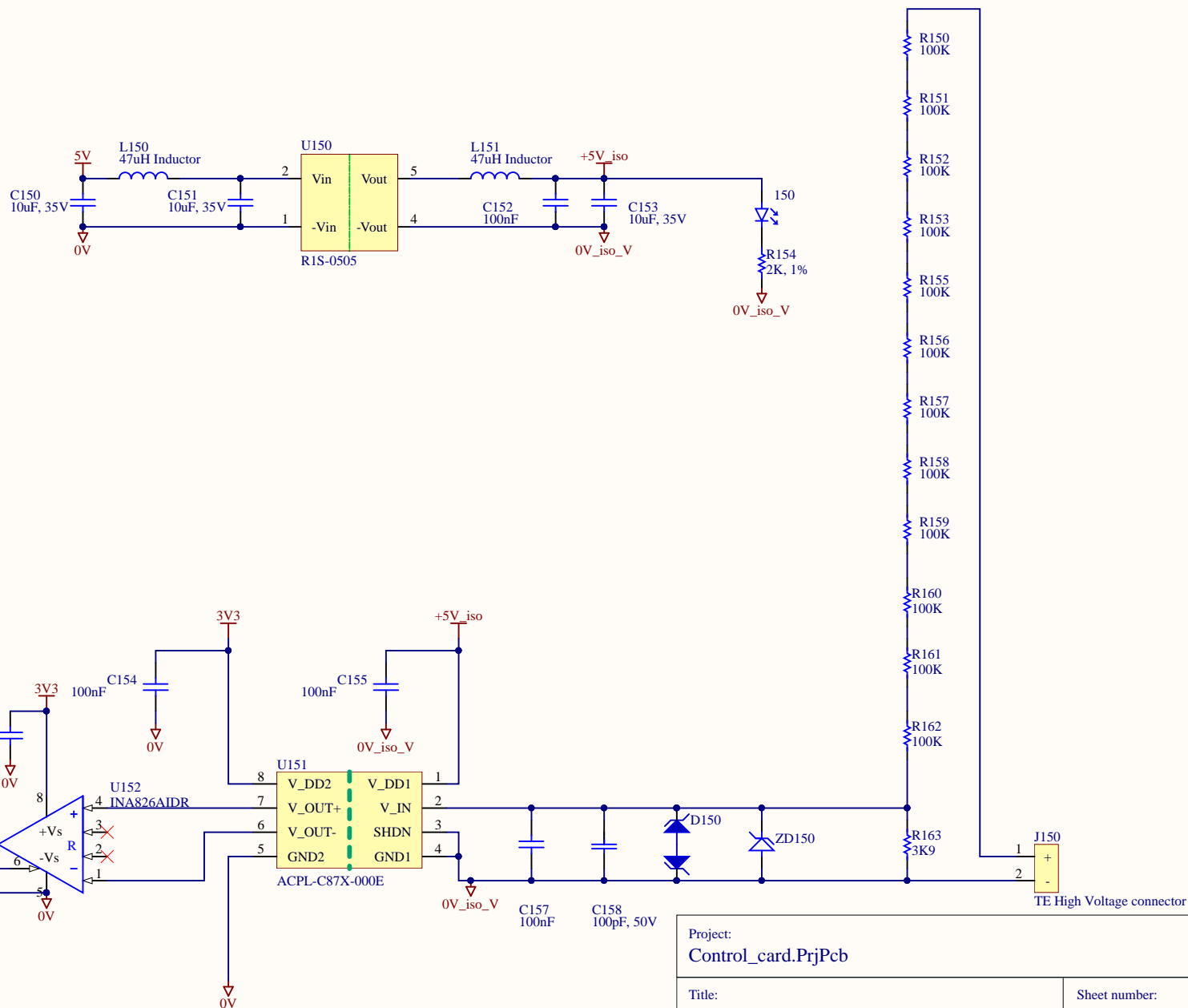
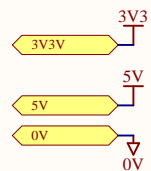
Sheet number:  
15 out of 16

Revision:  
2047

Designer:  
Anders Holter Bjørkto

Date:  
04.07.2016





Project:  
Control\_card.PrjPcb

Title:  
Control\_Voltage\_sens.SchDoc

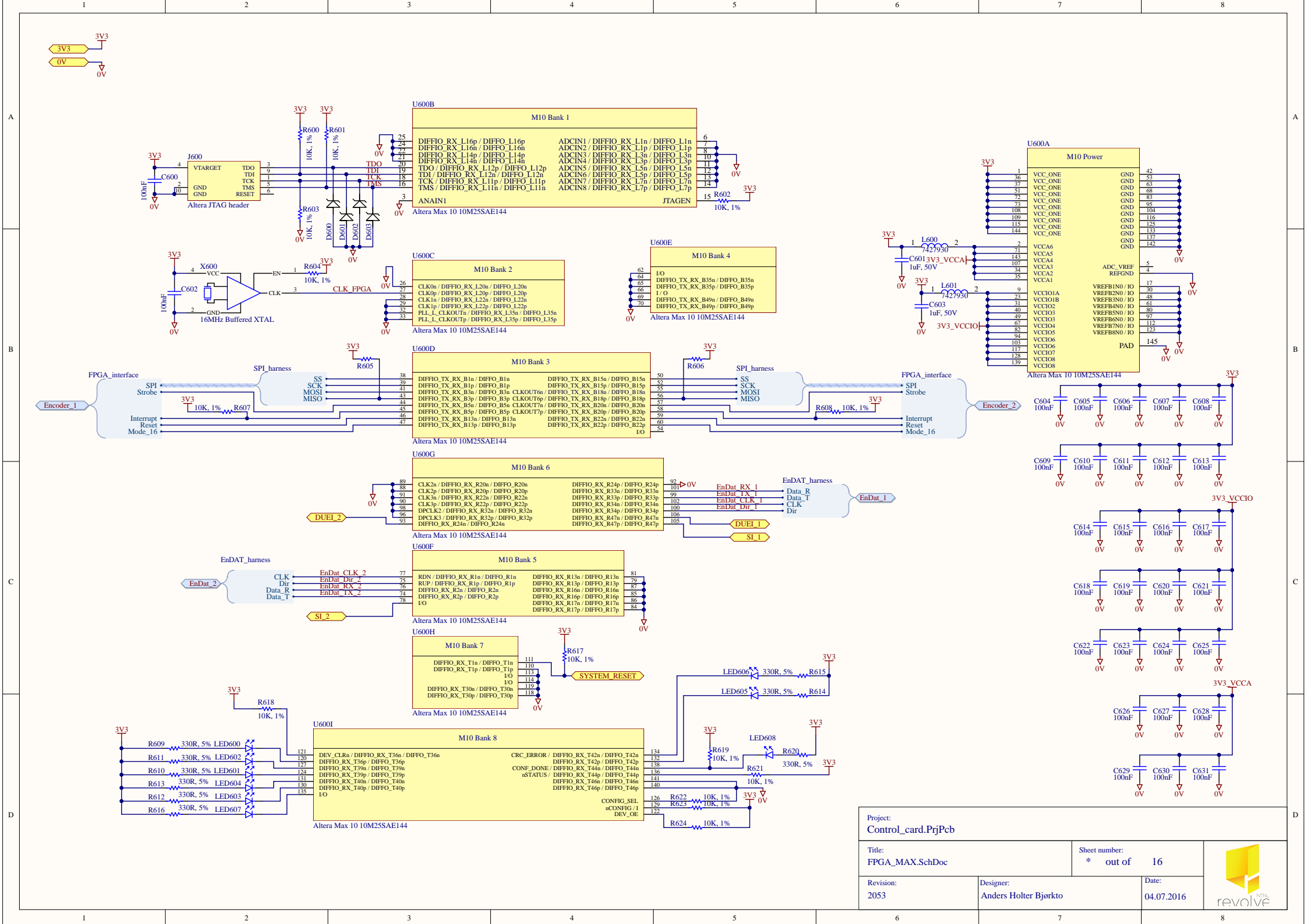
Revision:  
2047


Designer:  
Anders Holter Bjørkto

Sheet number:  
16 out of 16

Date:  
04.07.2016





Project: Control_card.PrjPcb				
Title: FPGA_MAX.SchDoc		Sheet number: * out of 16		
Revision: 2053	Designer: Anders Holter Bjørkto	Date: 04.07.2016		

