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Chapter 1

GaAs spacer thickness

Simulations with varying GaAs spacer thickness are shown in figures 1.1–1.21. The figures are arranged by increasing thickness, from 60 nm to 80 nm.

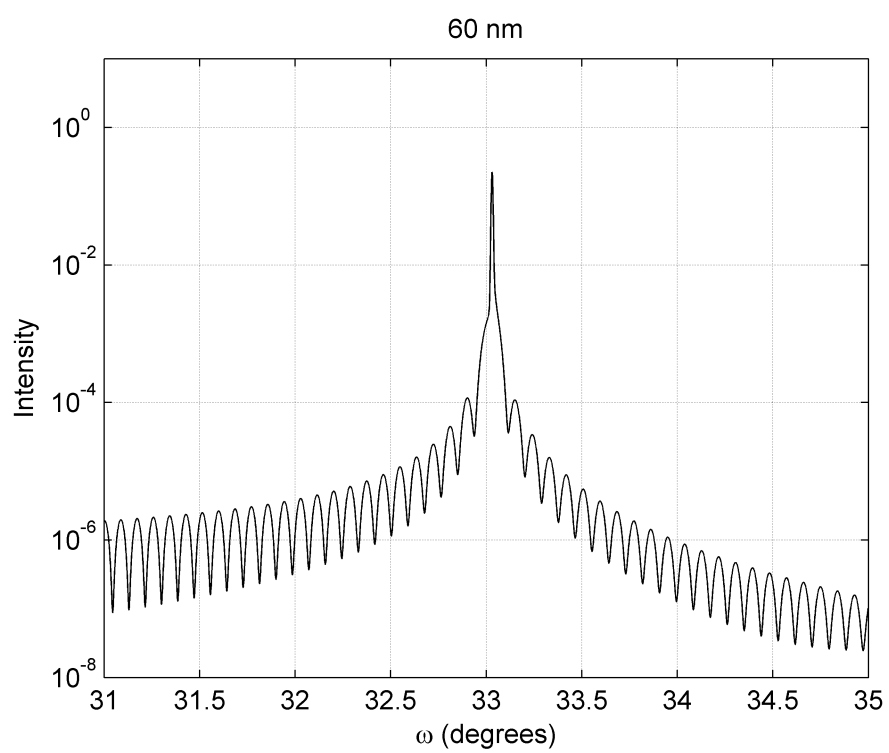


Figure 1.1

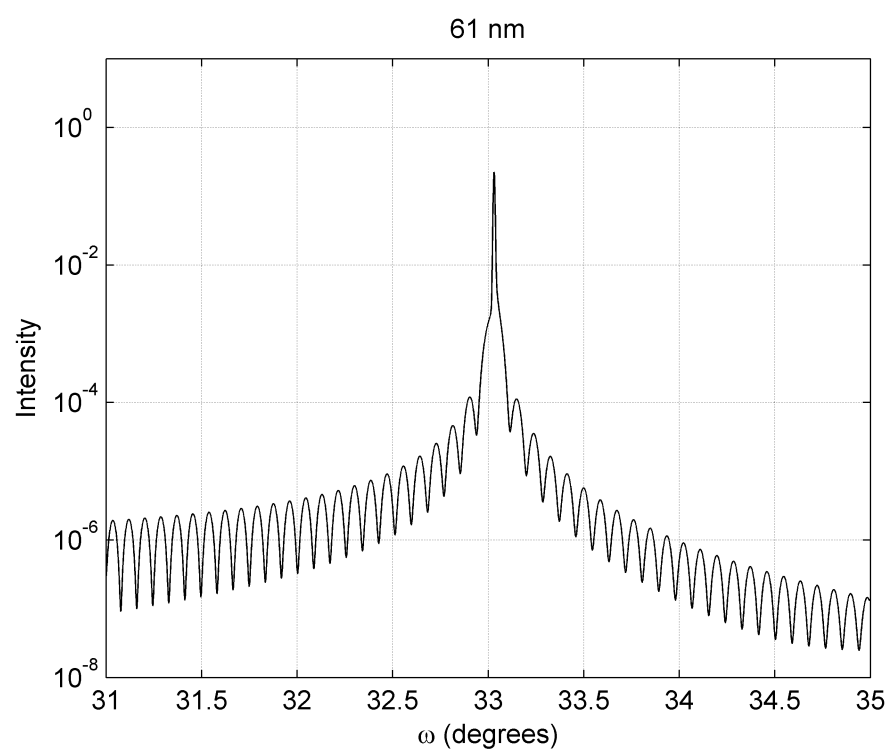


Figure 1.2

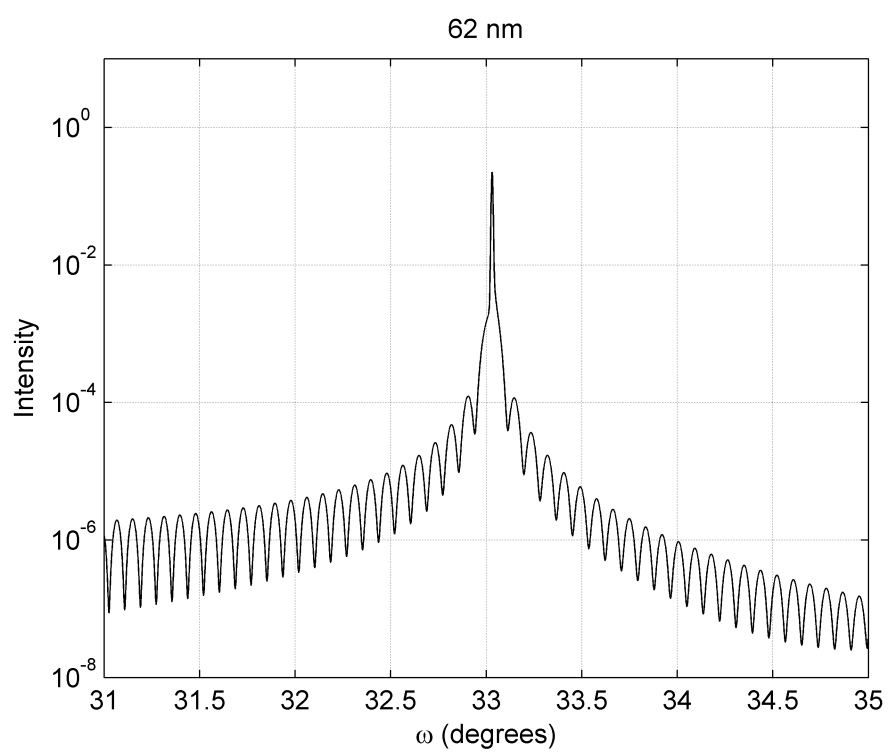


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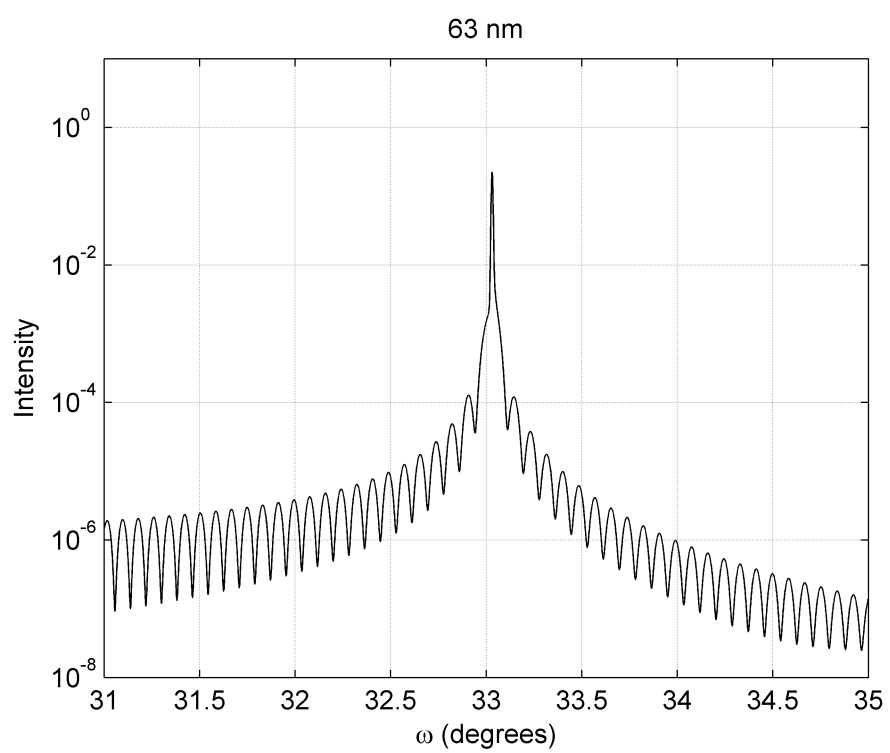


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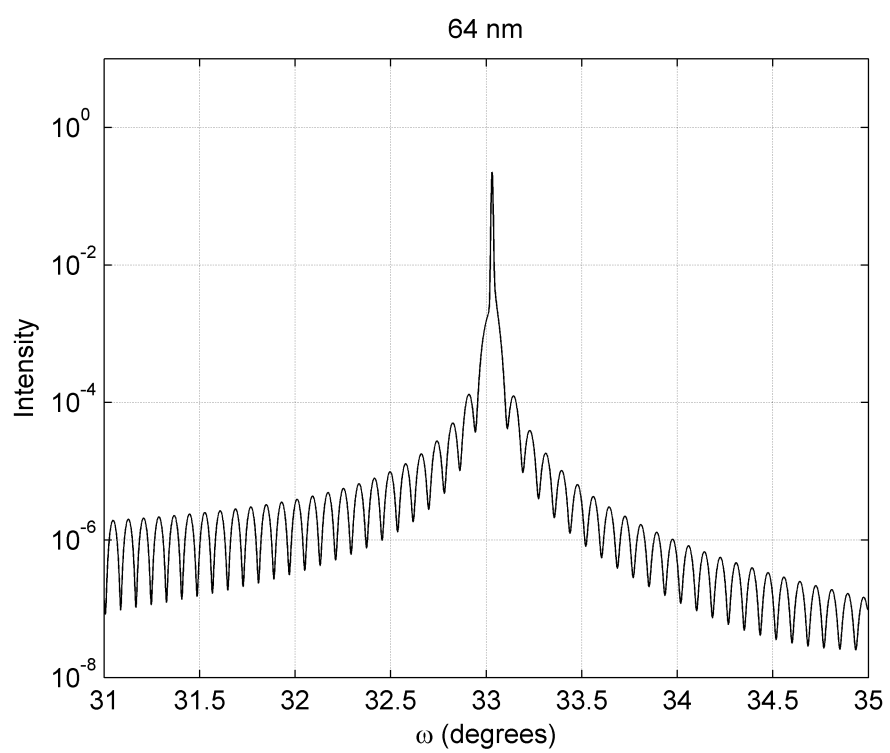


Figure 1.5

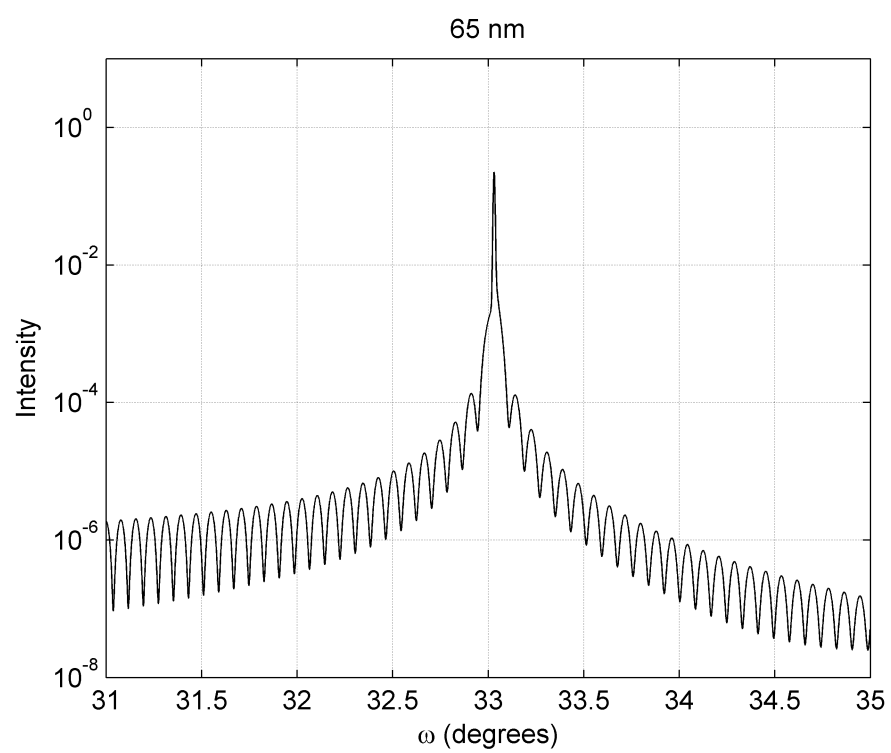


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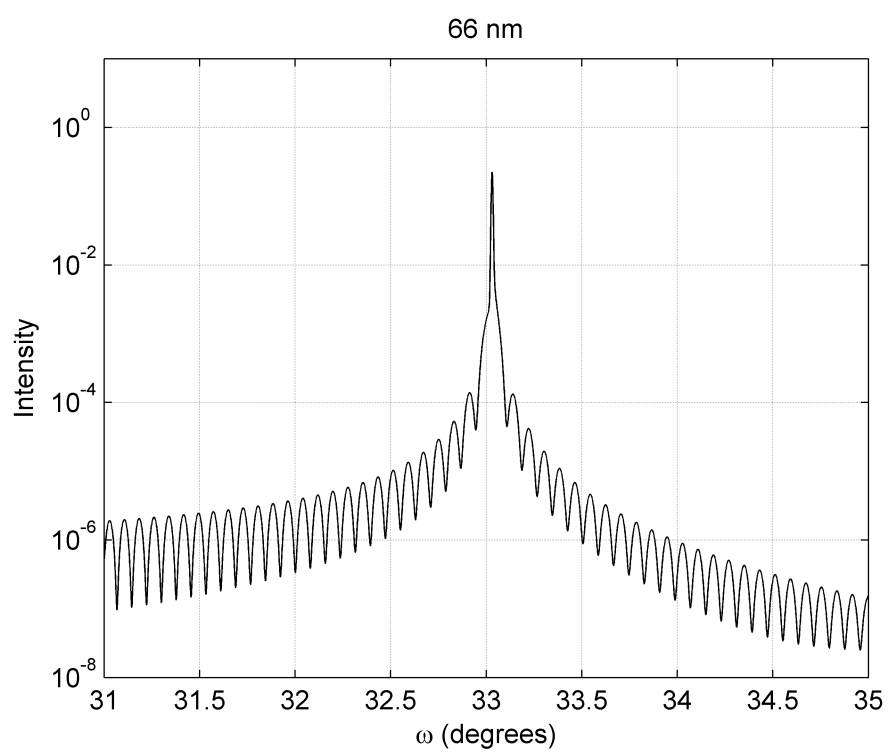


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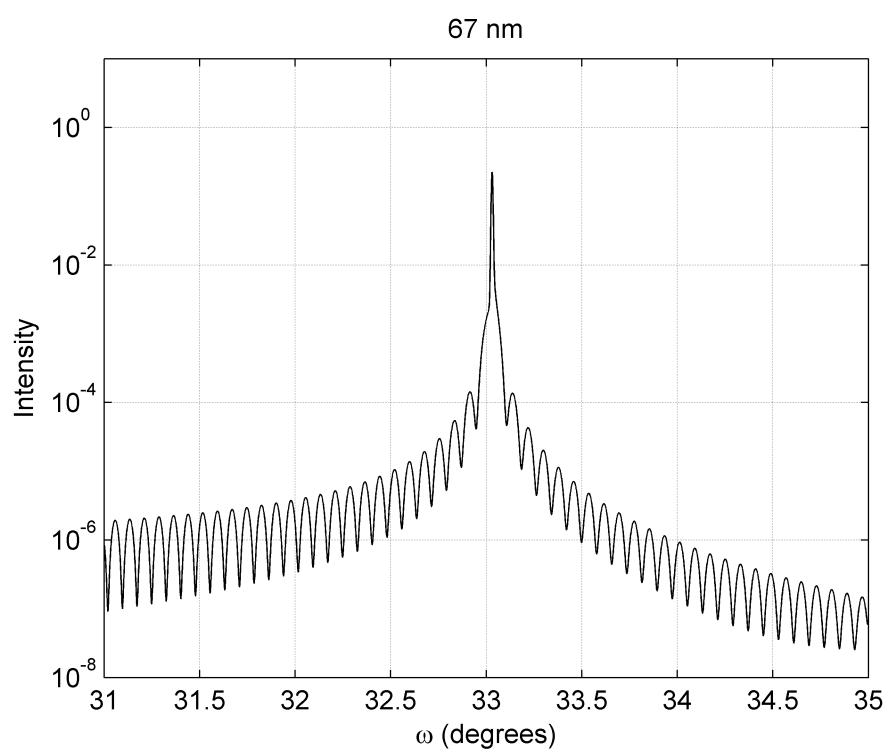


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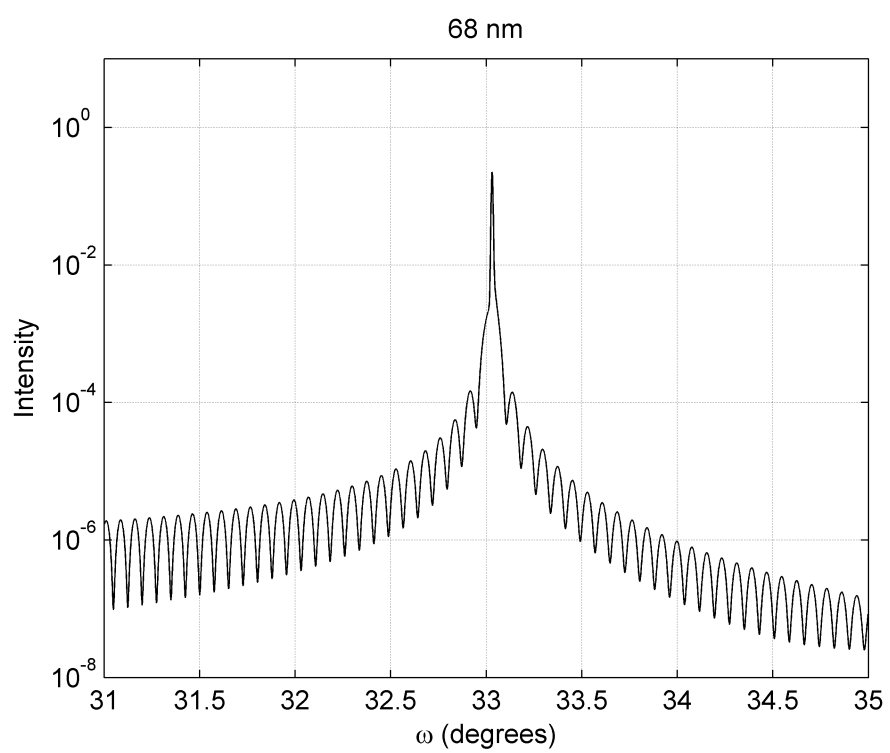


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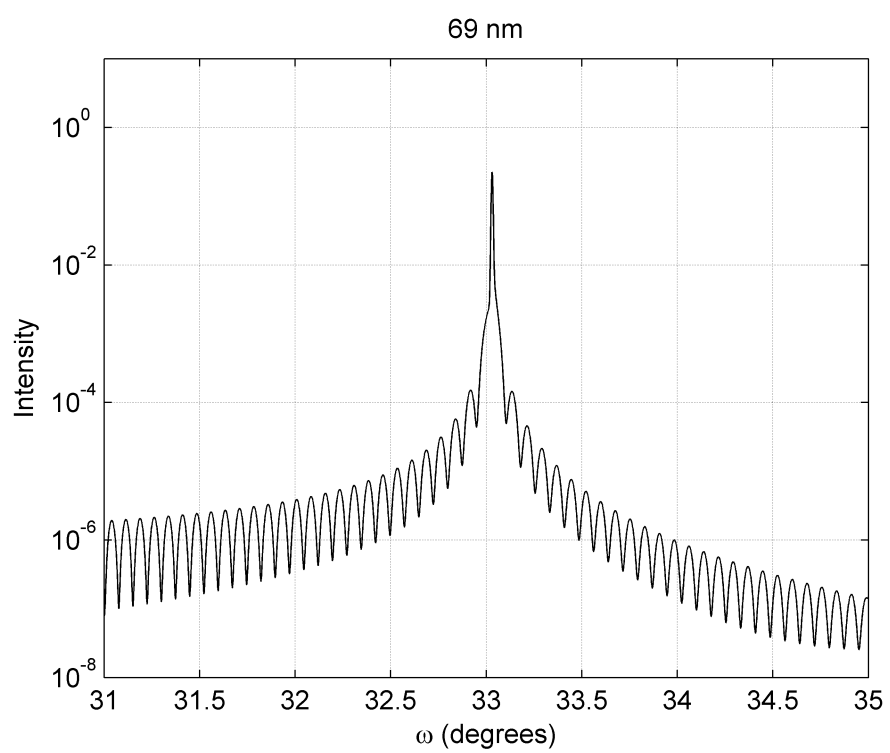


Figure 1.10

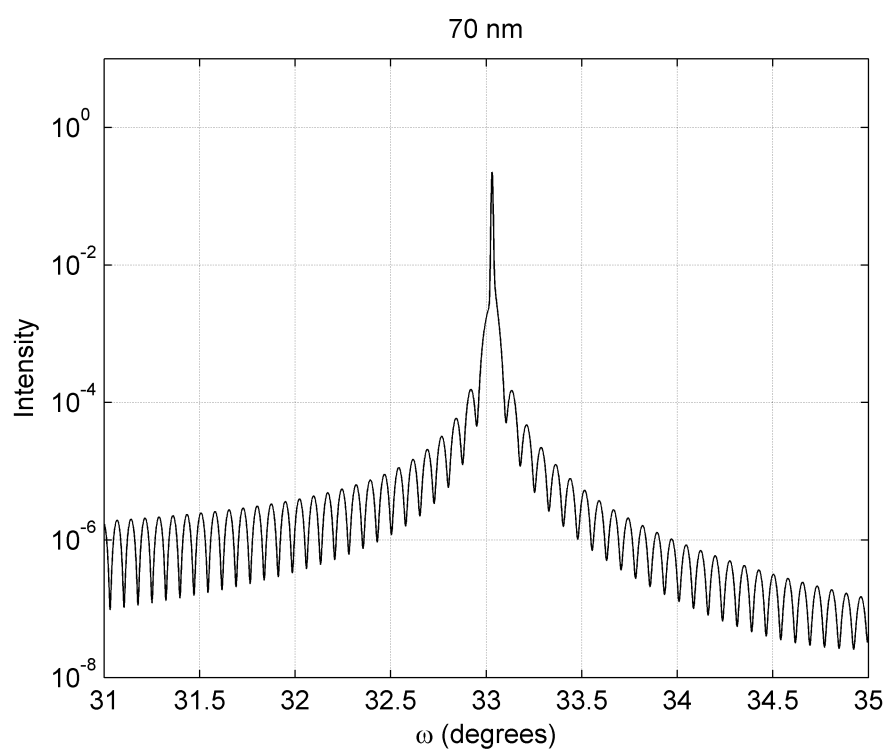


Figure 1.11

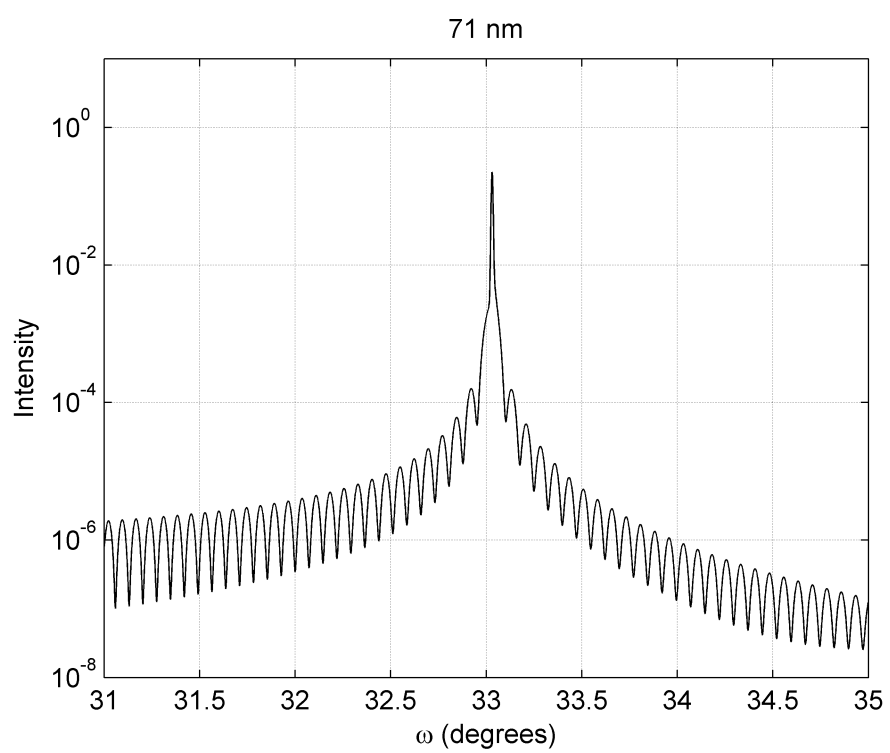


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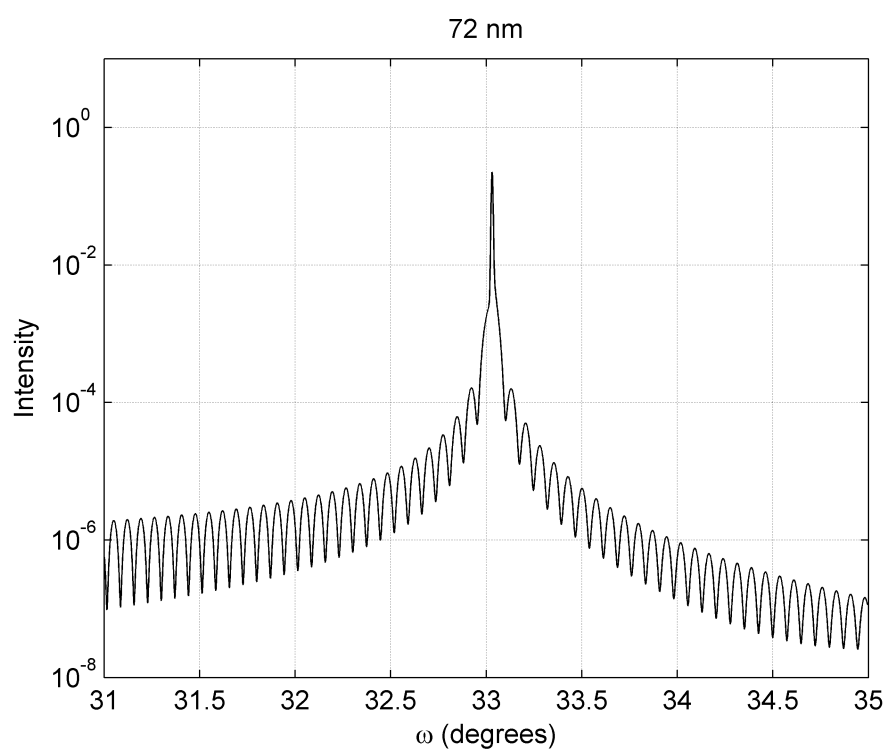


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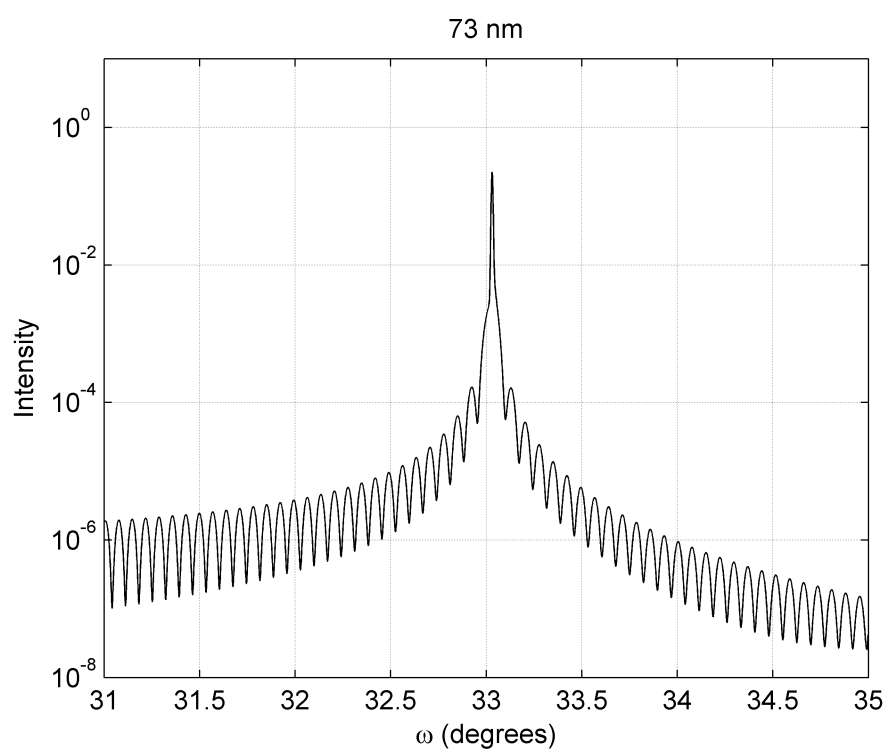


Figure 1.14

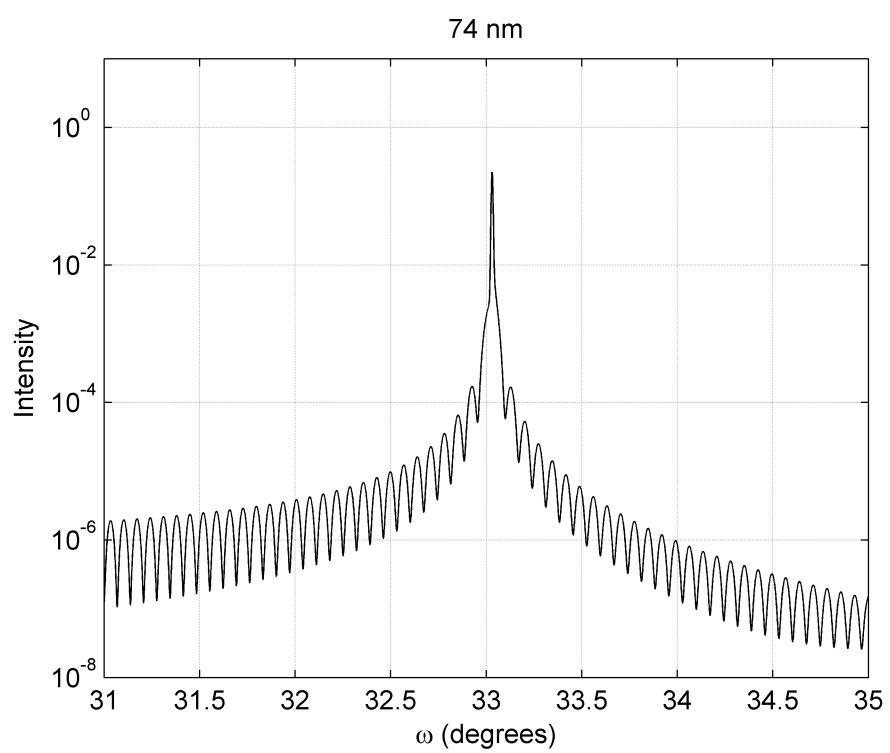


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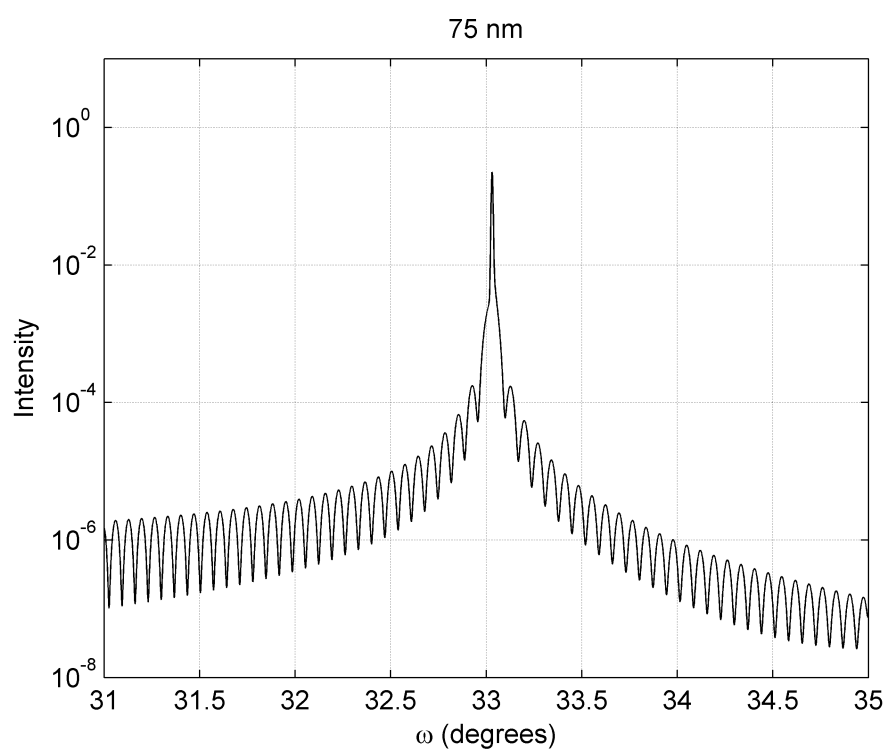


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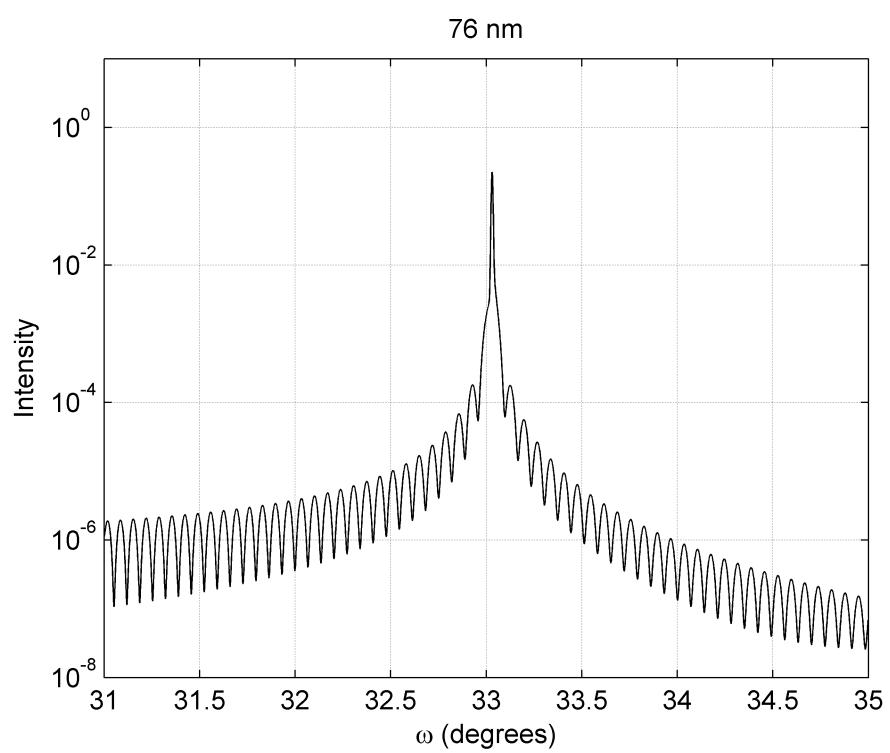


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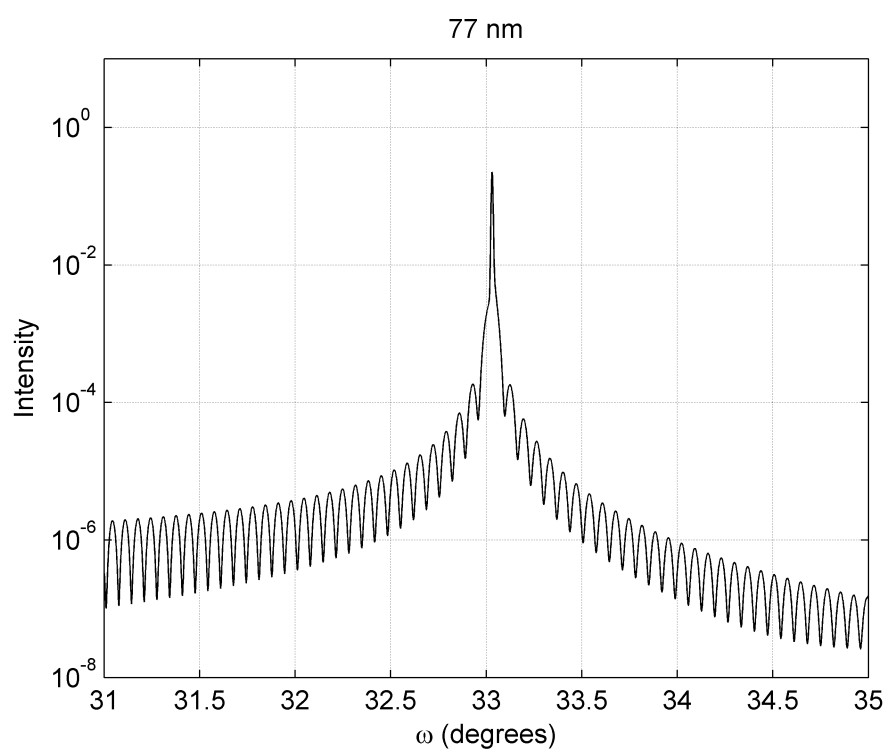


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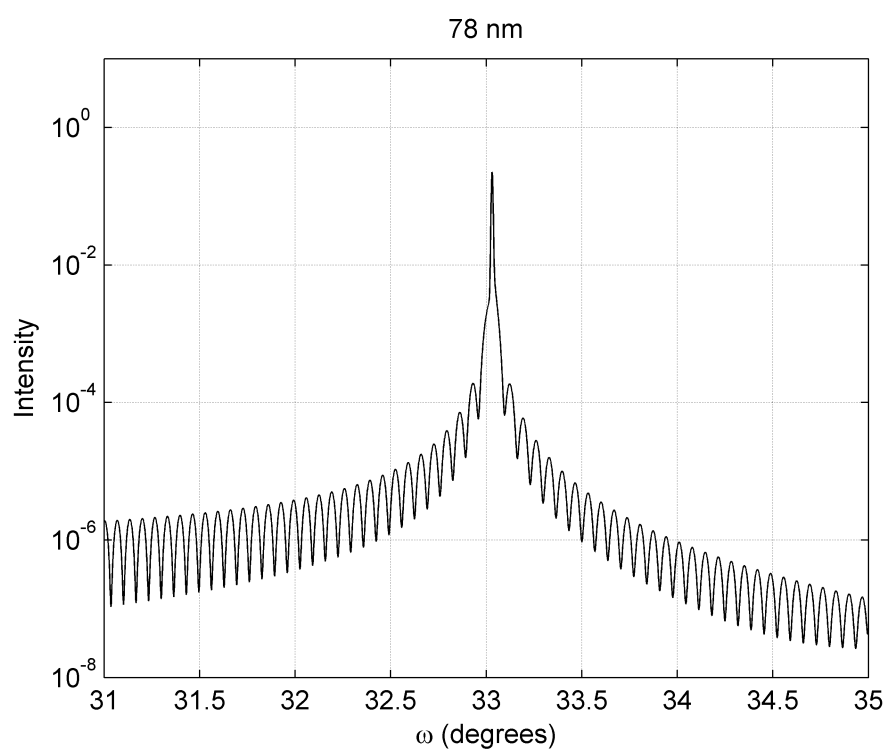


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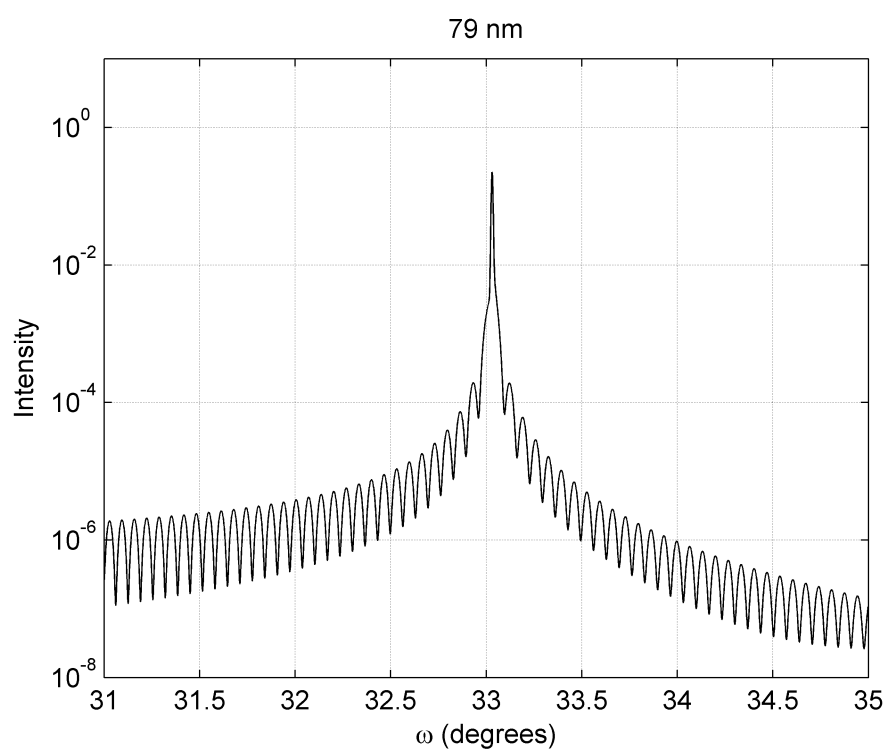


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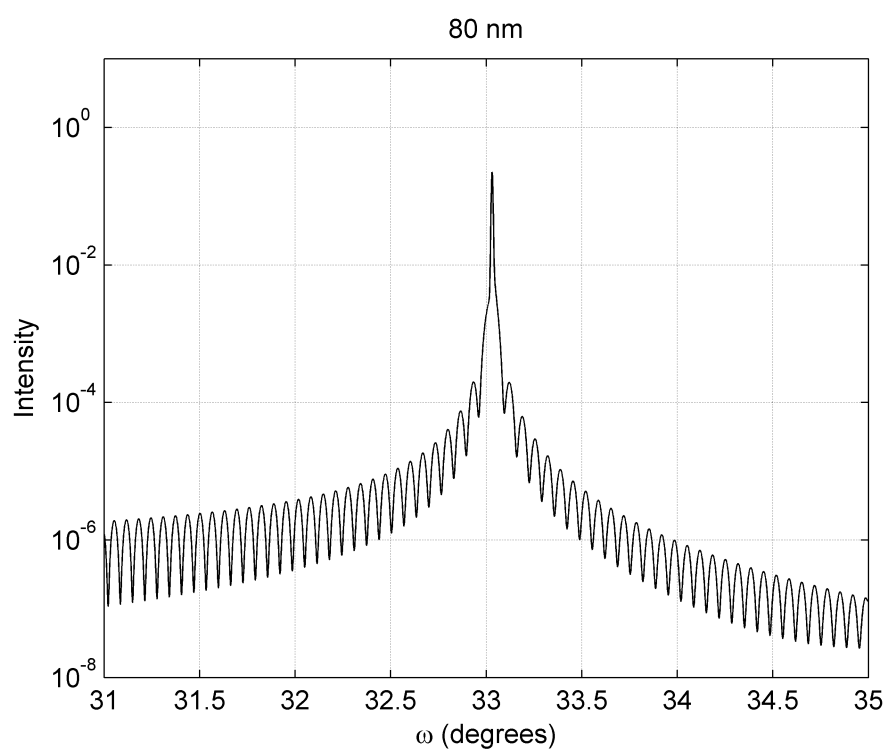


Figure 1.21

Chapter 2

InAs layer thicknesses

2.1 Bottom InAs layer

Simulations with varying InAs bottom layer thickness and a constant top layer thickness of 2 monolayers are shown in figures 2.1–2.41. They are arranged by increasing bottom layer thickness from 0 to 4 monolayers.

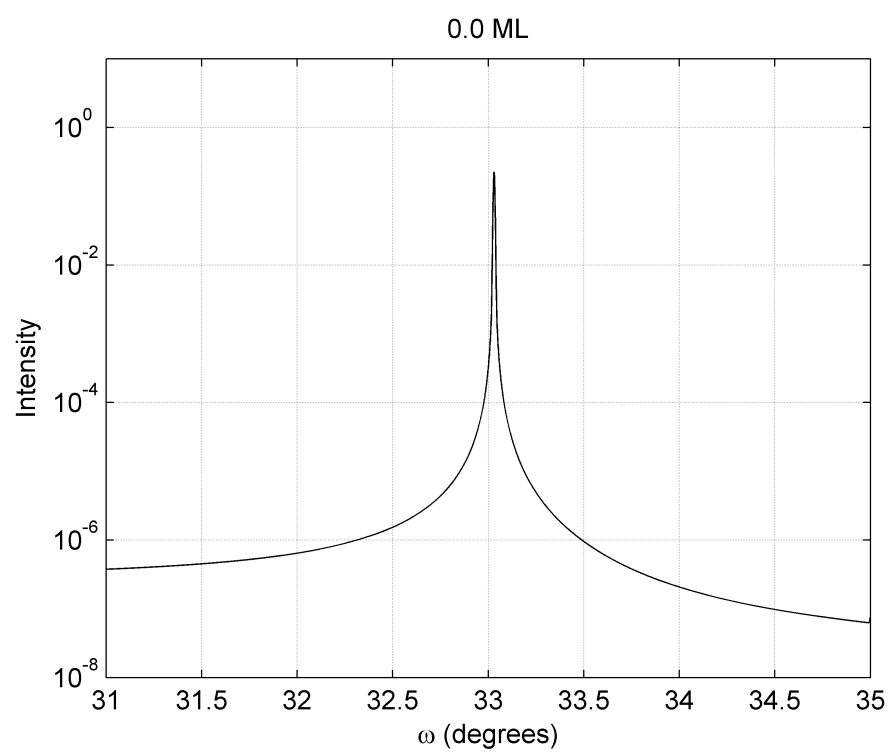


Figure 2.1

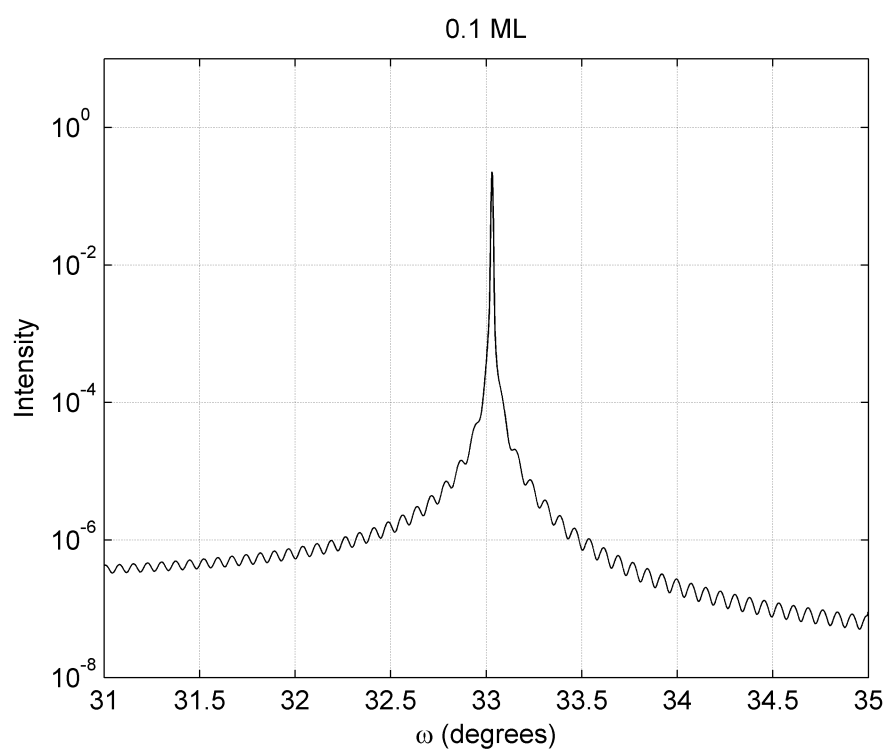


Figure 2.2

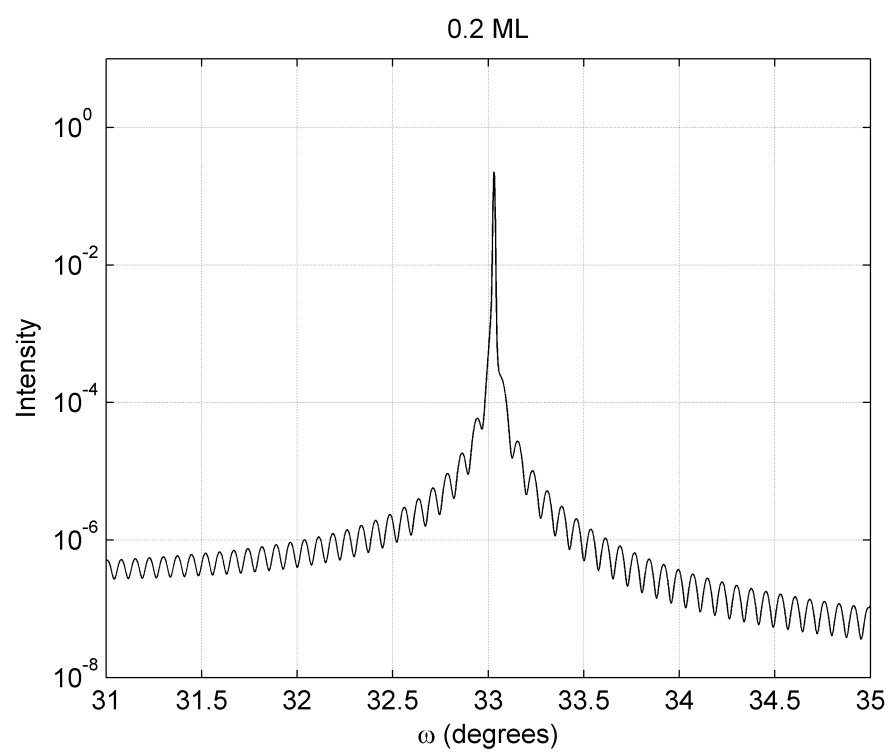


Figure 2.3

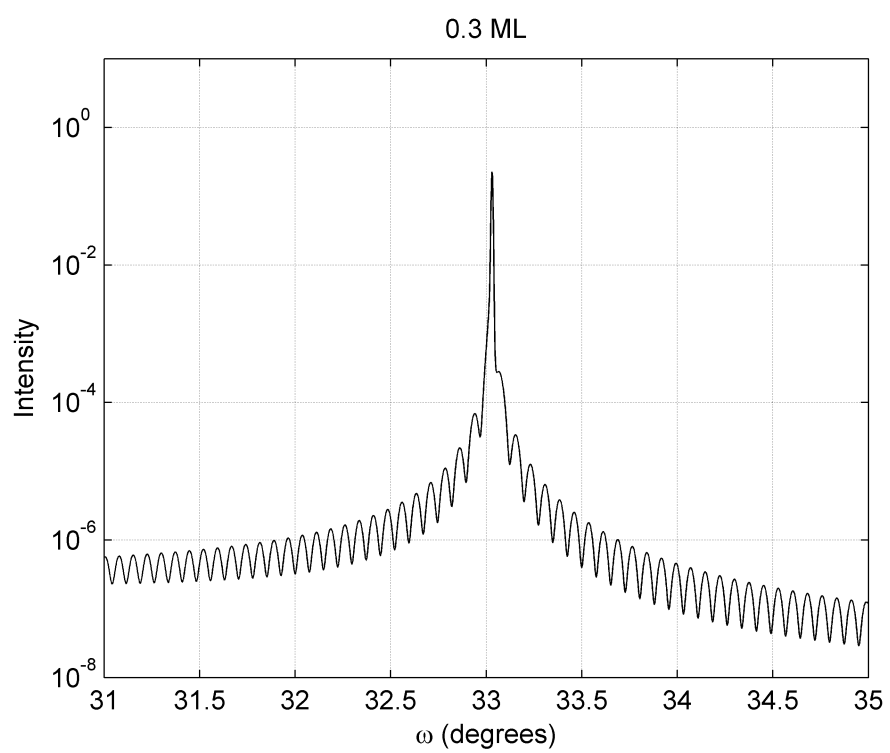


Figure 2.4

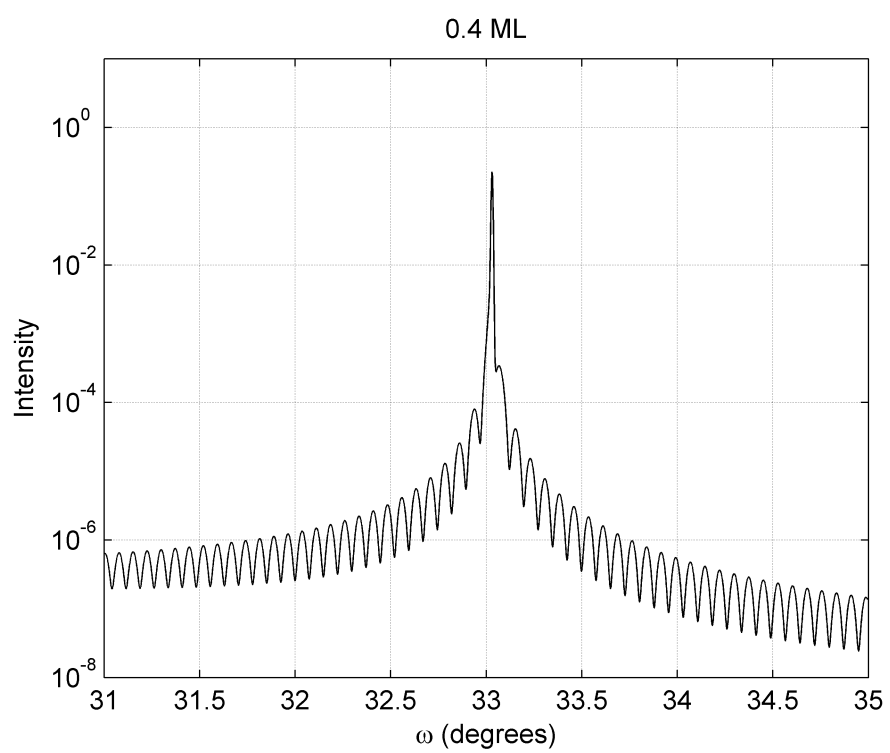


Figure 2.5

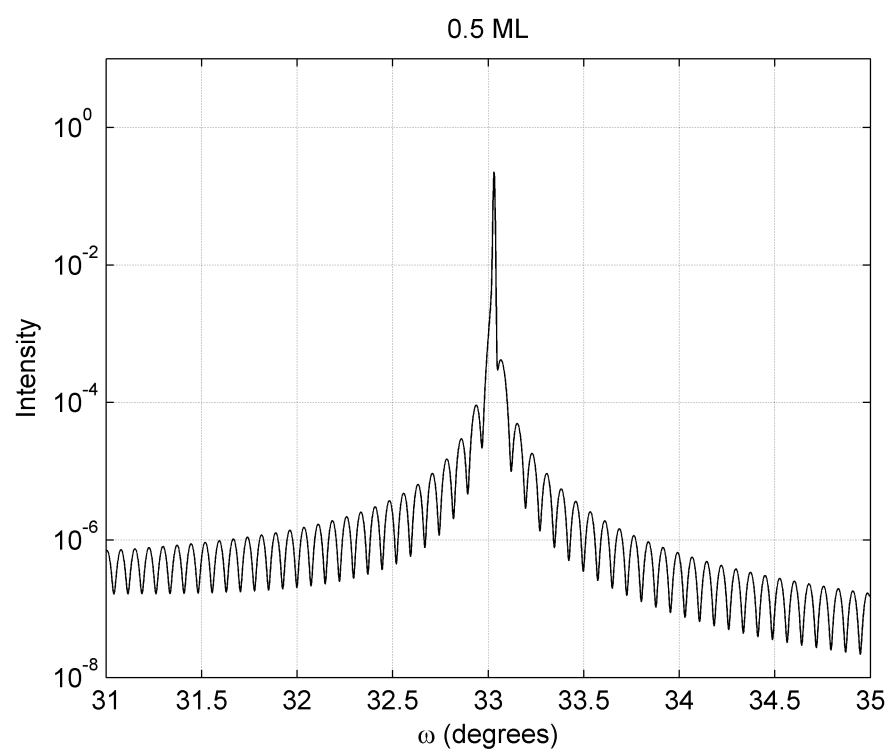


Figure 2.6

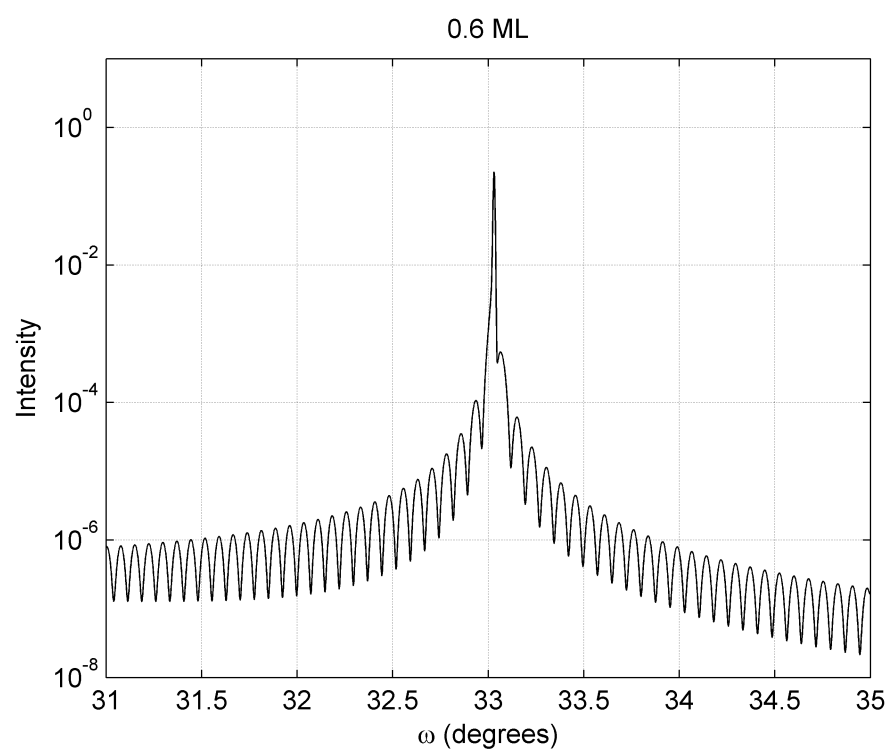


Figure 2.7

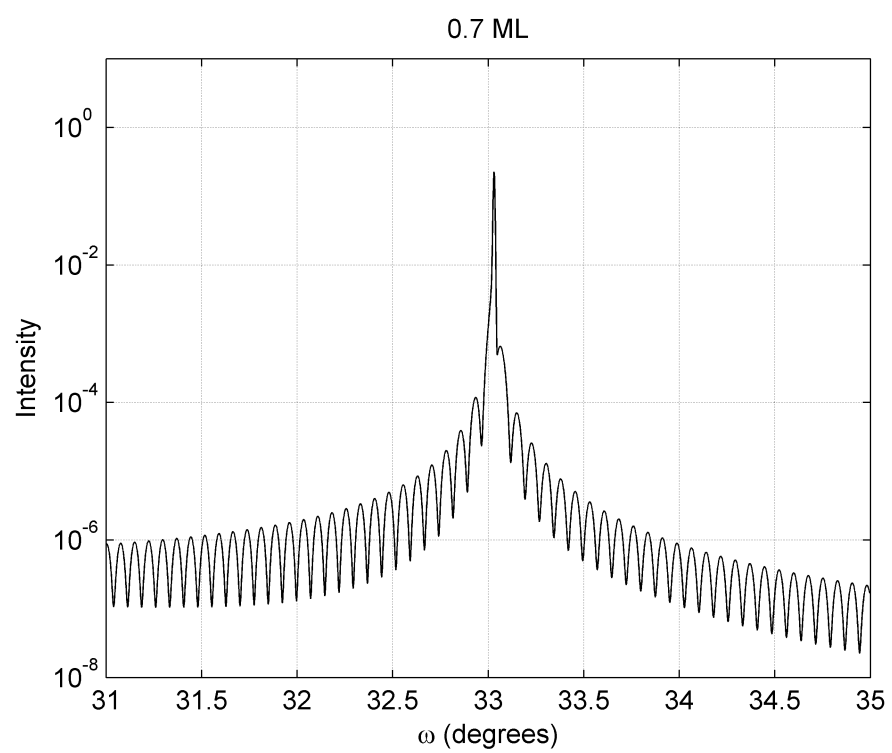


Figure 2.8

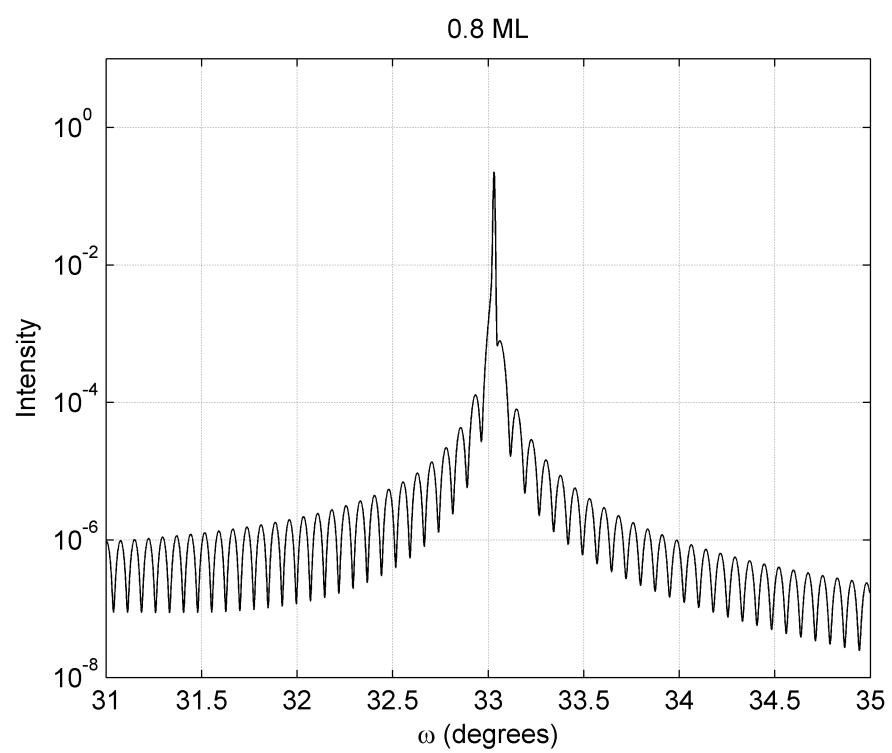


Figure 2.9

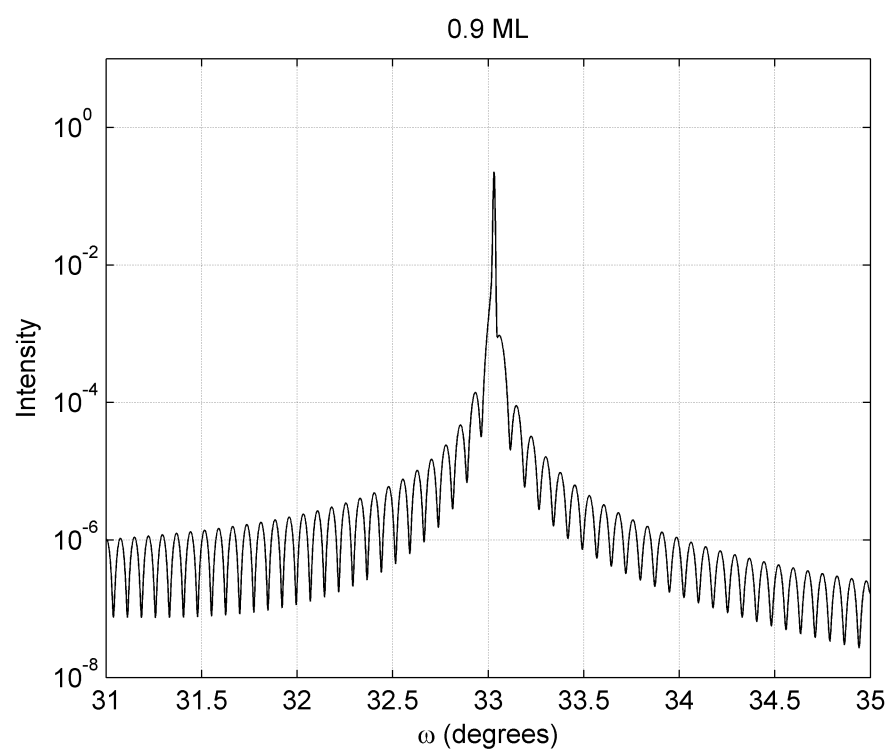


Figure 2.10

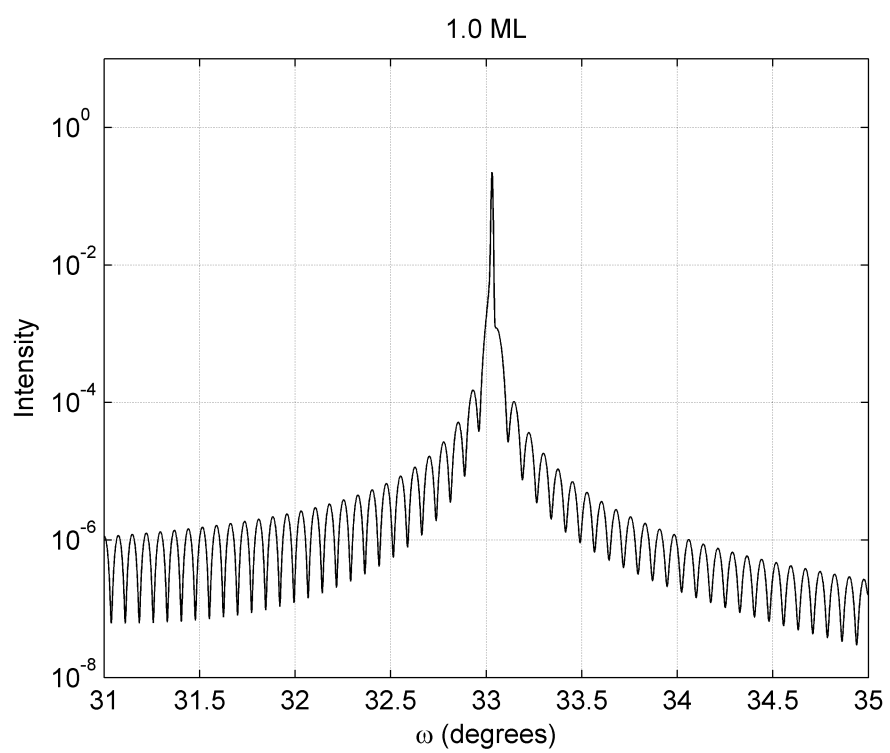


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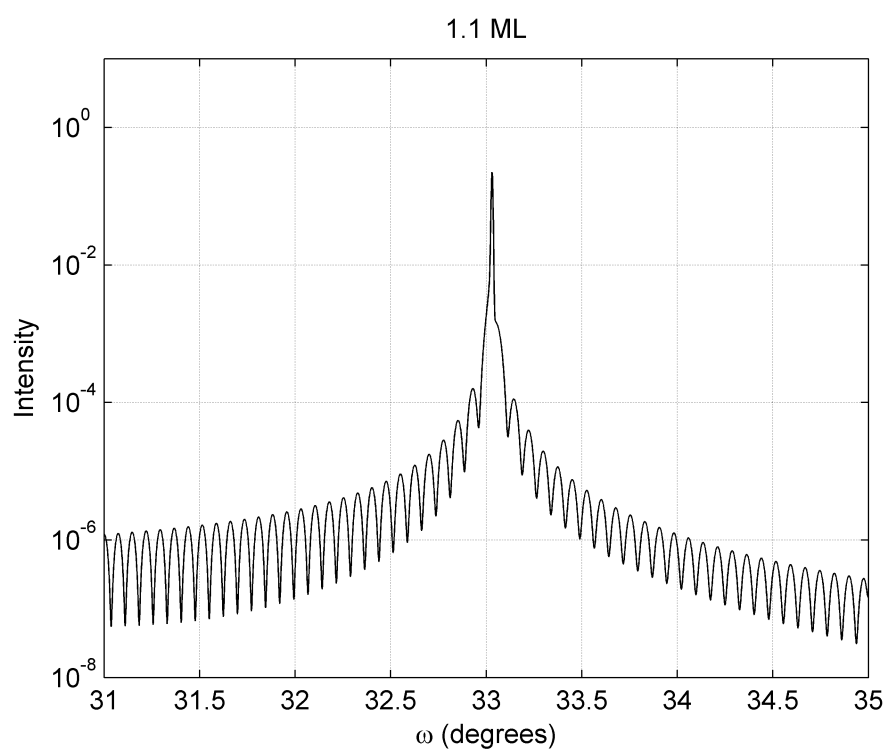


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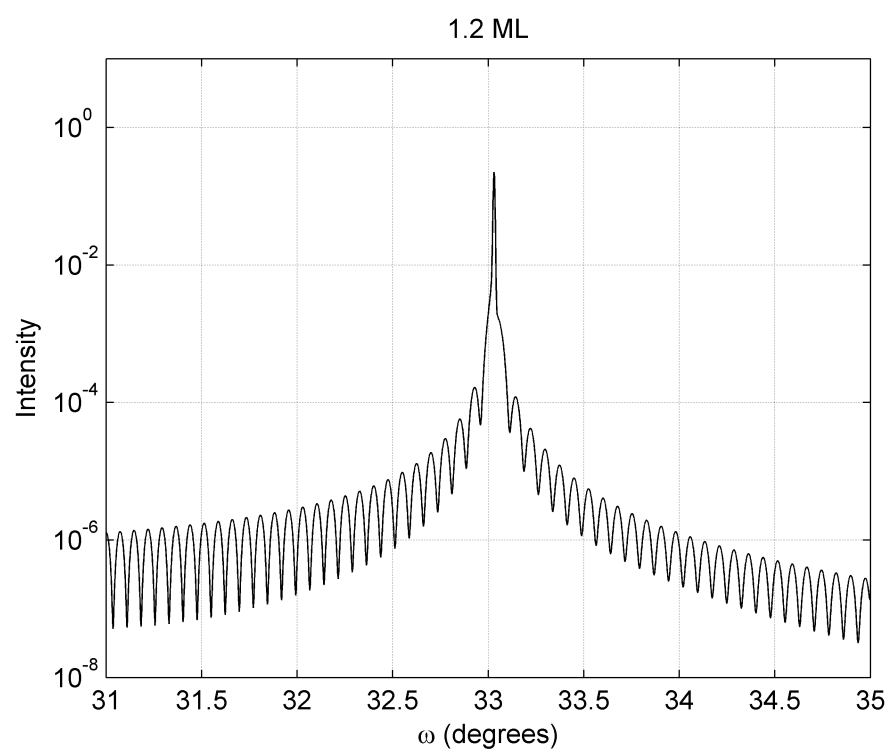


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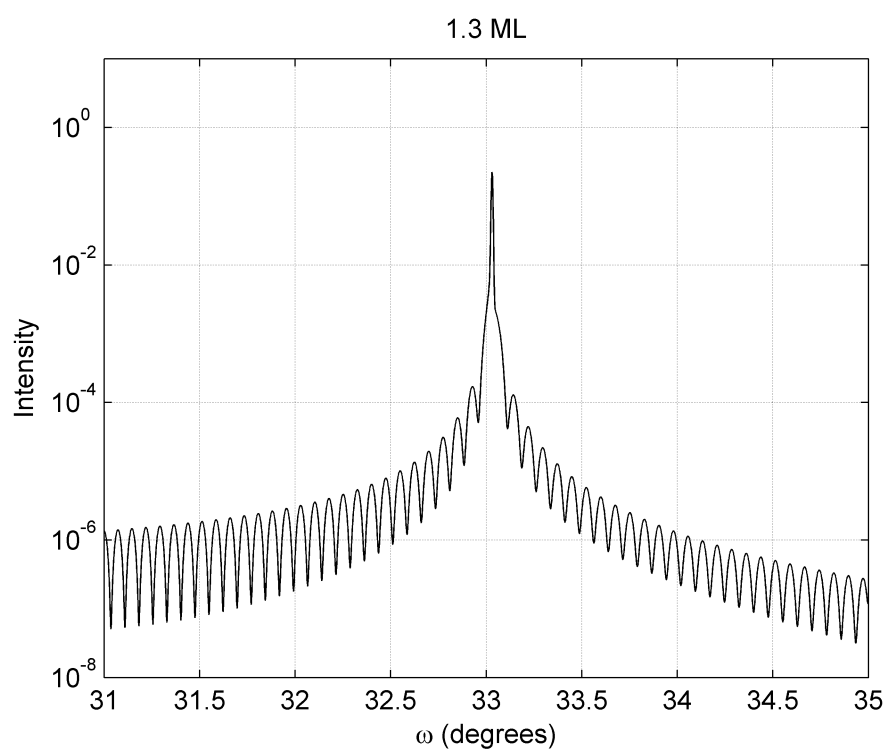


Figure 2.14

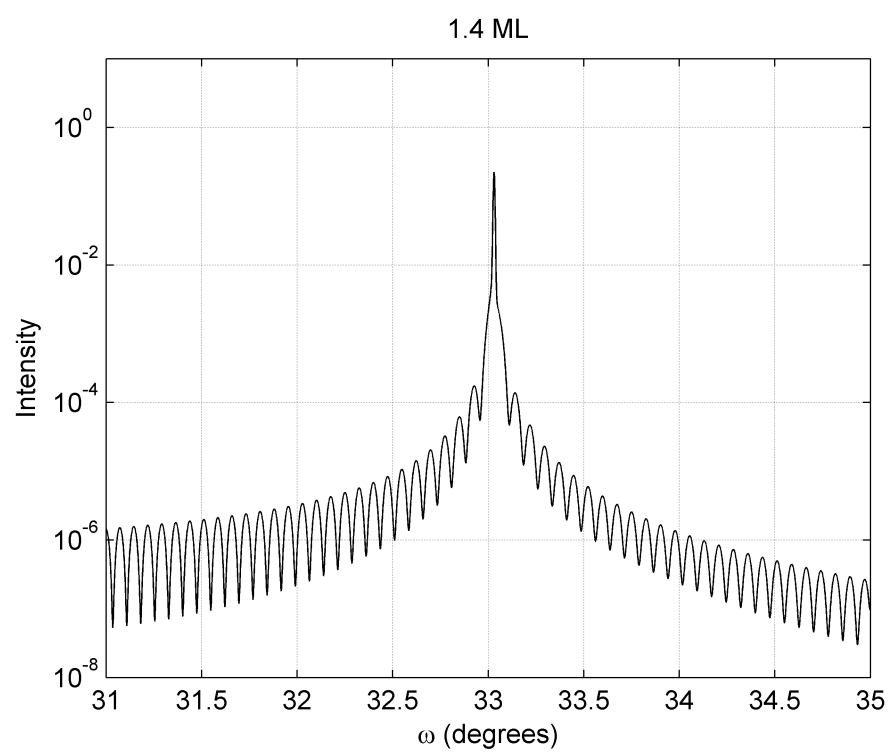


Figure 2.15

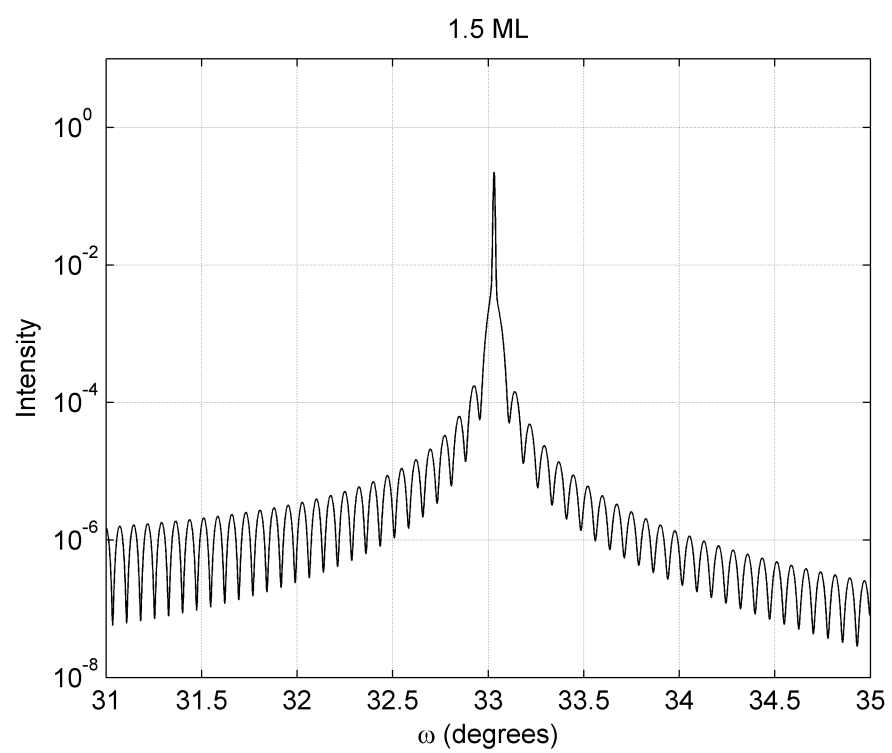


Figure 2.16

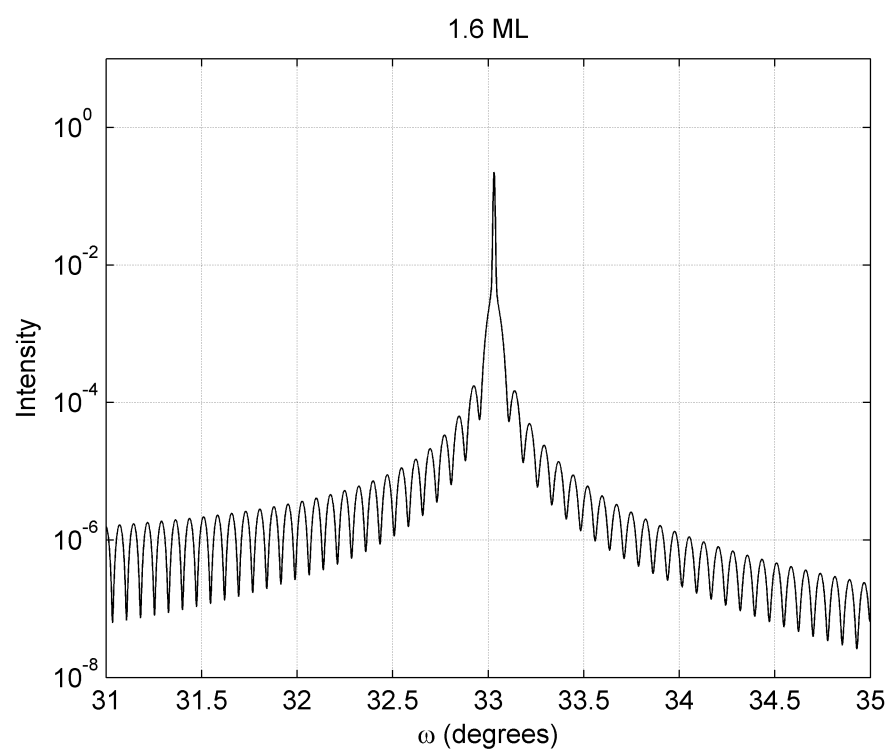


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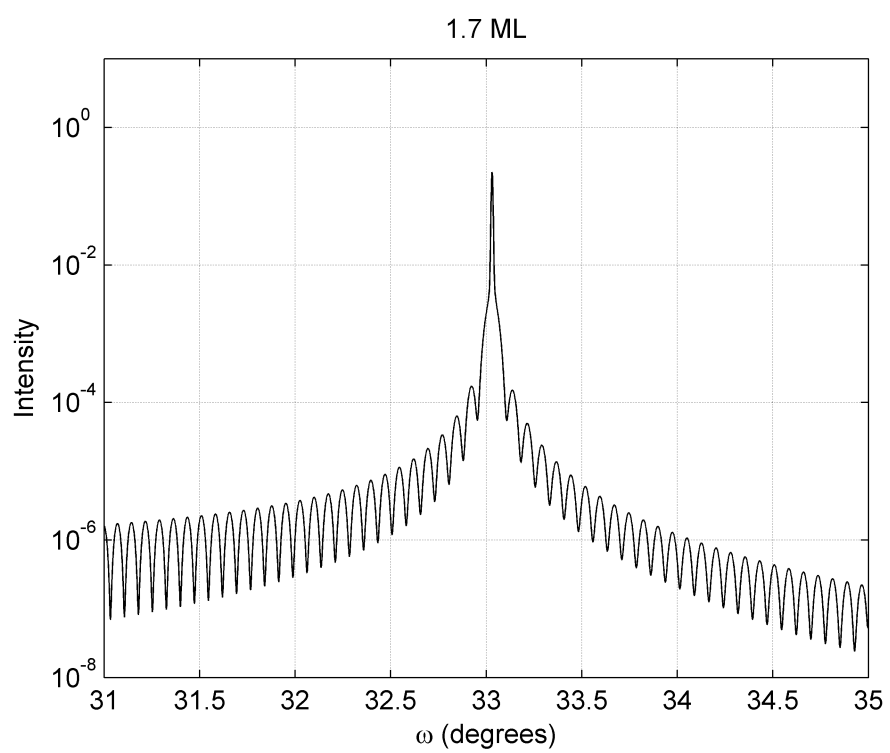


Figure 2.18

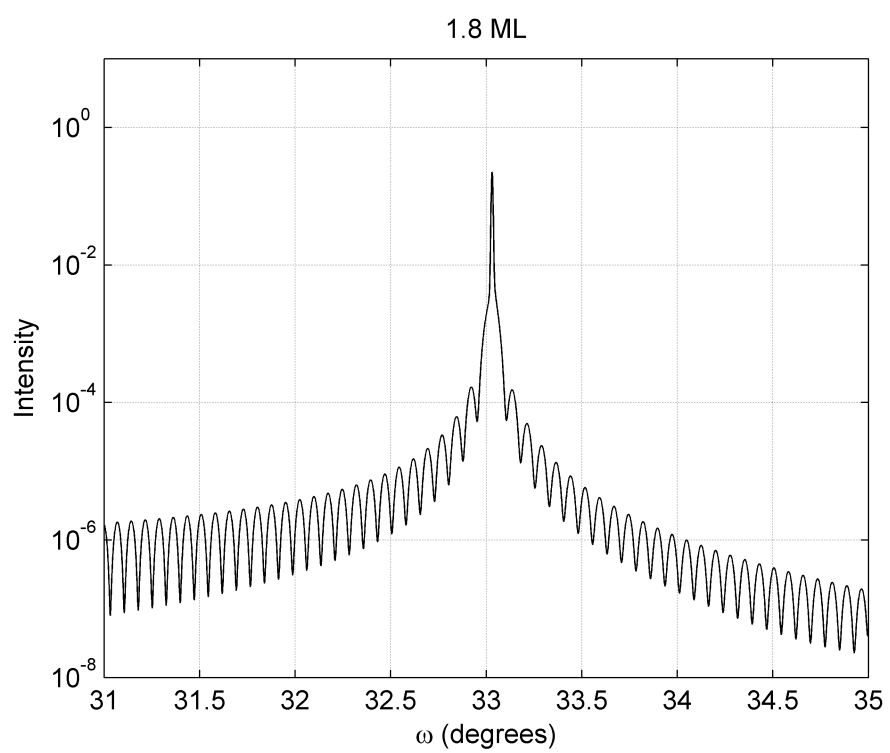


Figure 2.19

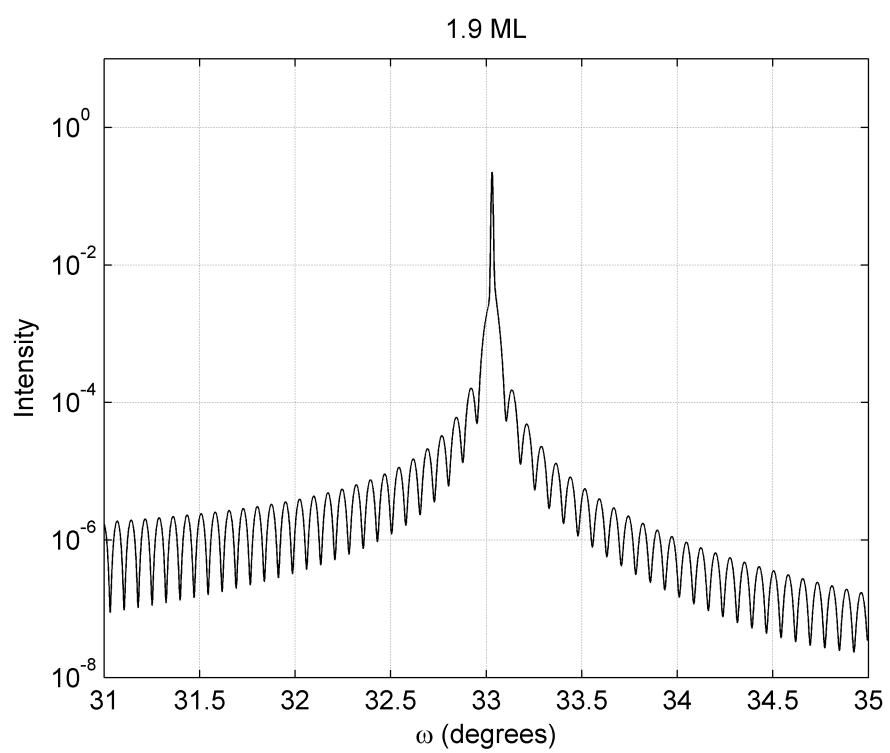


Figure 2.20

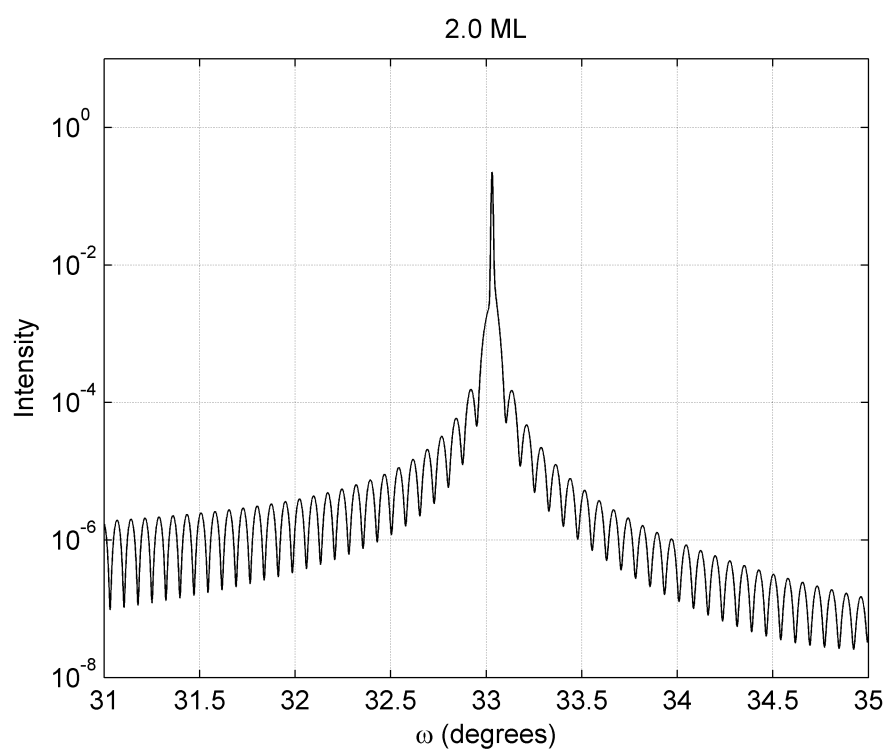


Figure 2.21

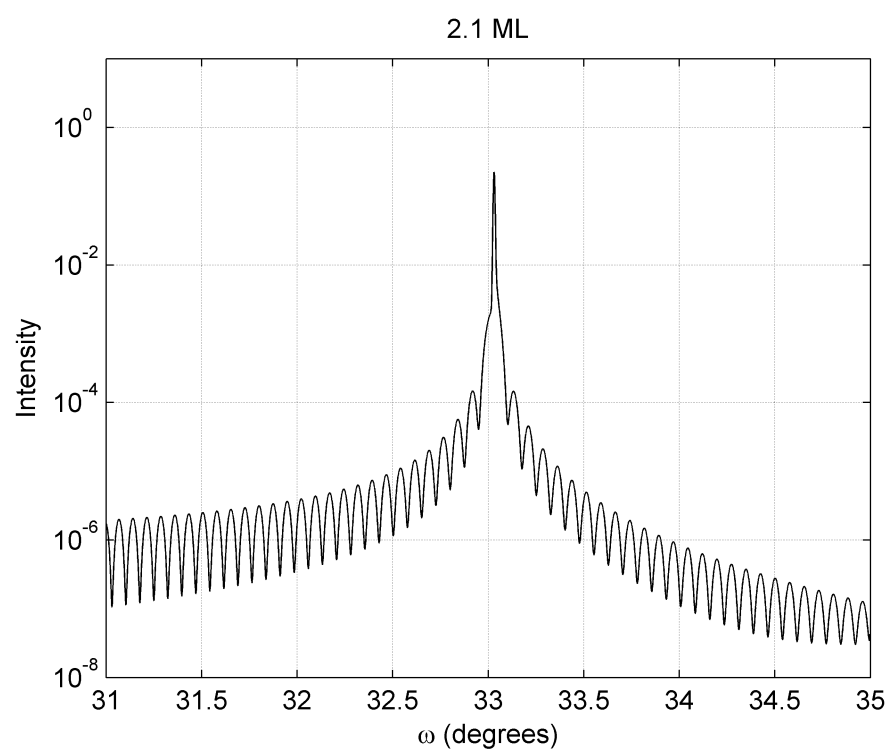


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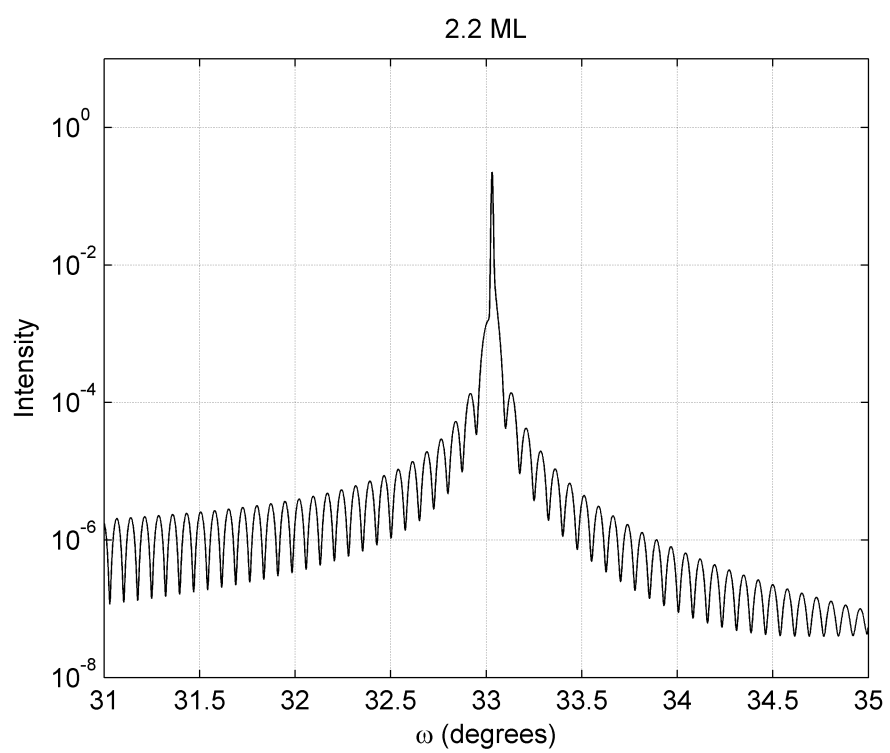


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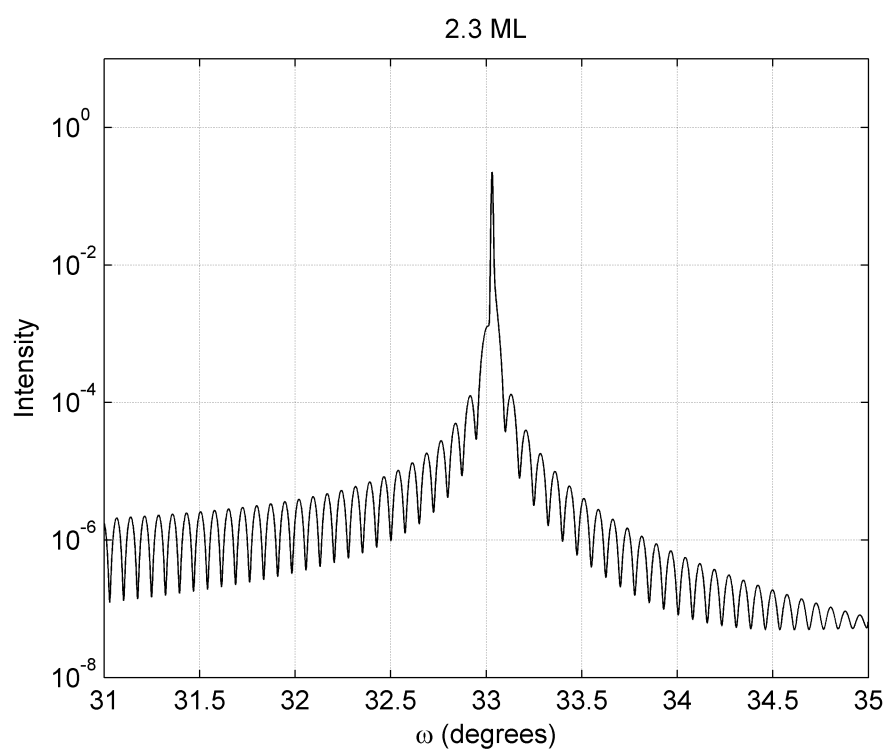


Figure 2.24

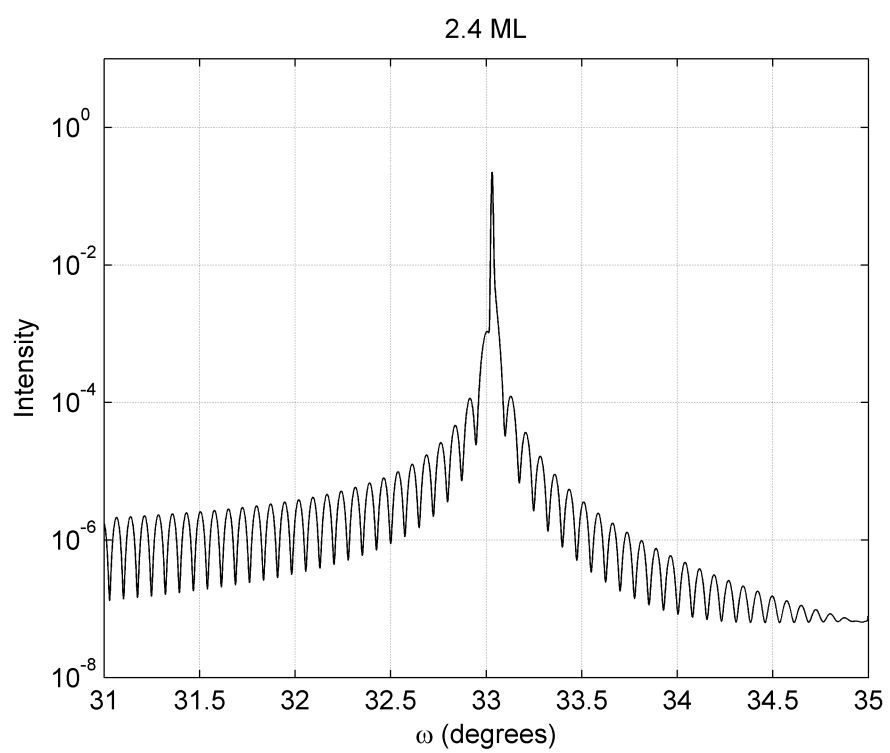


Figure 2.25

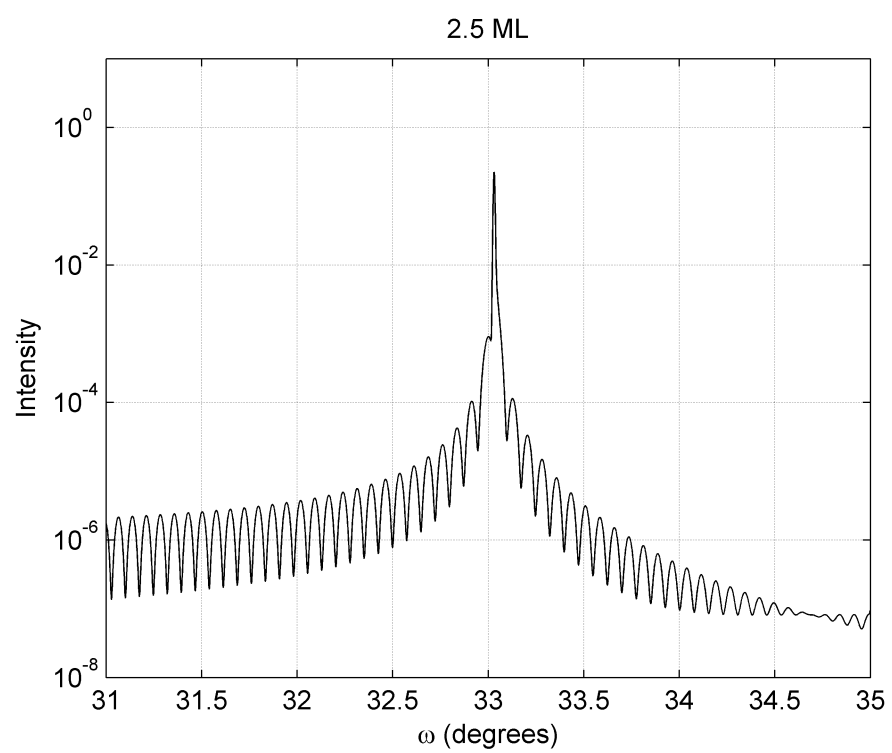


Figure 2.26

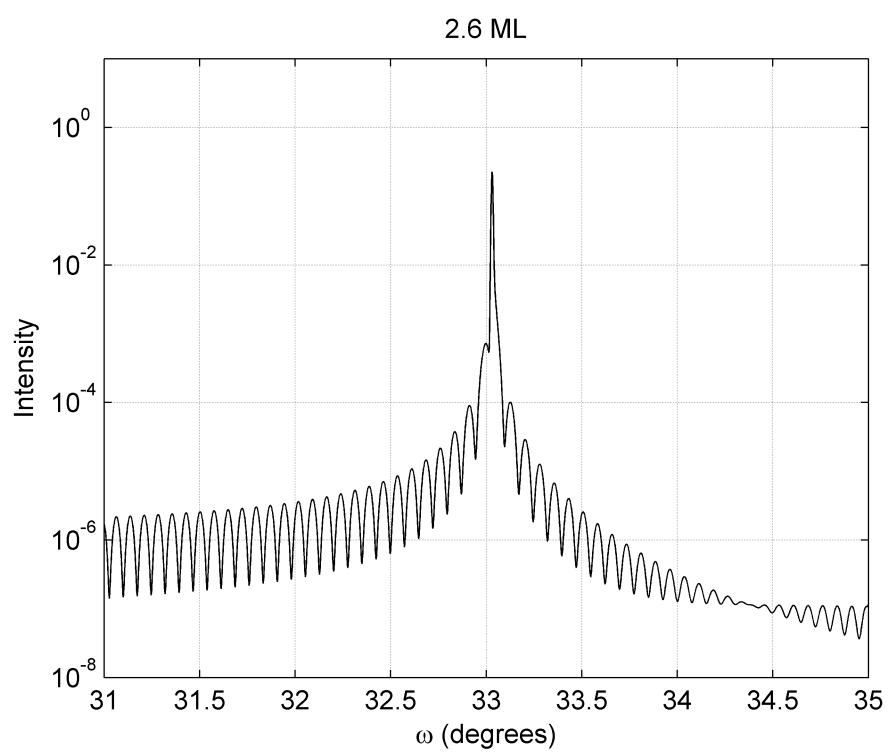


Figure 2.27

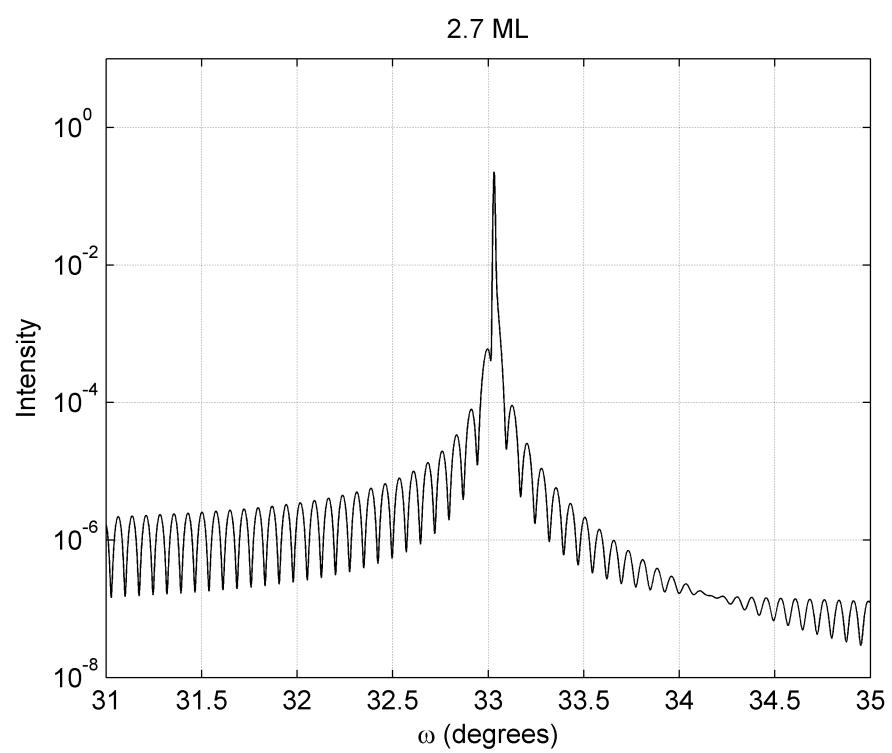


Figure 2.28

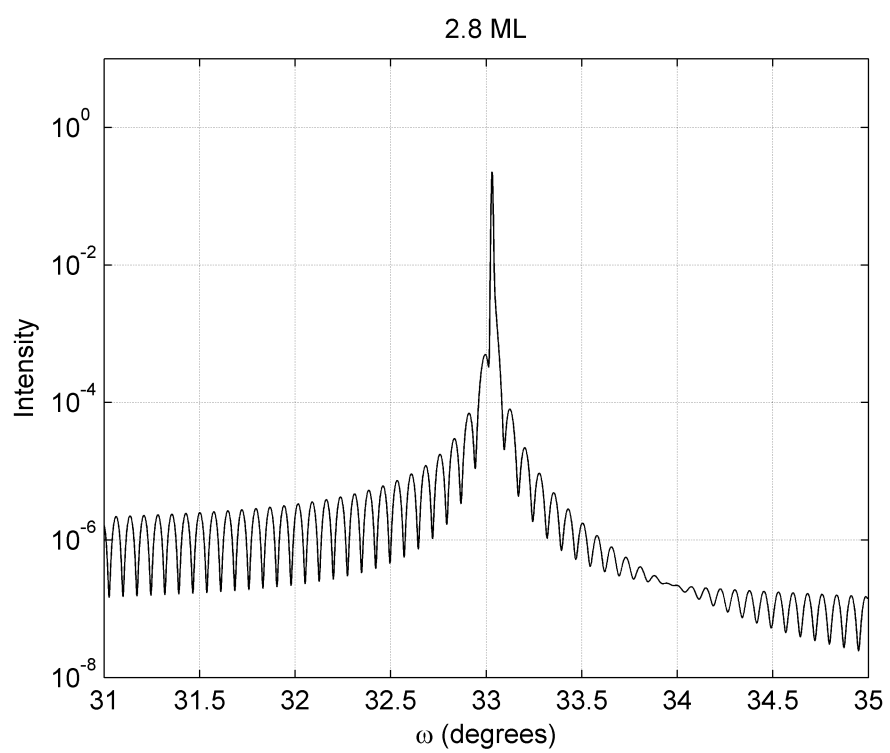


Figure 2.29

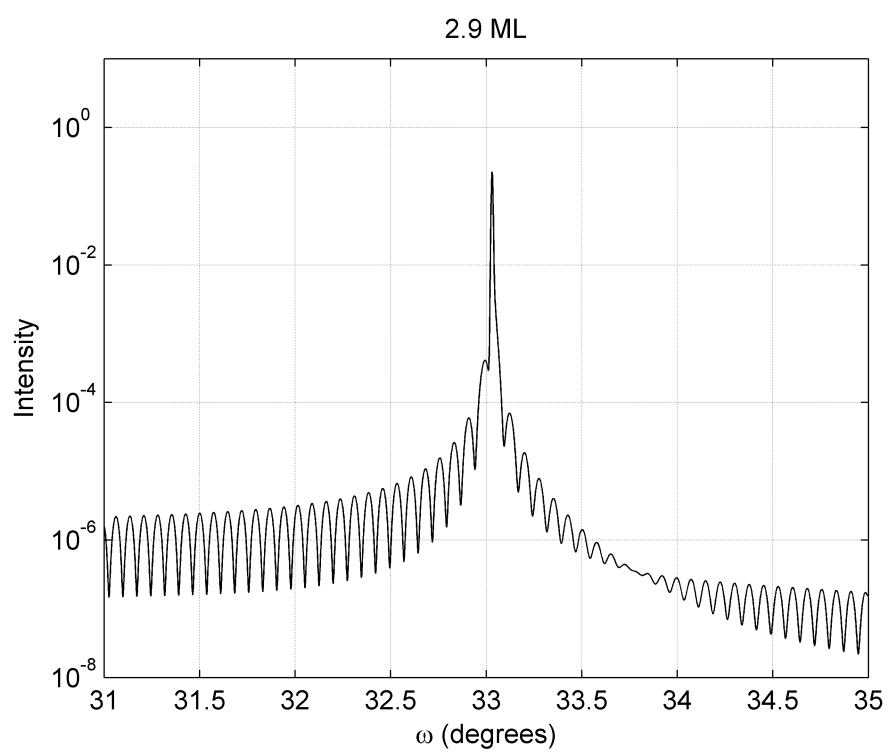


Figure 2.30

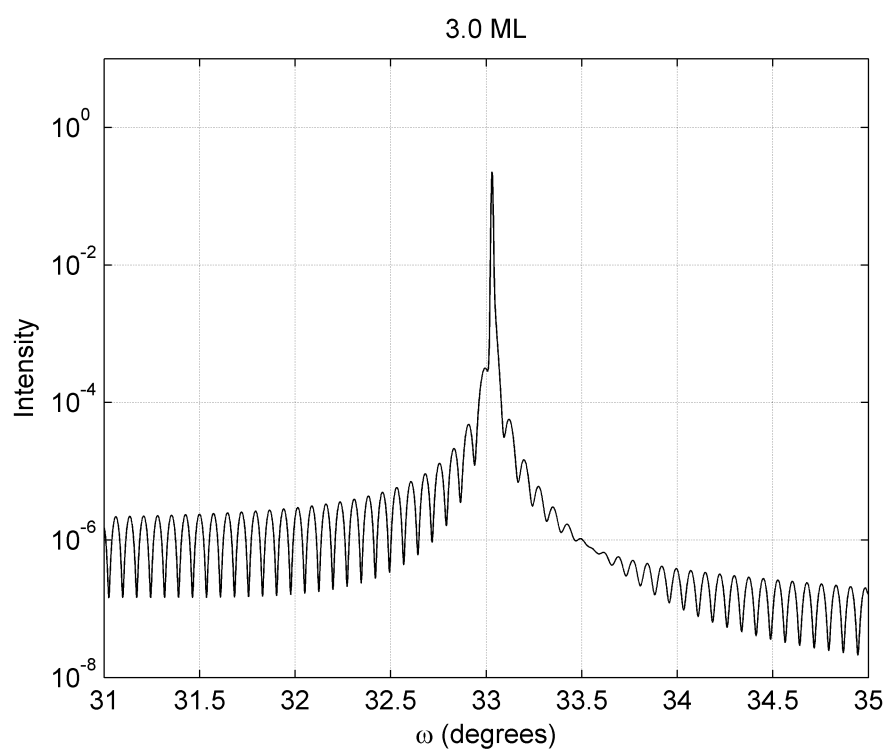


Figure 2.31

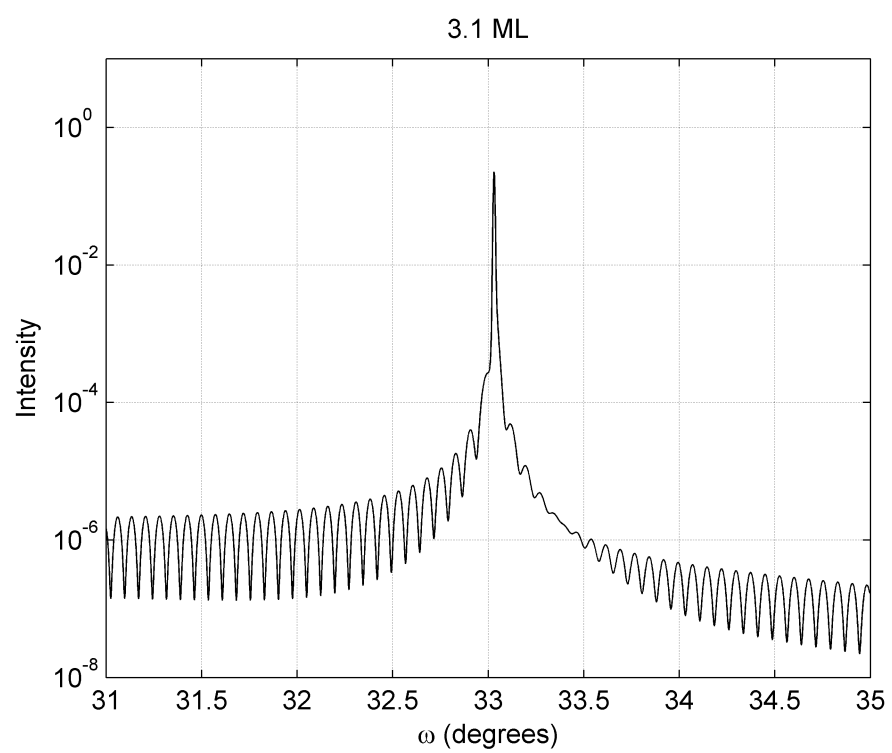


Figure 2.32

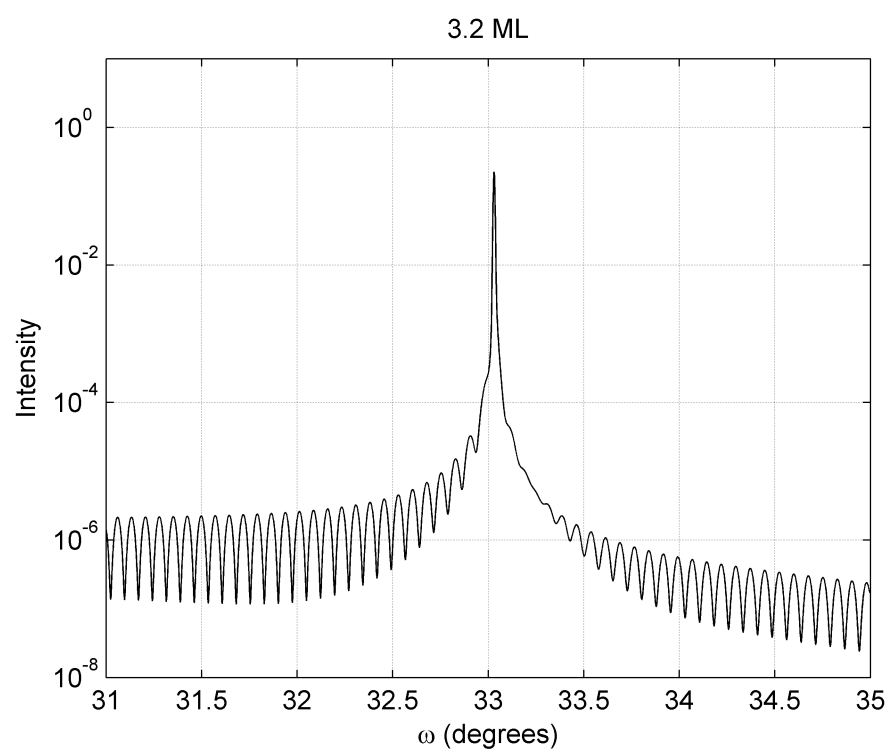


Figure 2.33

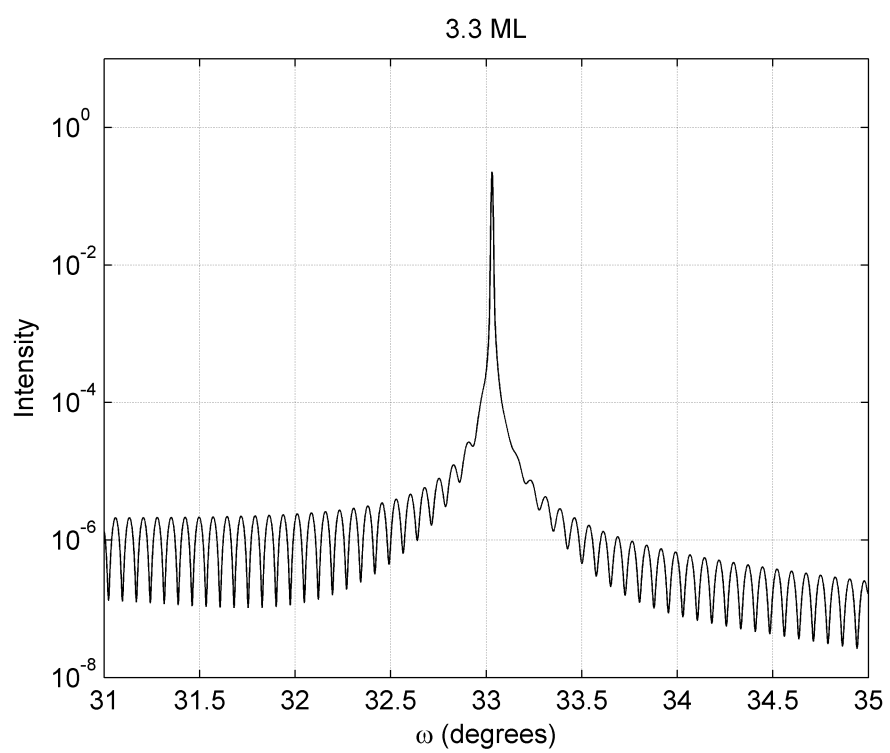


Figure 2.34

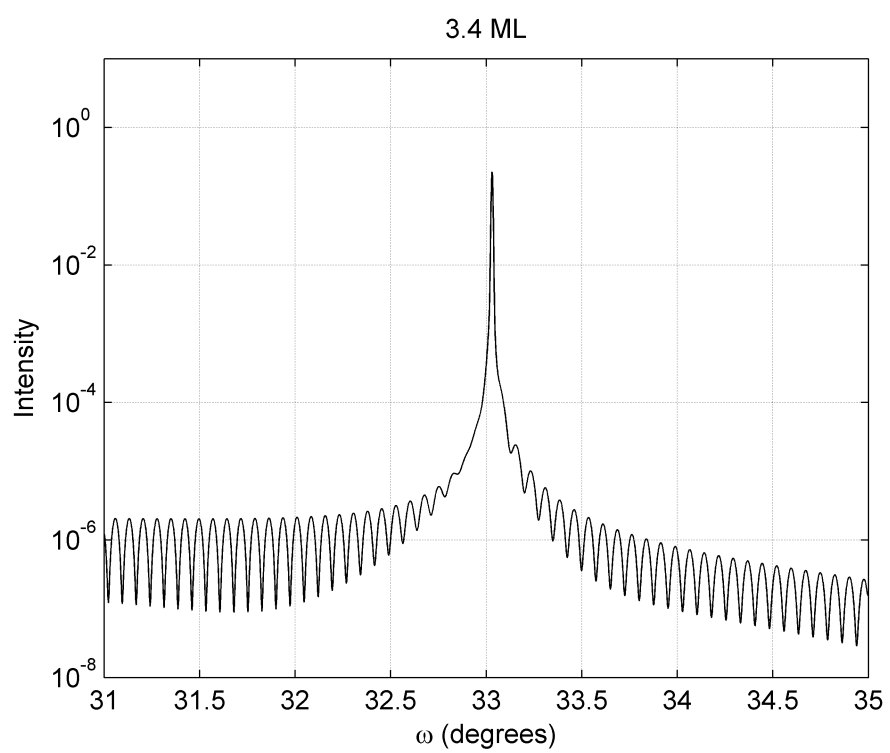


Figure 2.35

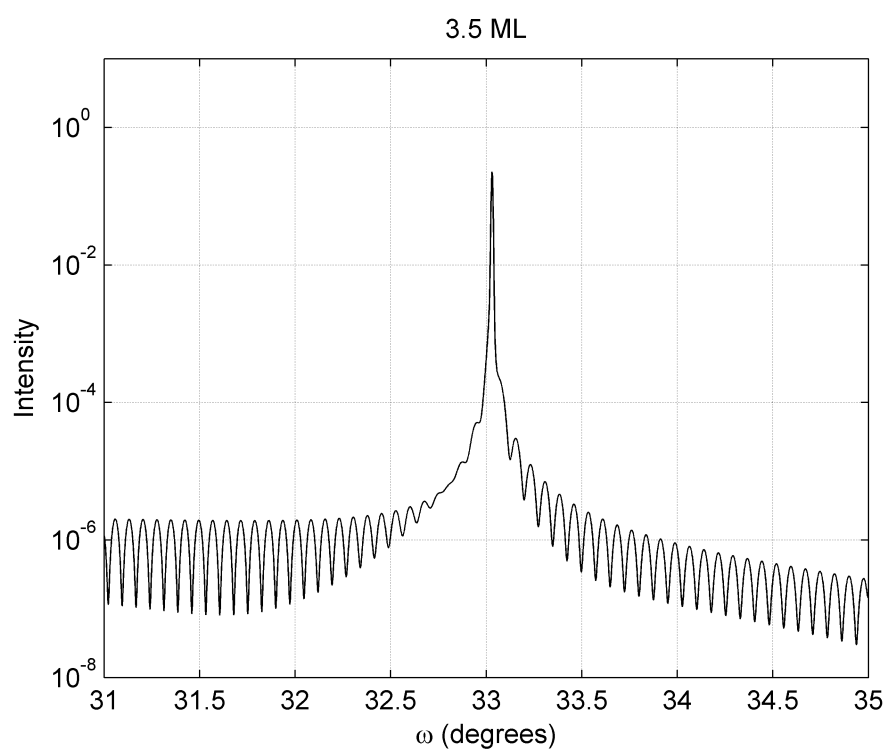


Figure 2.36

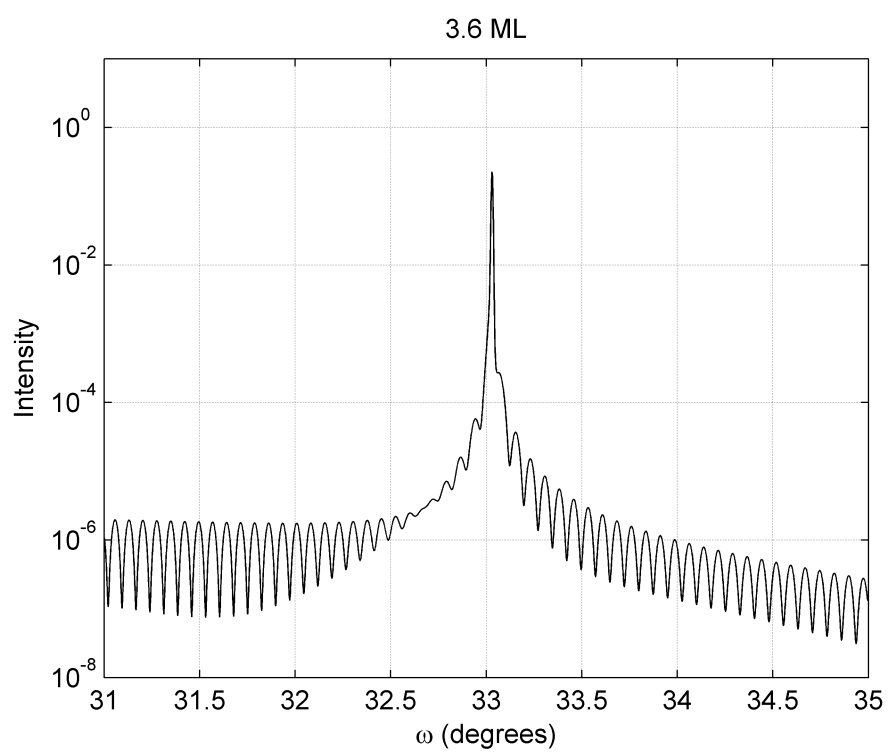


Figure 2.37

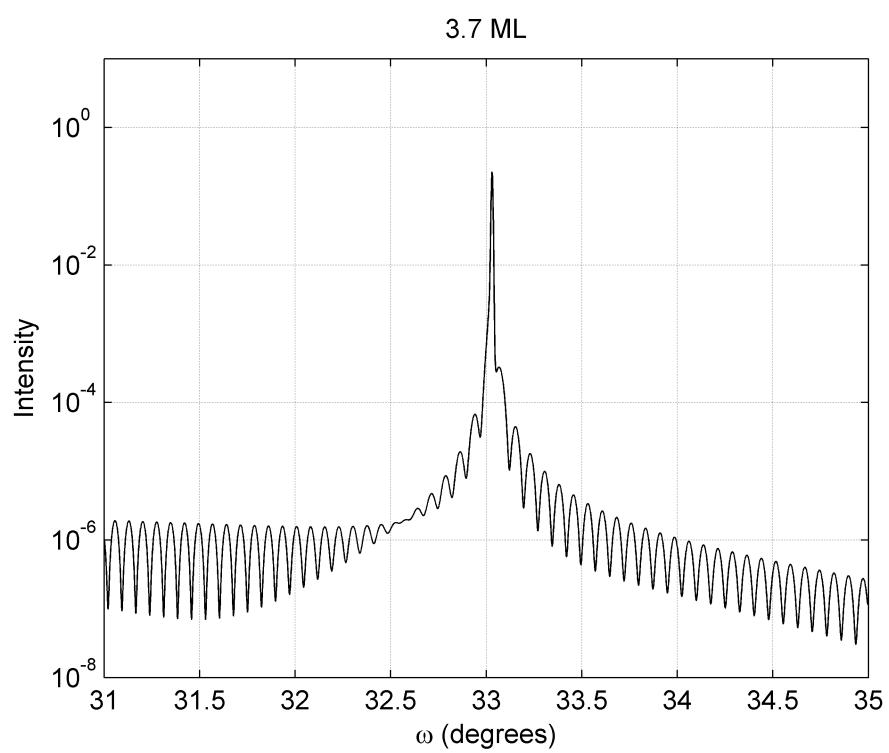


Figure 2.38

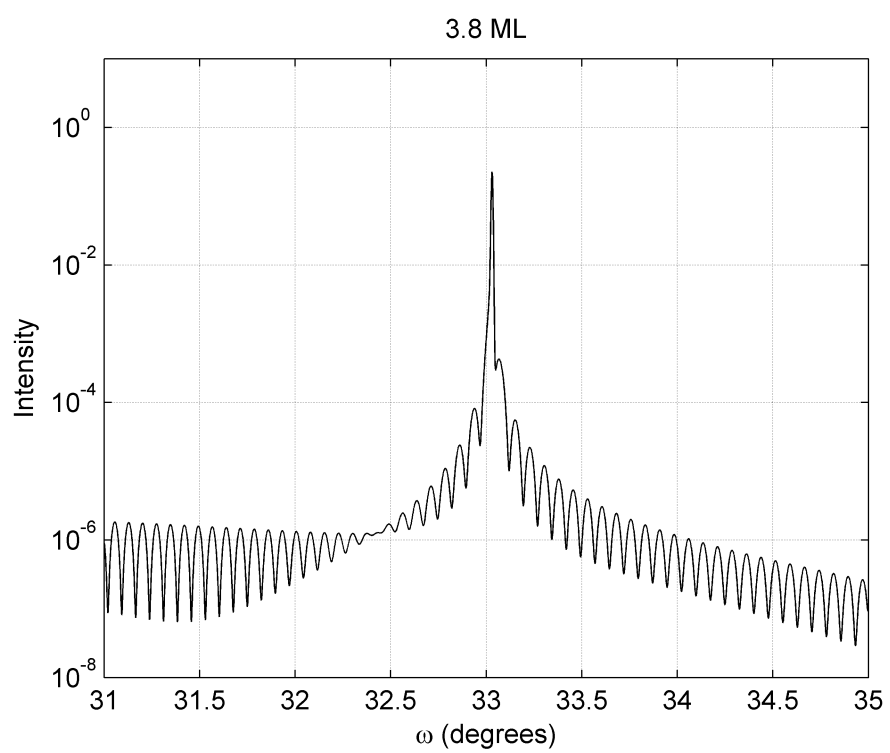


Figure 2.39

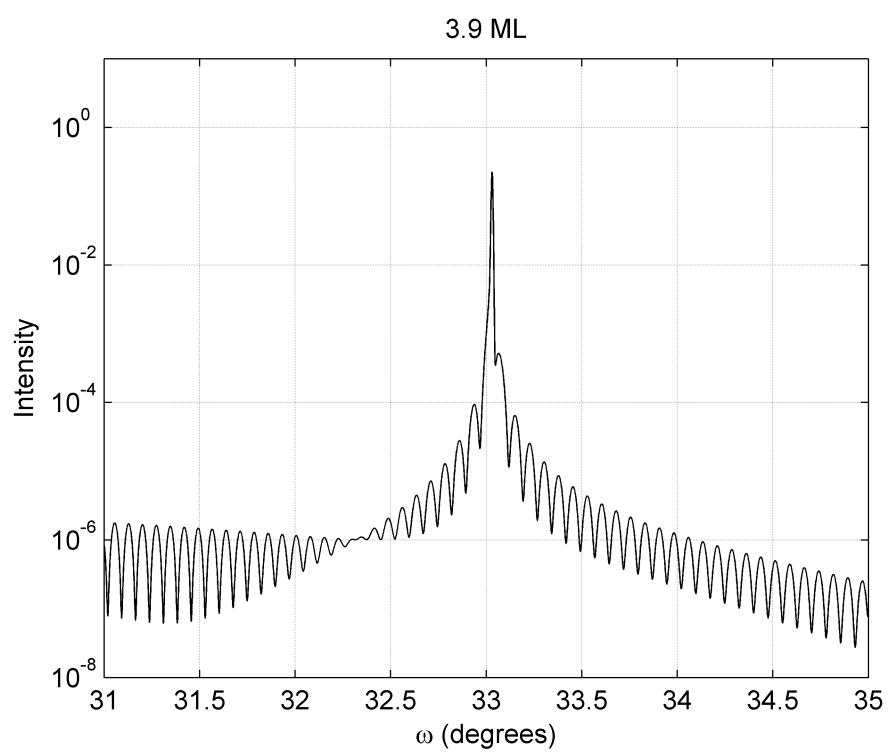


Figure 2.40

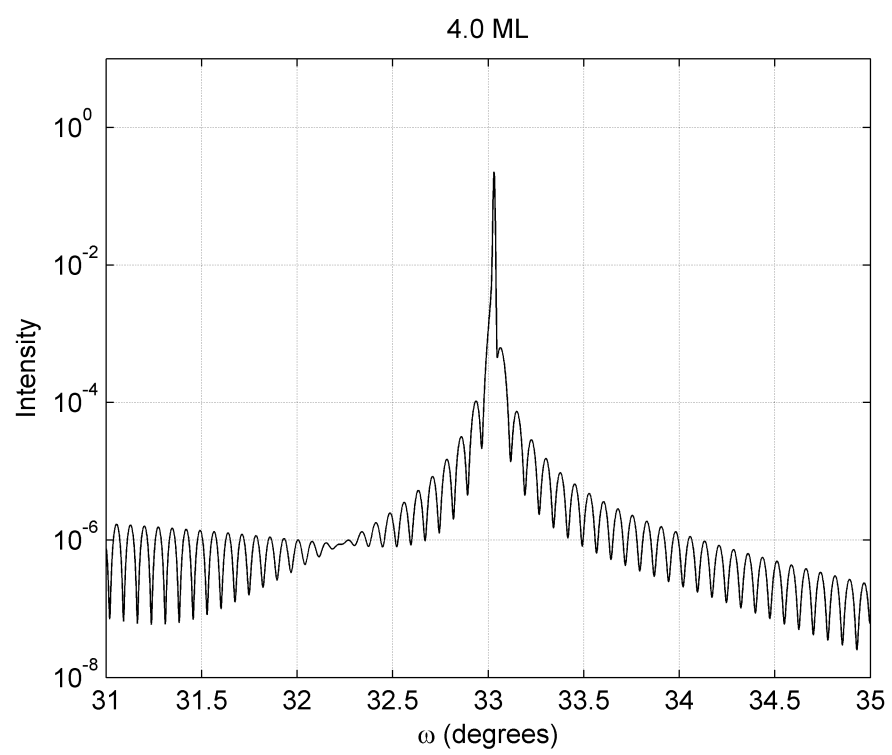


Figure 2.41

2.2 Top InAs layer

Simulations with varying InAs top layer thickness and a constant bottom layer thickness of 2 monolayers are shown in figures 2.42–2.82. They are arranged by increasing top layer thickness from 0 to 4 monolayers.

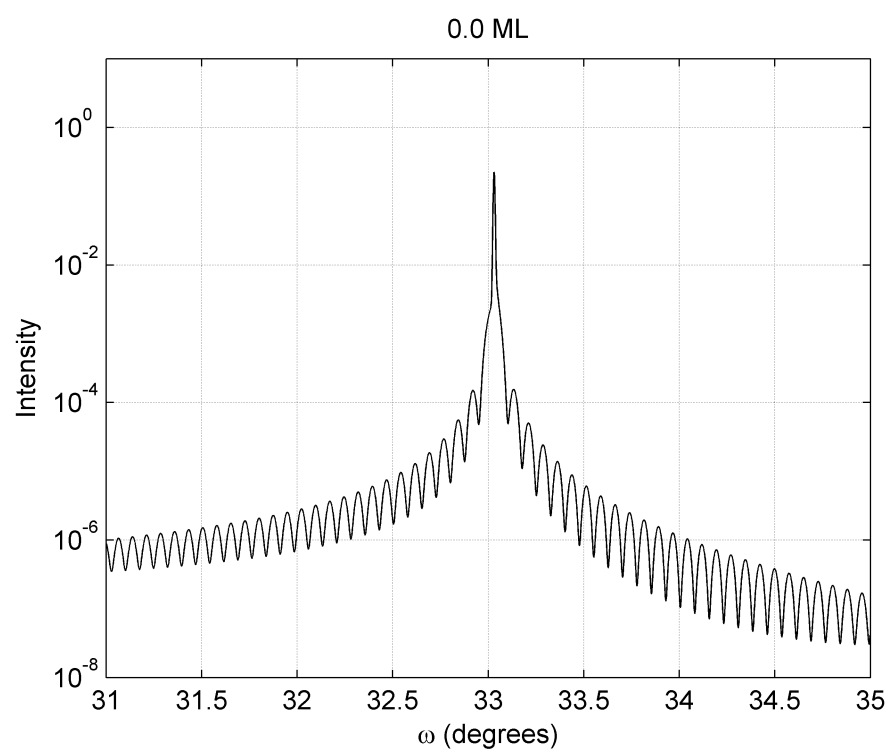


Figure 2.42

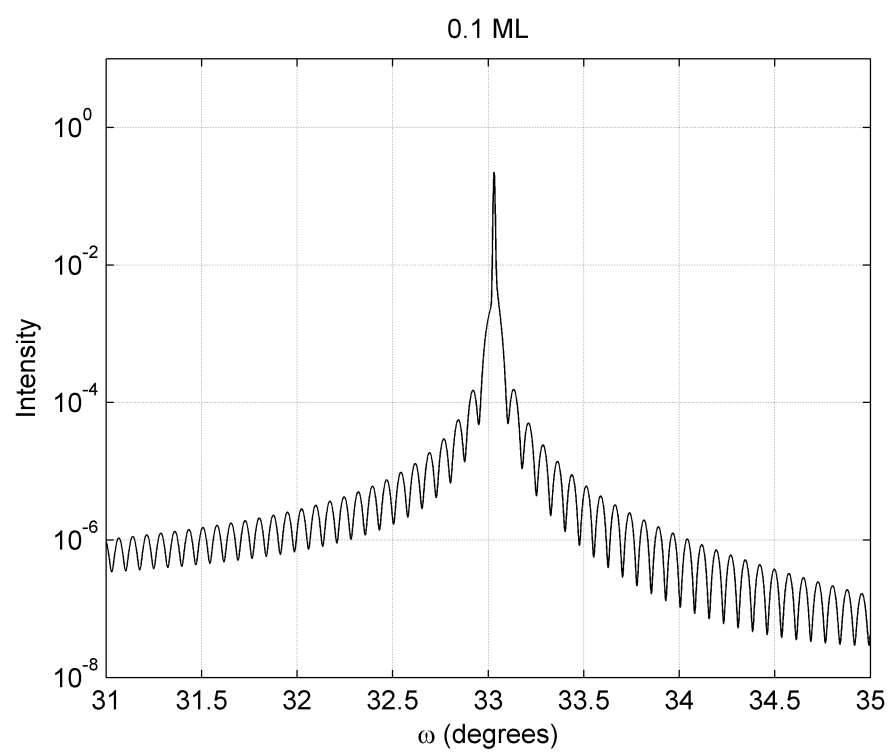


Figure 2.43

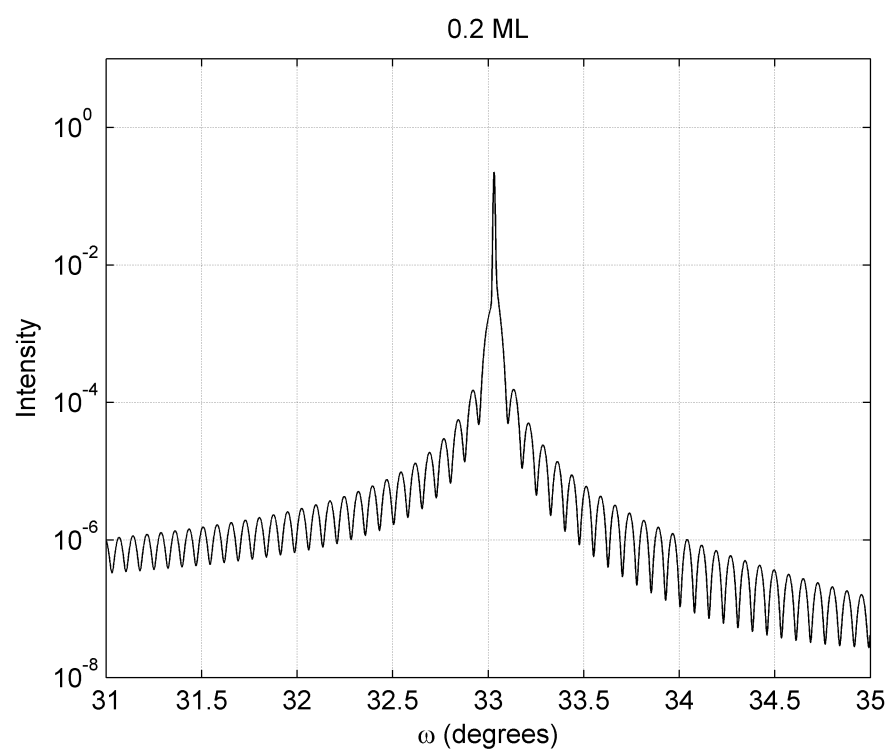


Figure 2.44

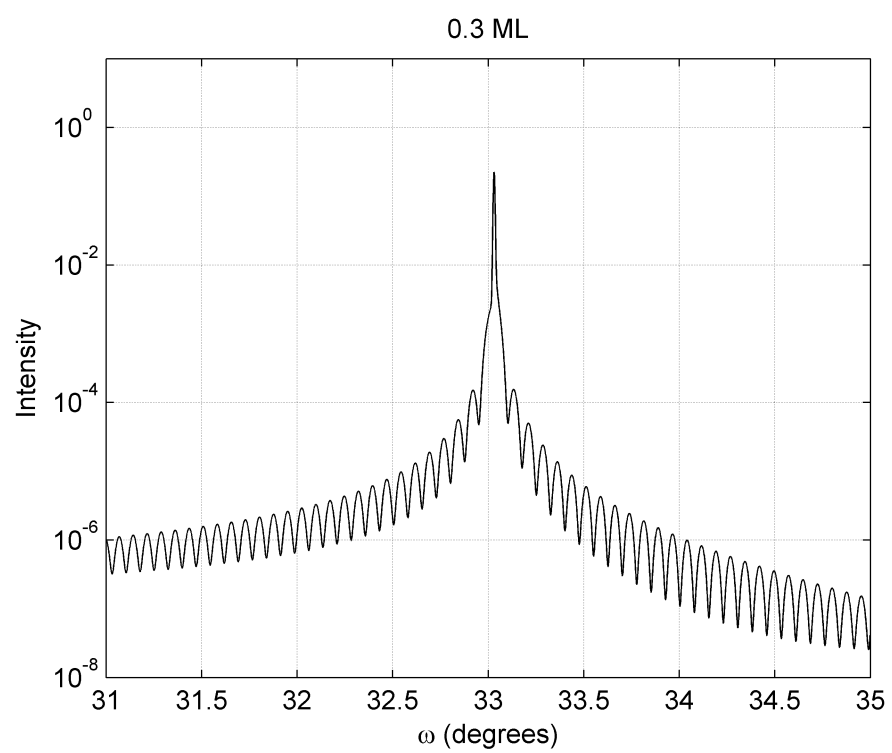


Figure 2.45

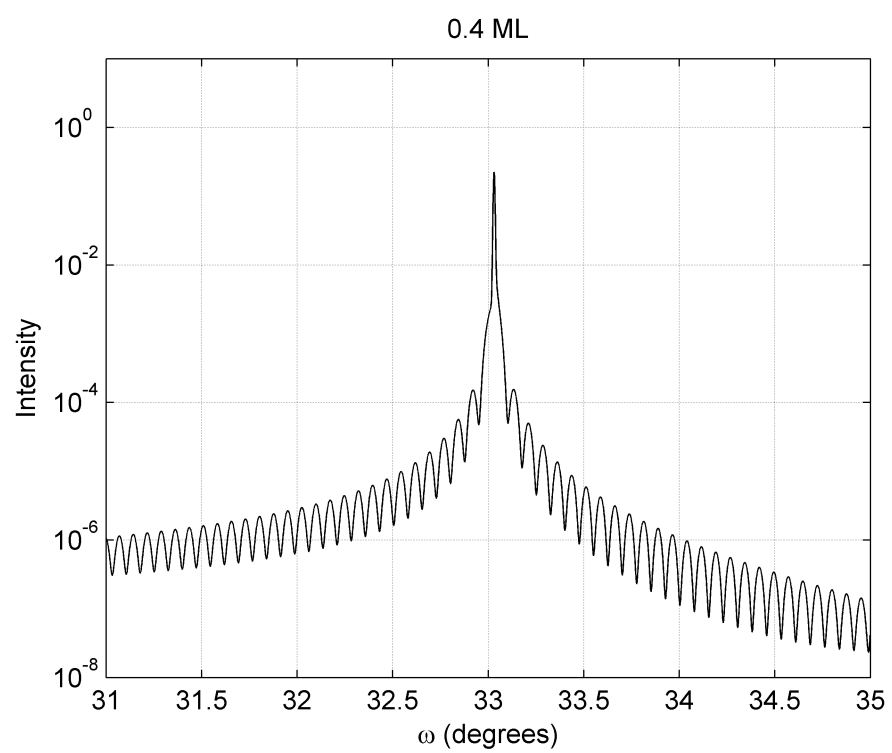


Figure 2.46

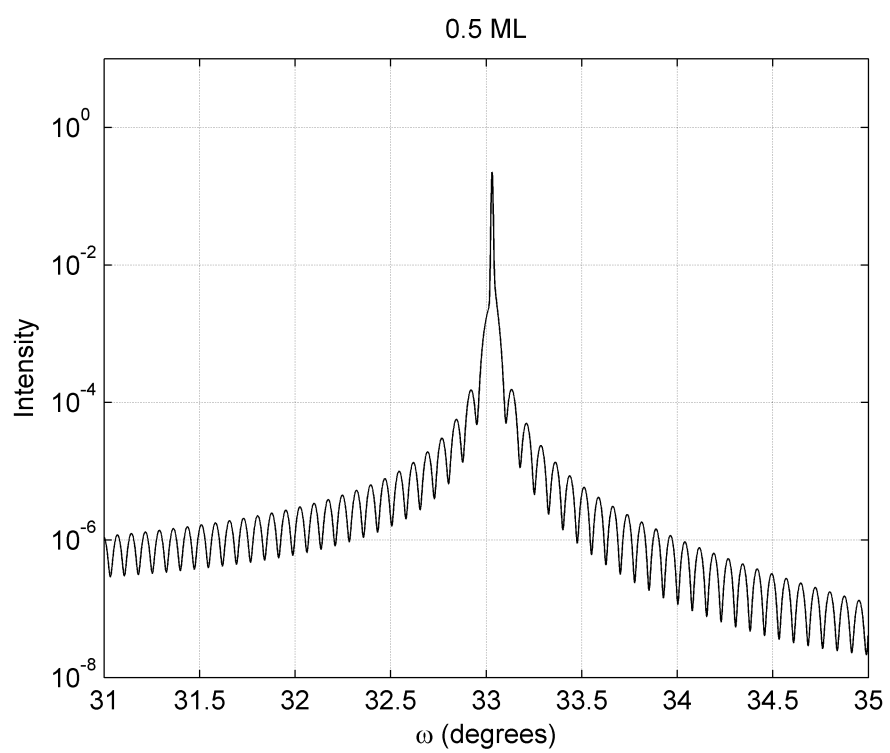


Figure 2.47

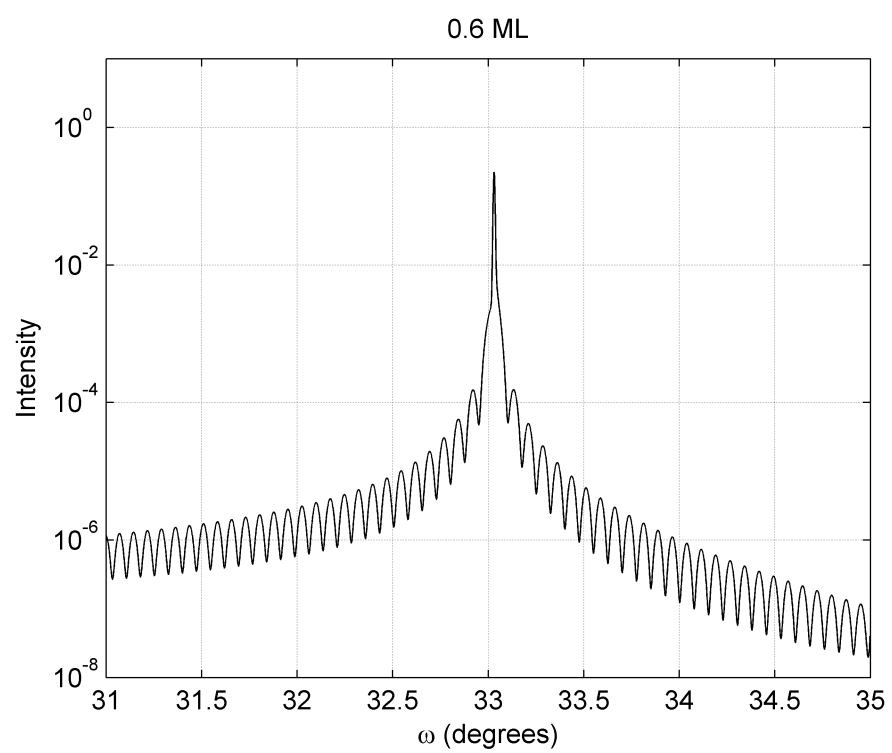


Figure 2.48

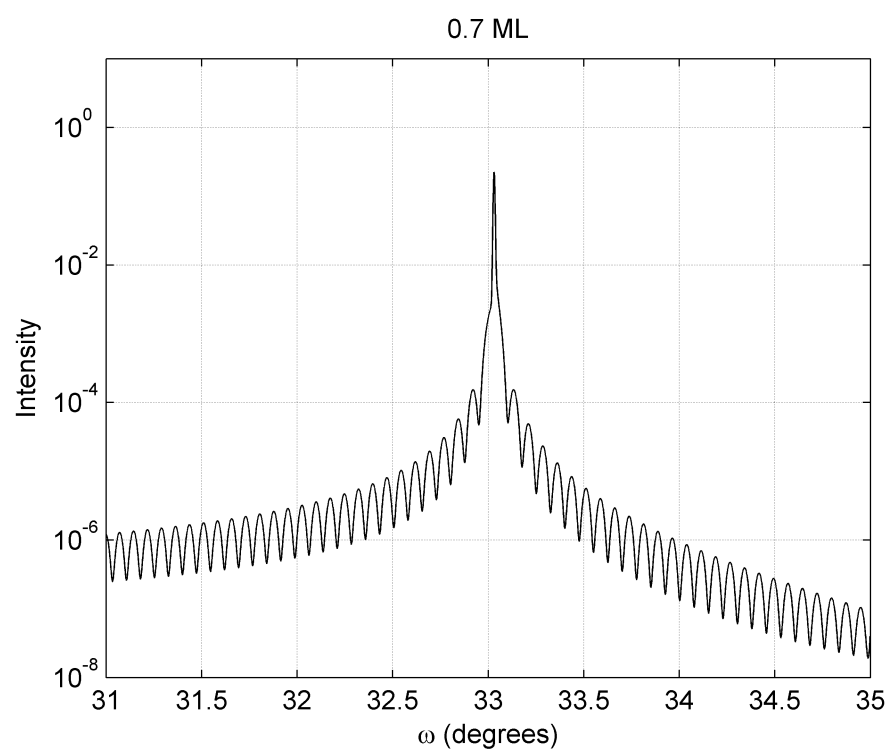


Figure 2.49

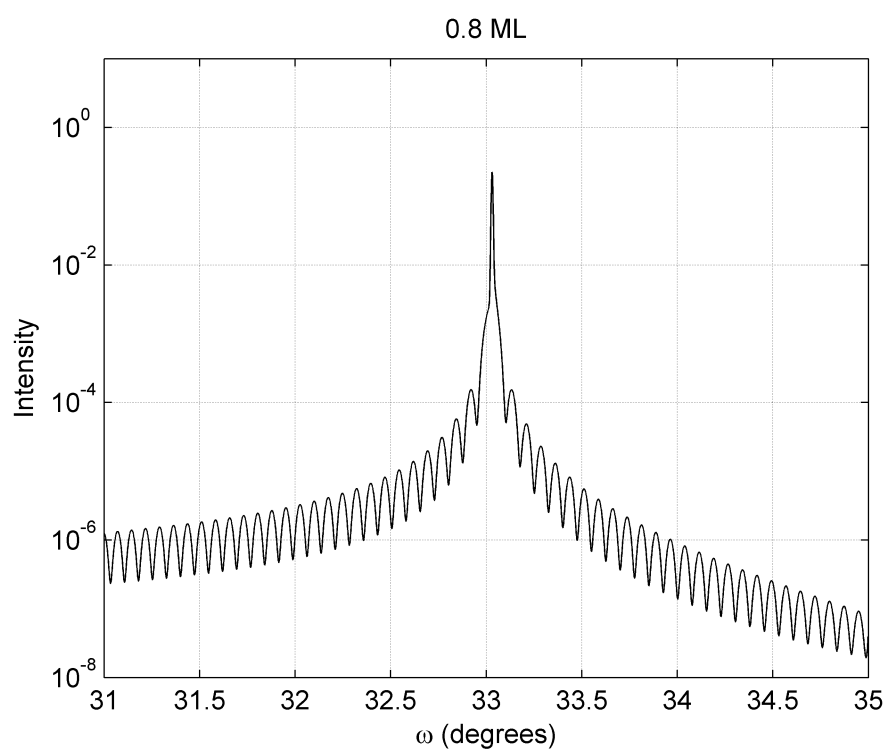


Figure 2.50

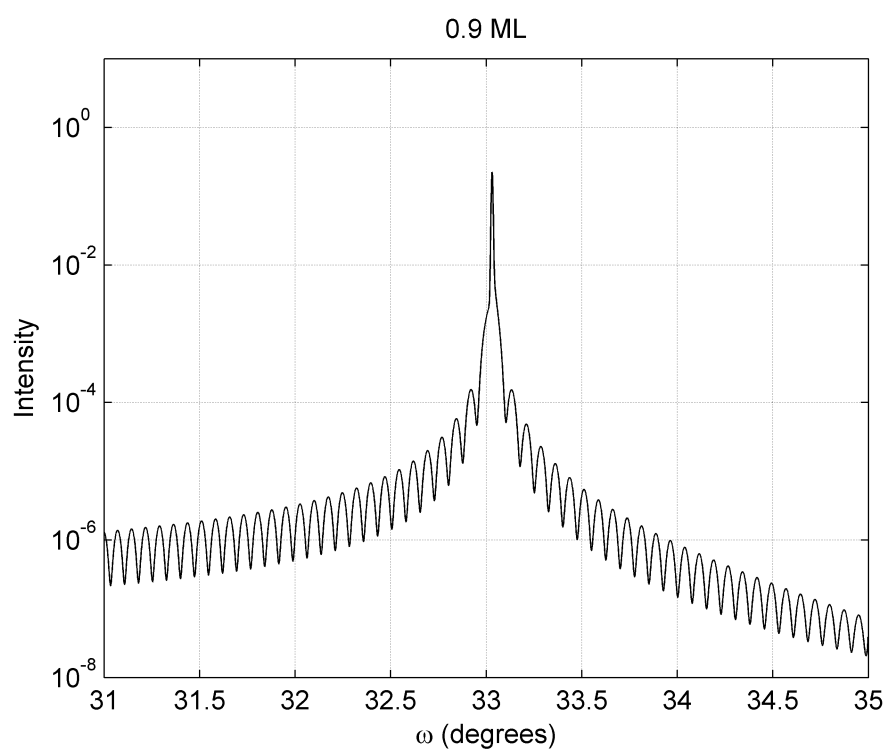


Figure 2.51

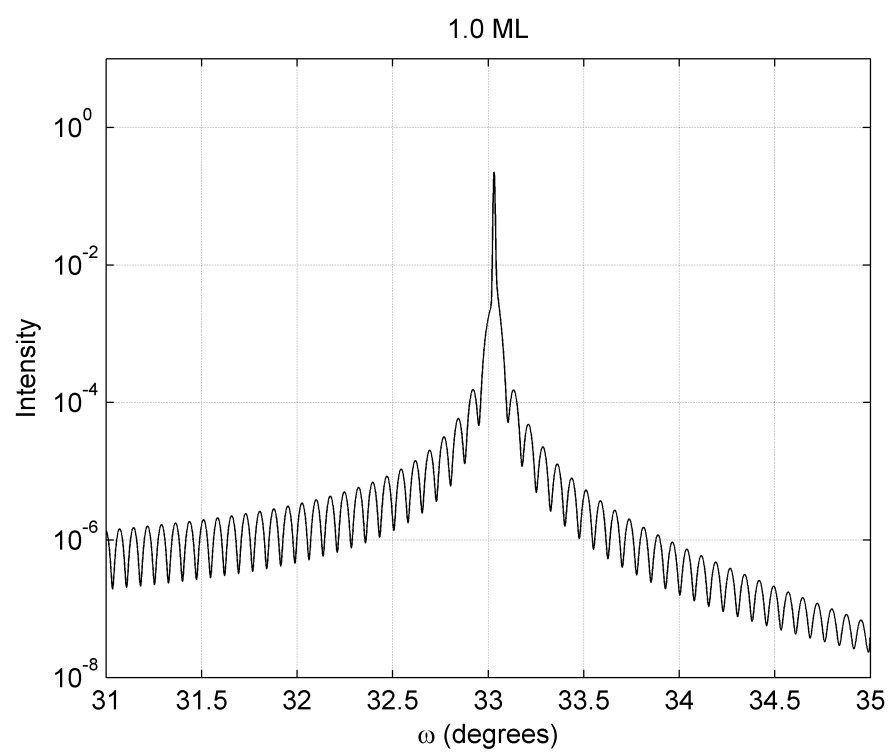


Figure 2.52

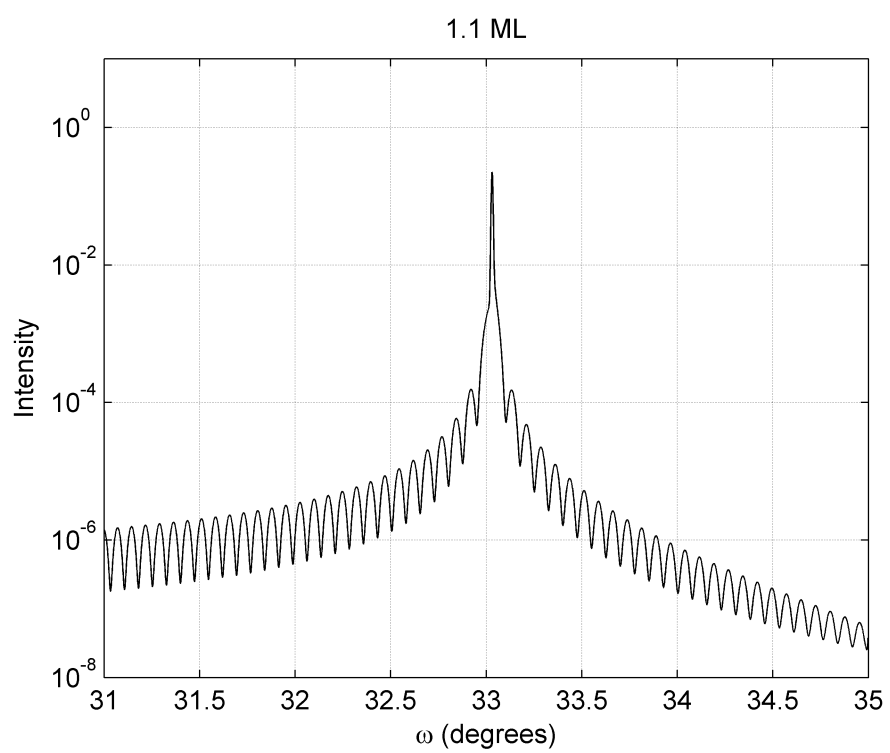


Figure 2.53

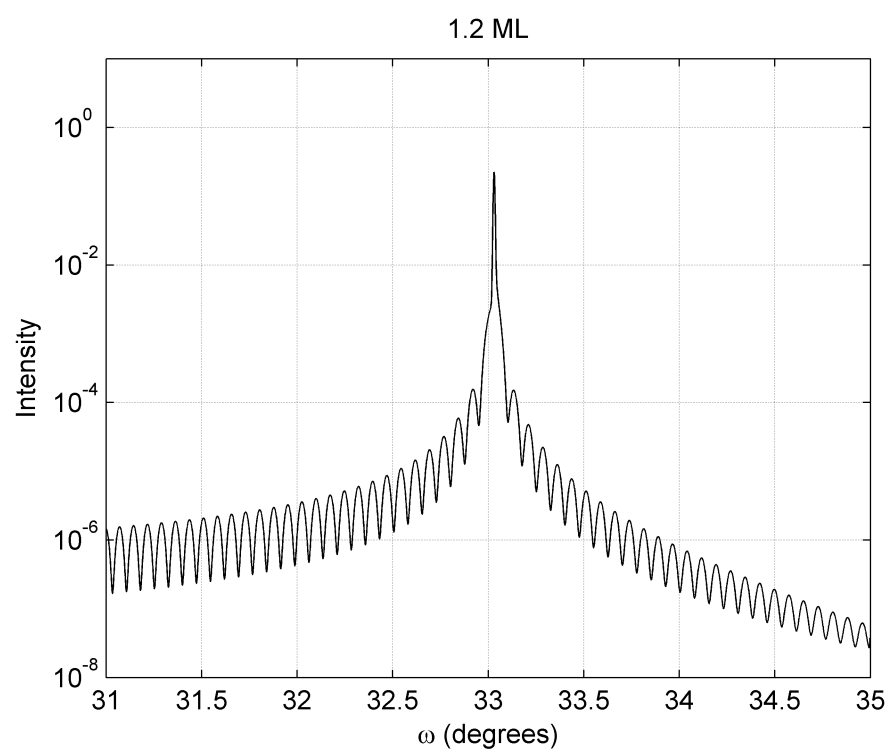


Figure 2.54

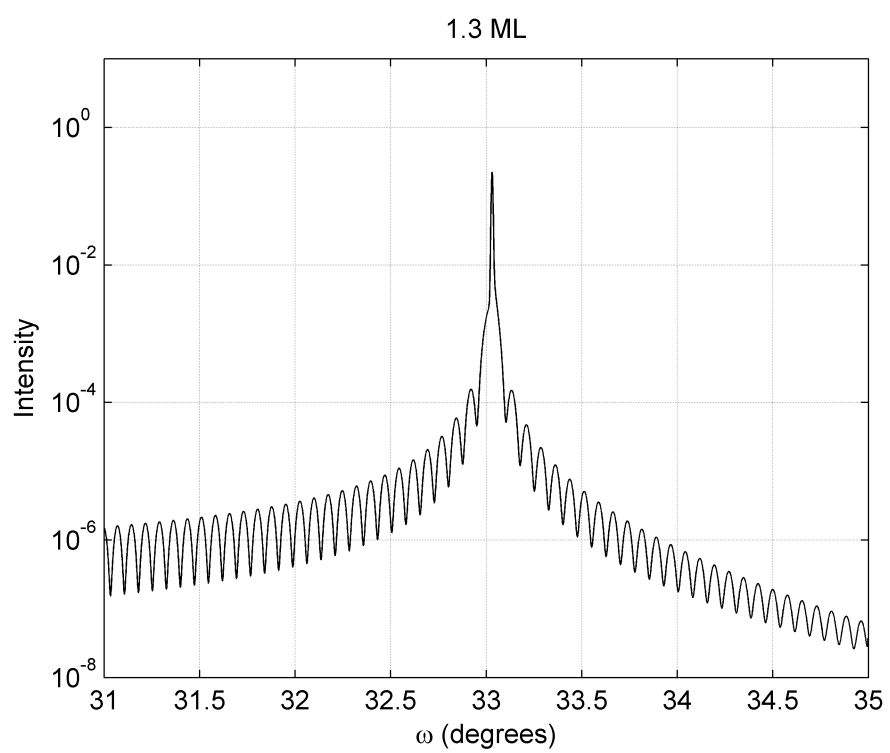


Figure 2.55

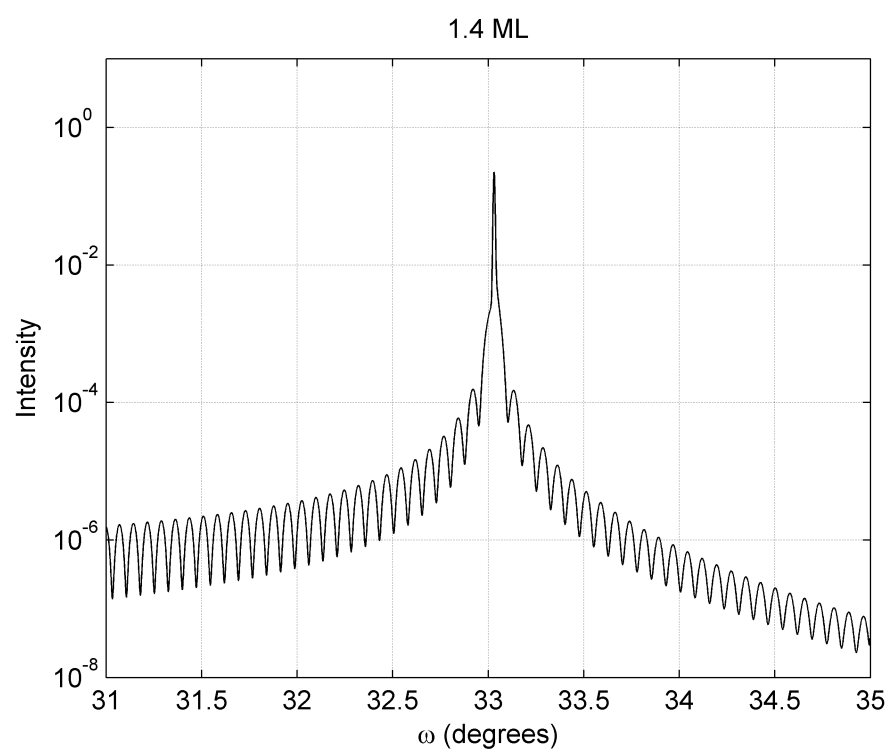


Figure 2.56

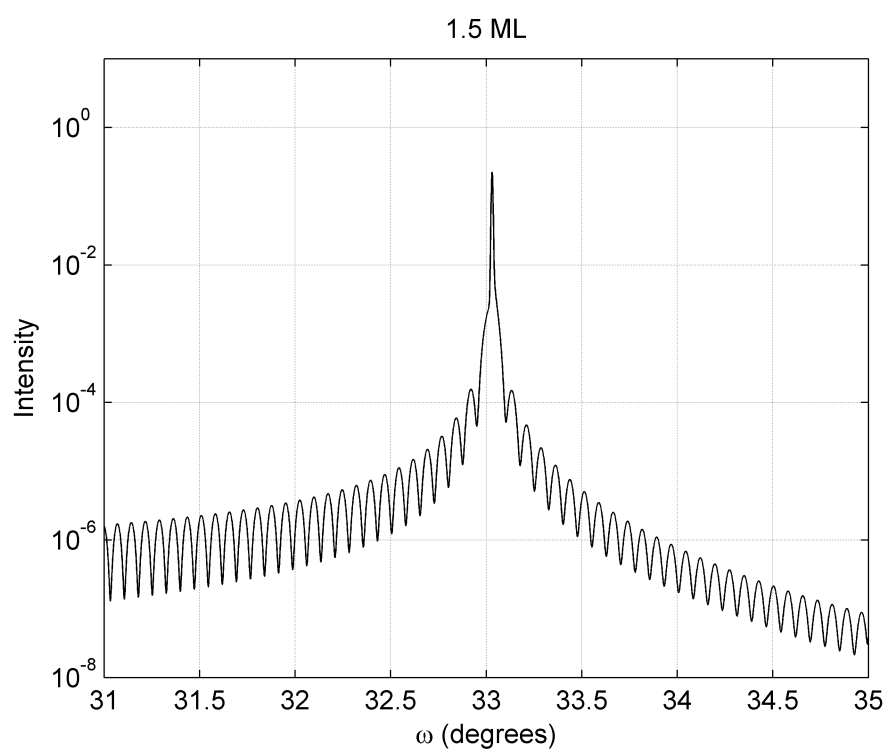


Figure 2.57

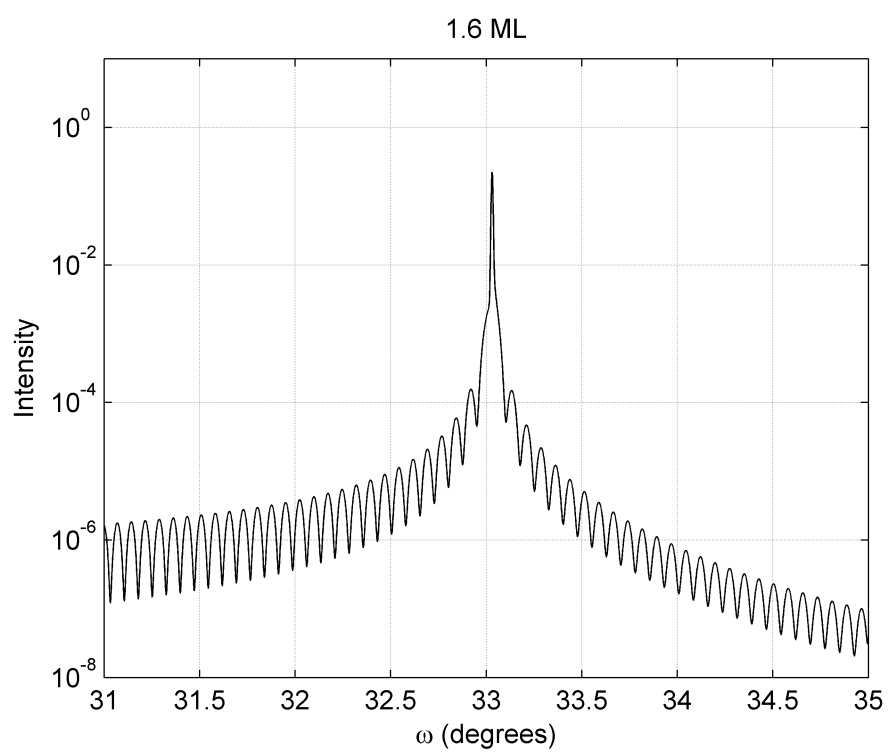


Figure 2.58

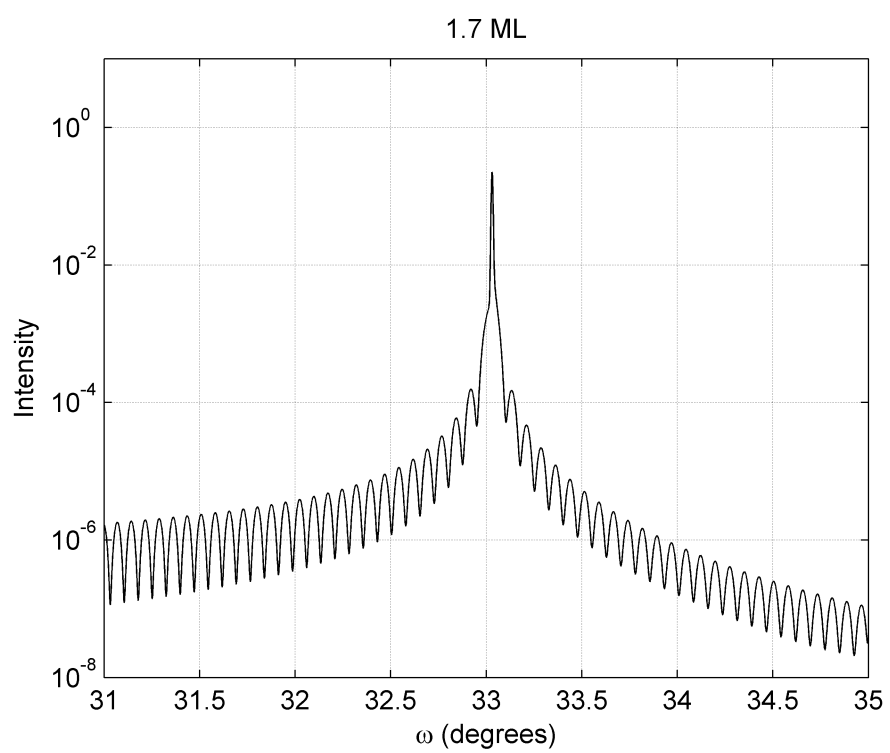


Figure 2.59

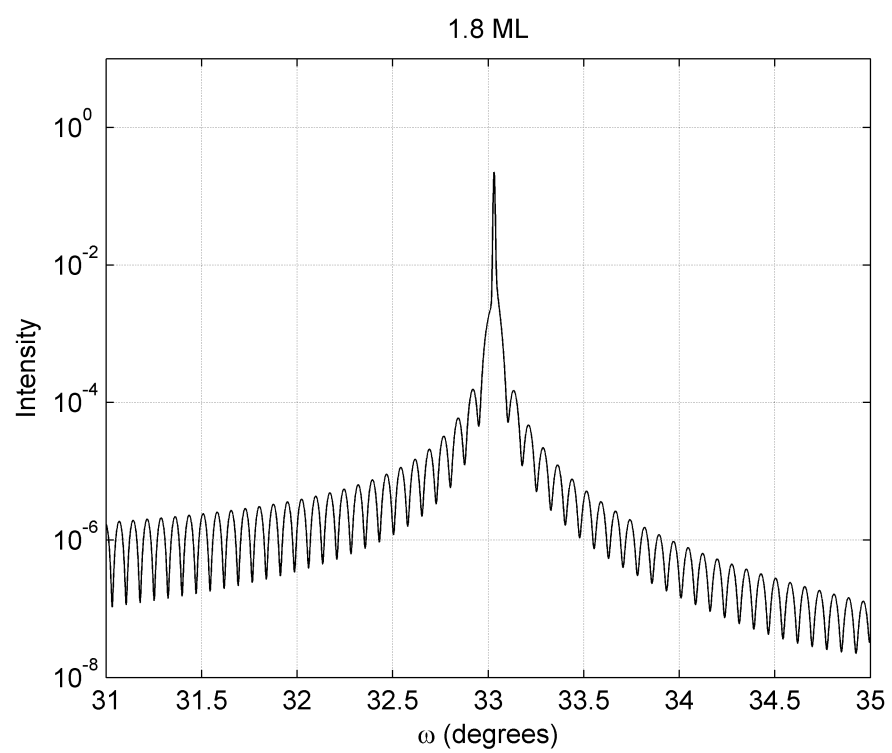


Figure 2.60

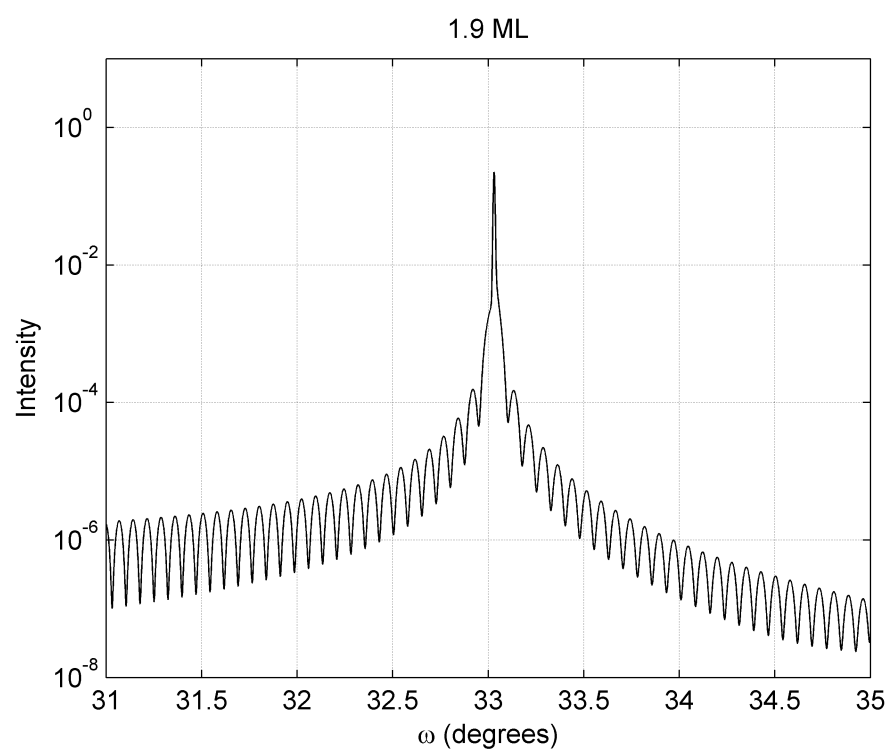


Figure 2.61

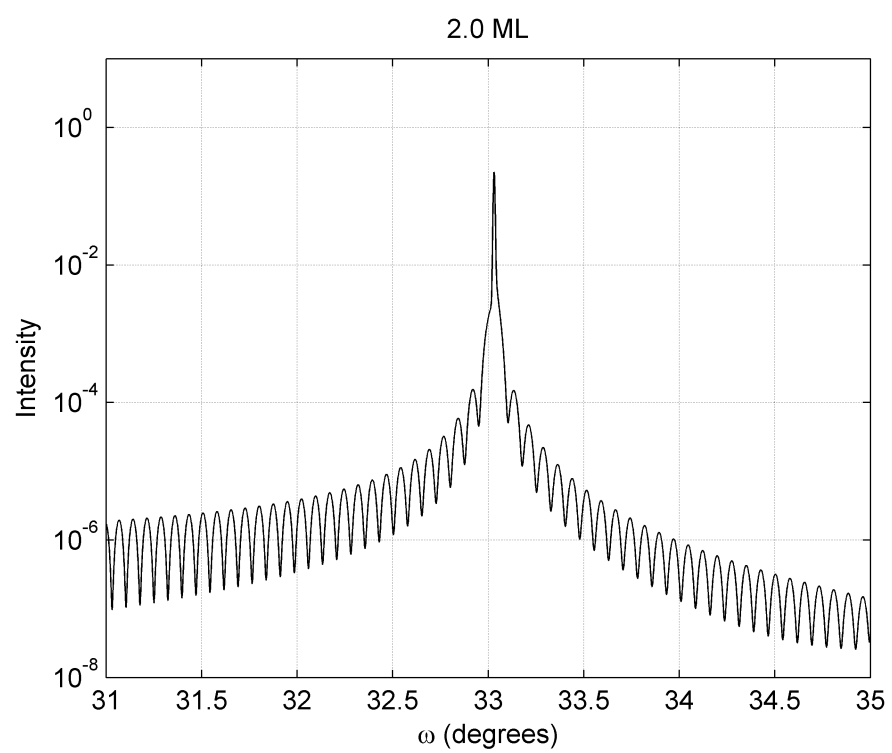


Figure 2.62

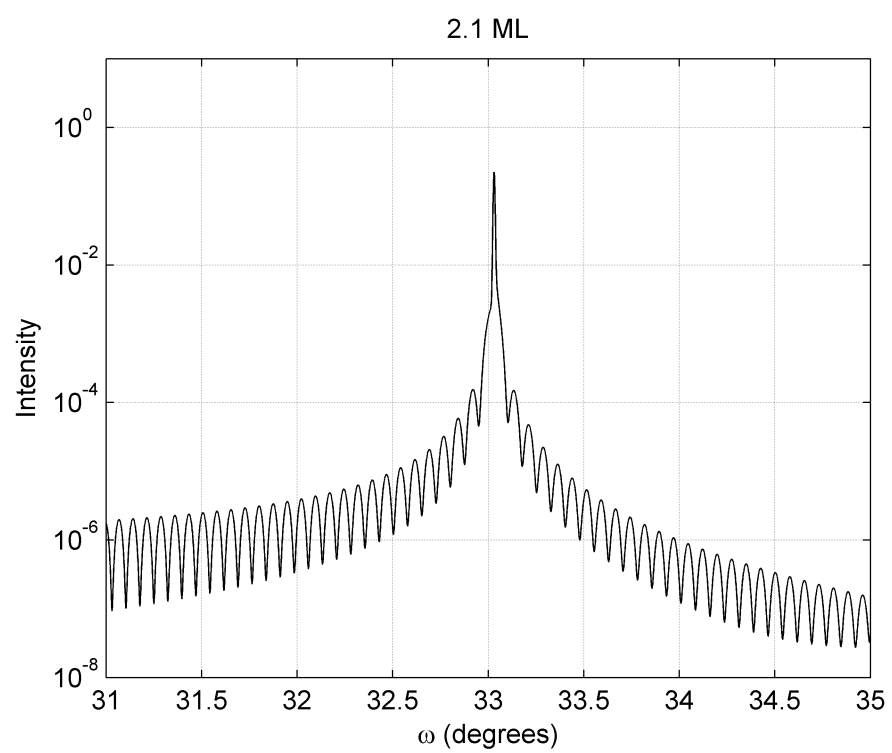


Figure 2.63

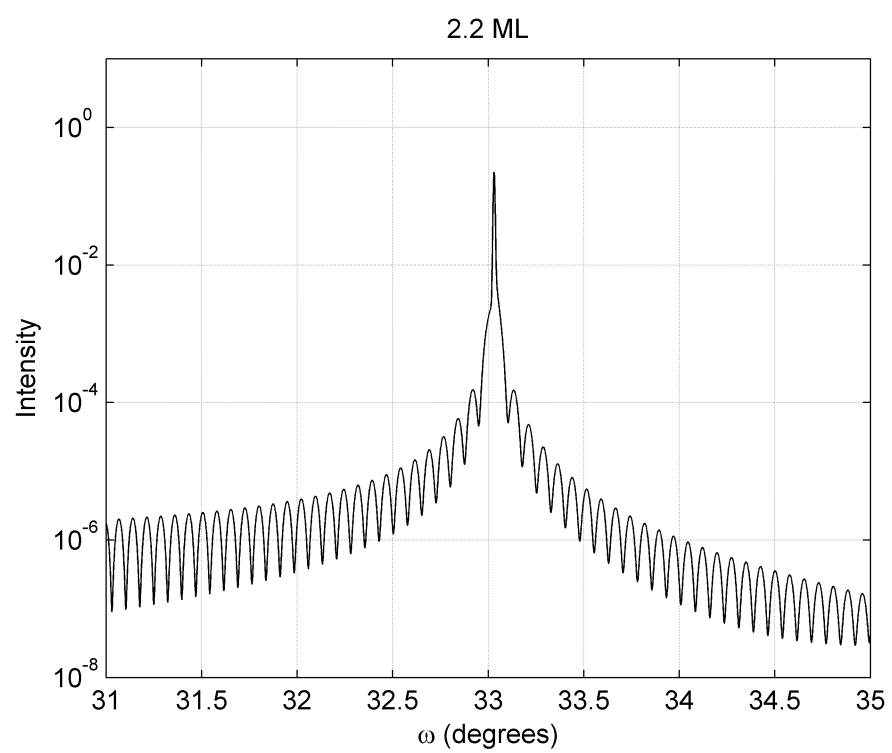


Figure 2.64

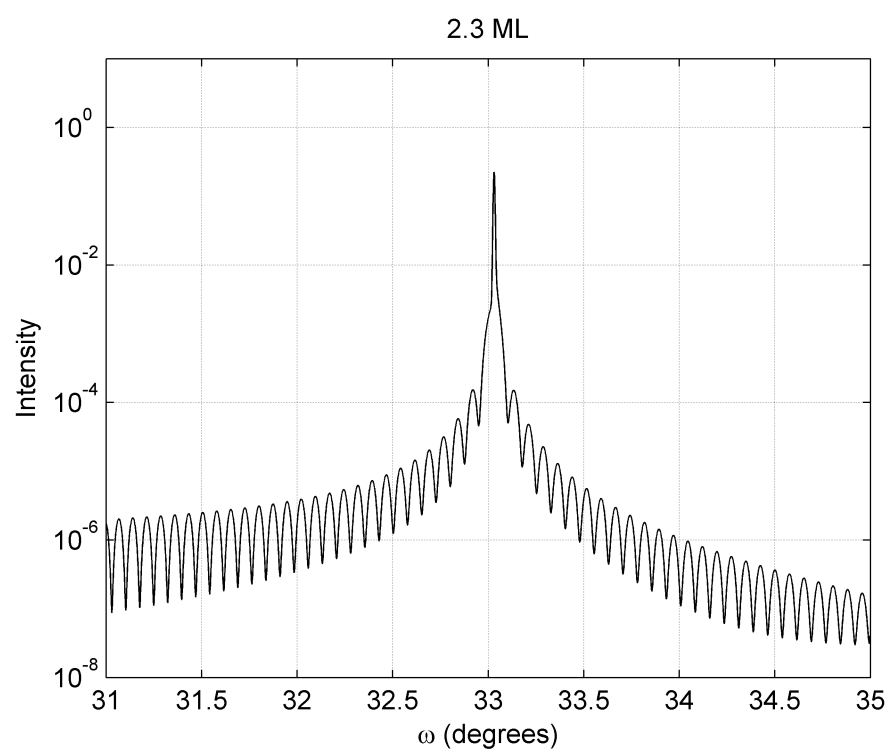


Figure 2.65

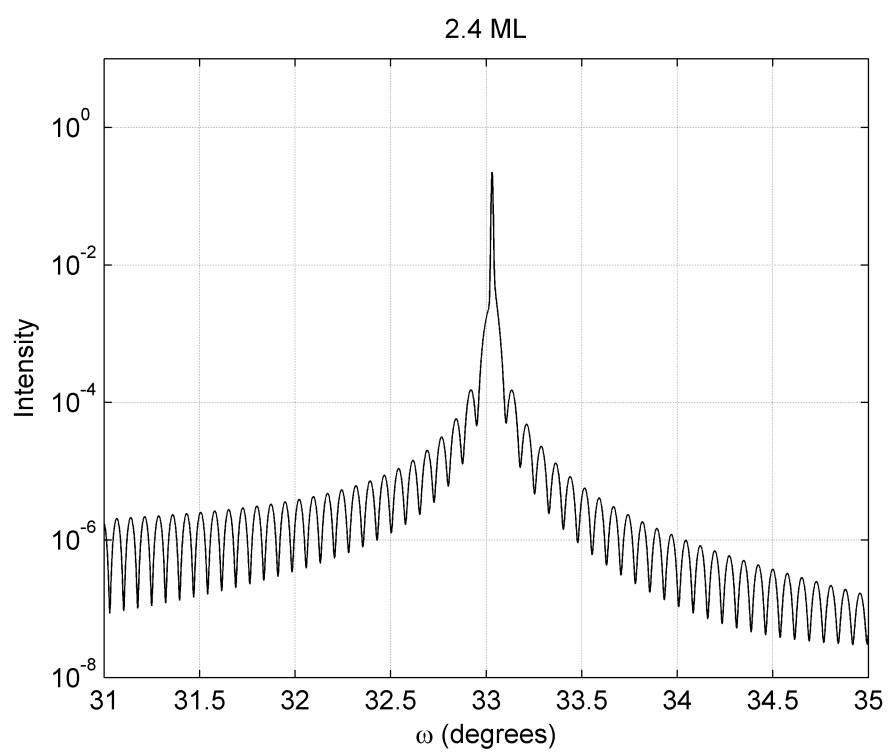


Figure 2.66

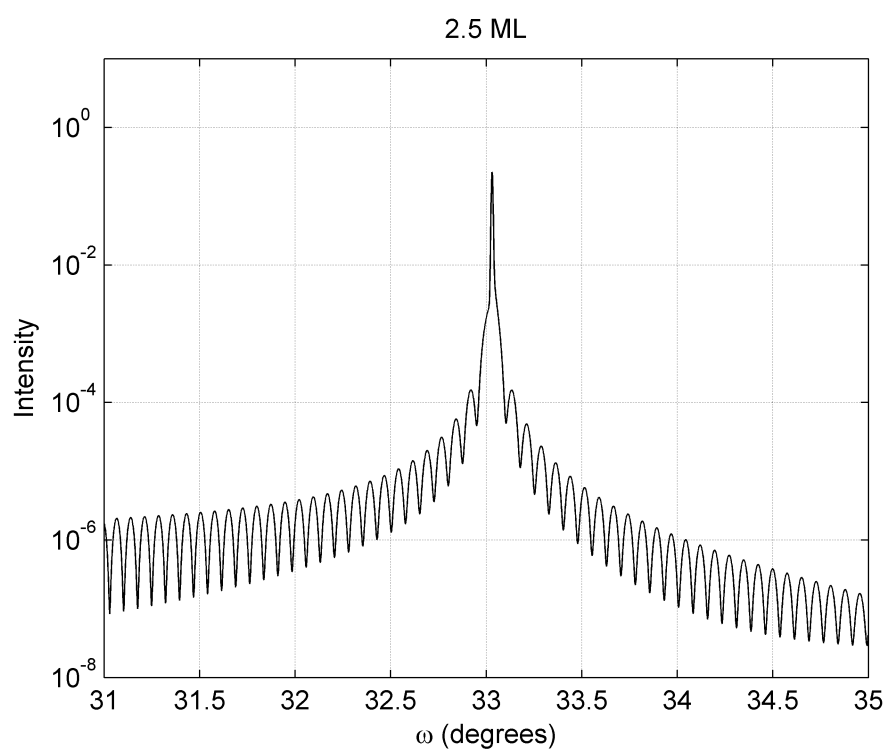


Figure 2.67

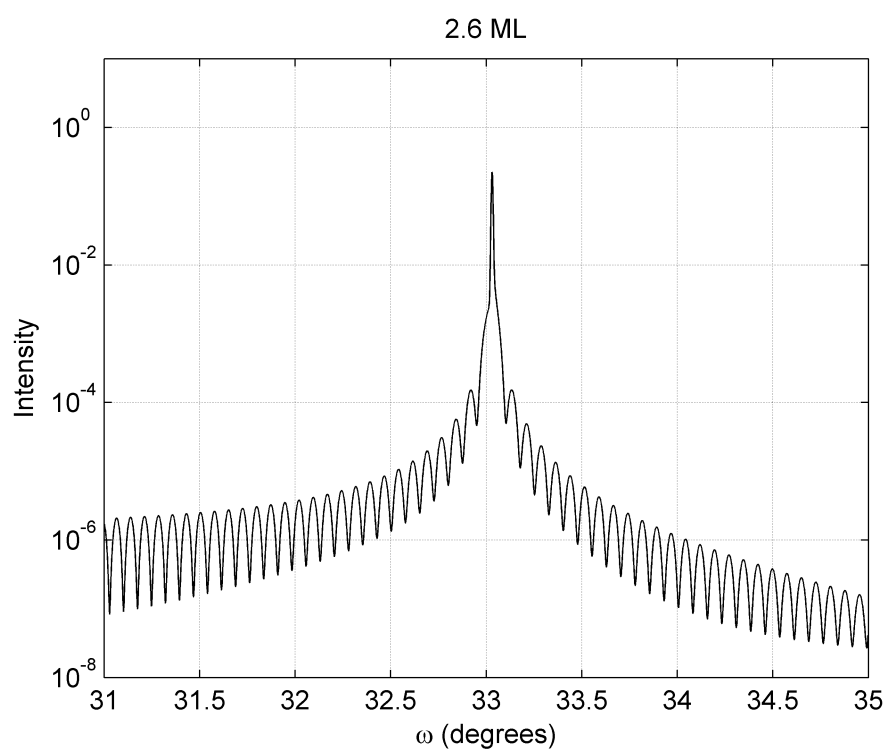


Figure 2.68

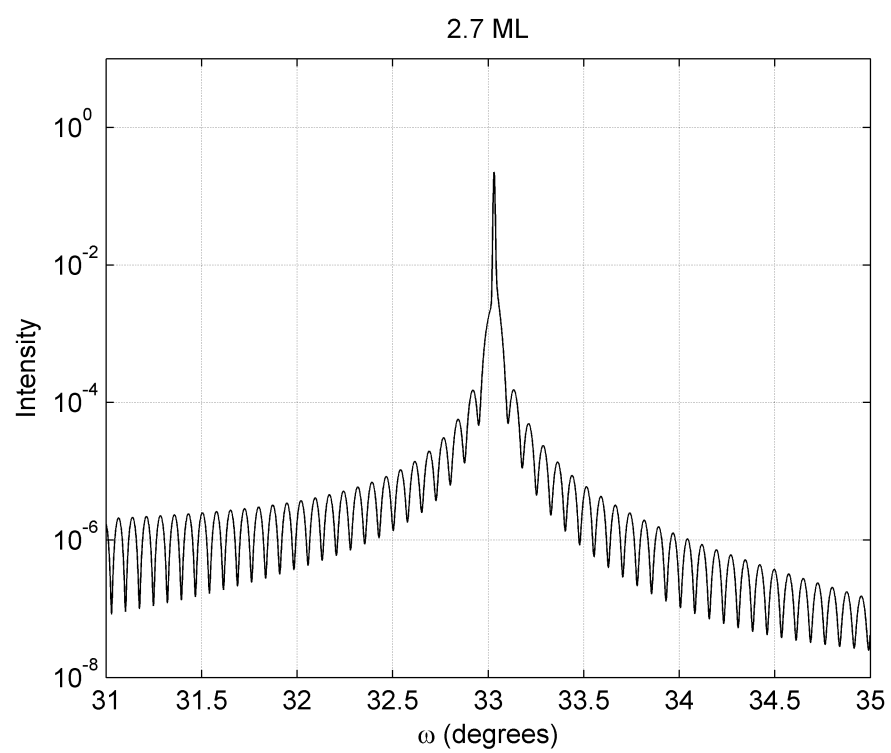


Figure 2.69

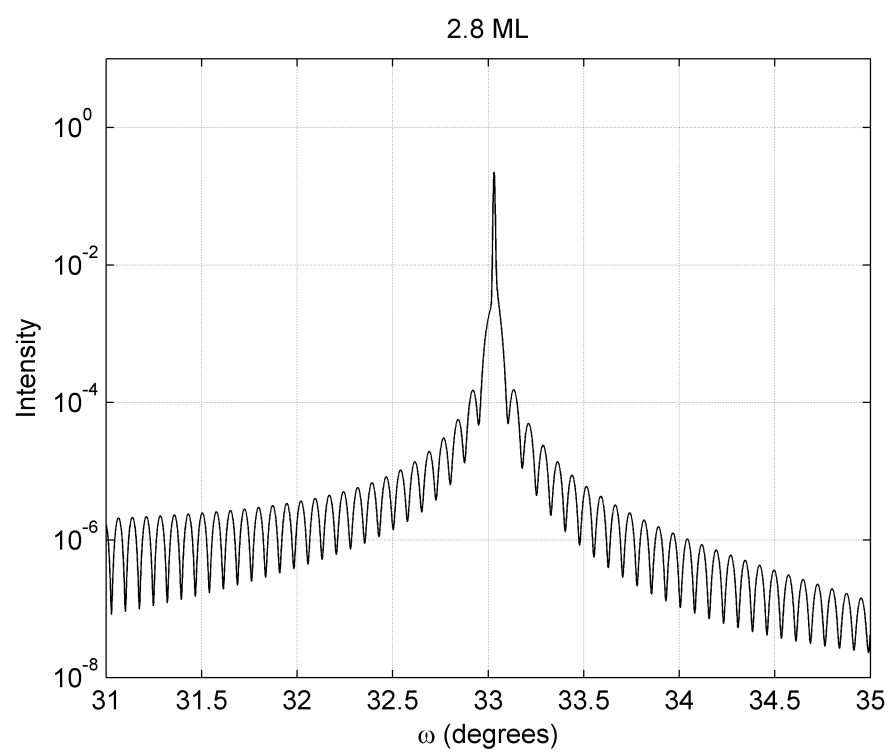


Figure 2.70

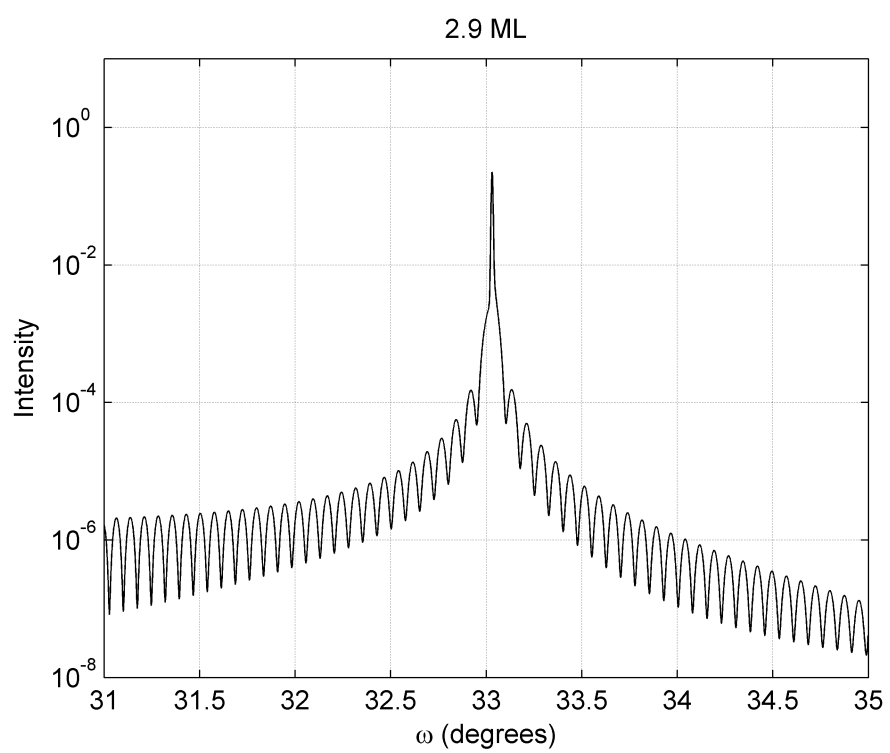


Figure 2.71

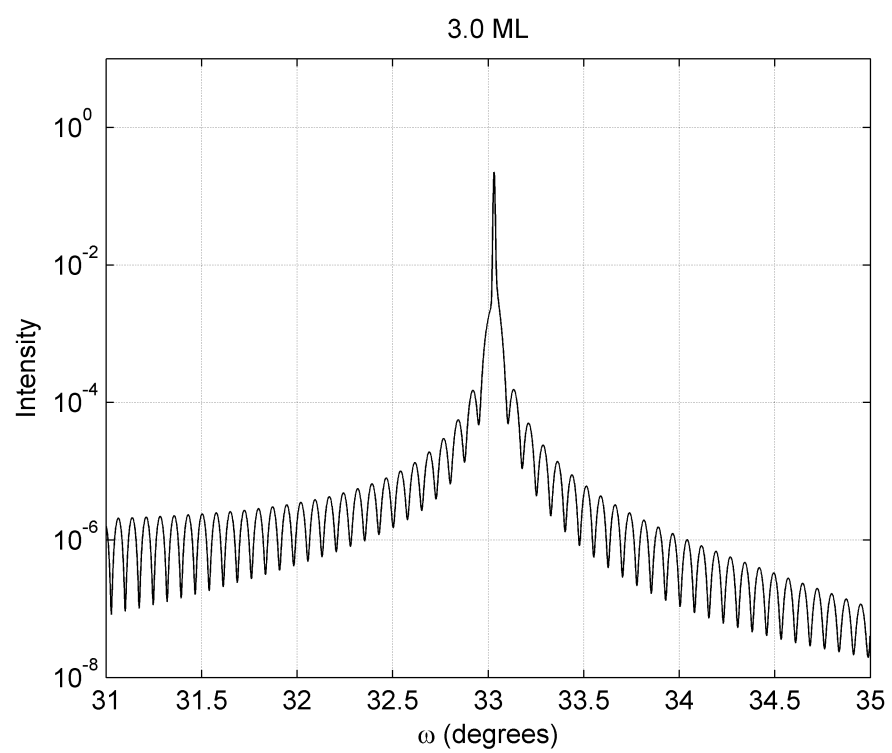


Figure 2.72

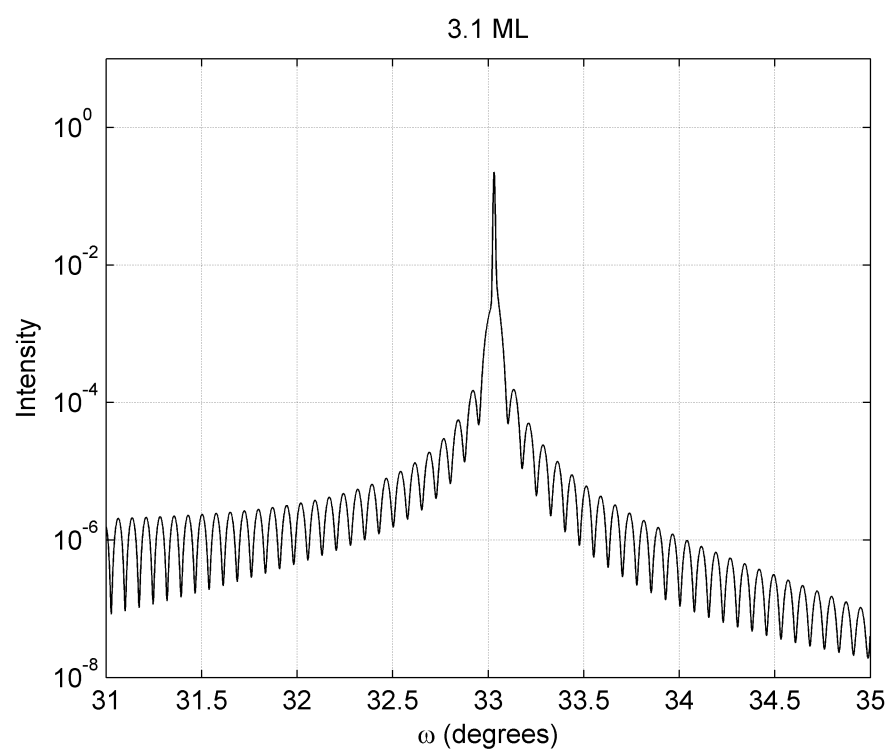


Figure 2.73

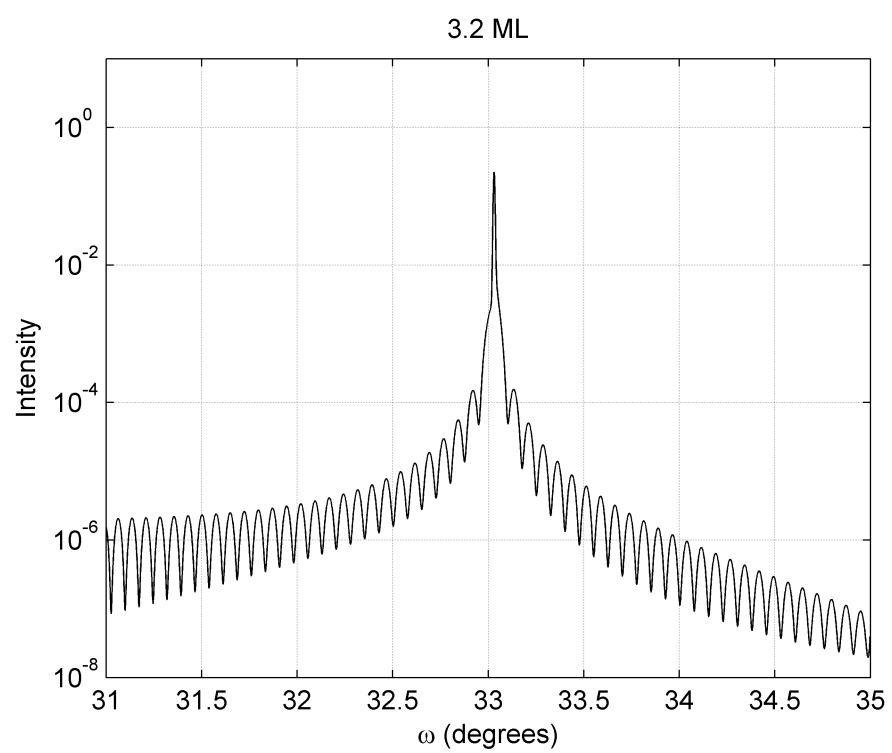


Figure 2.74

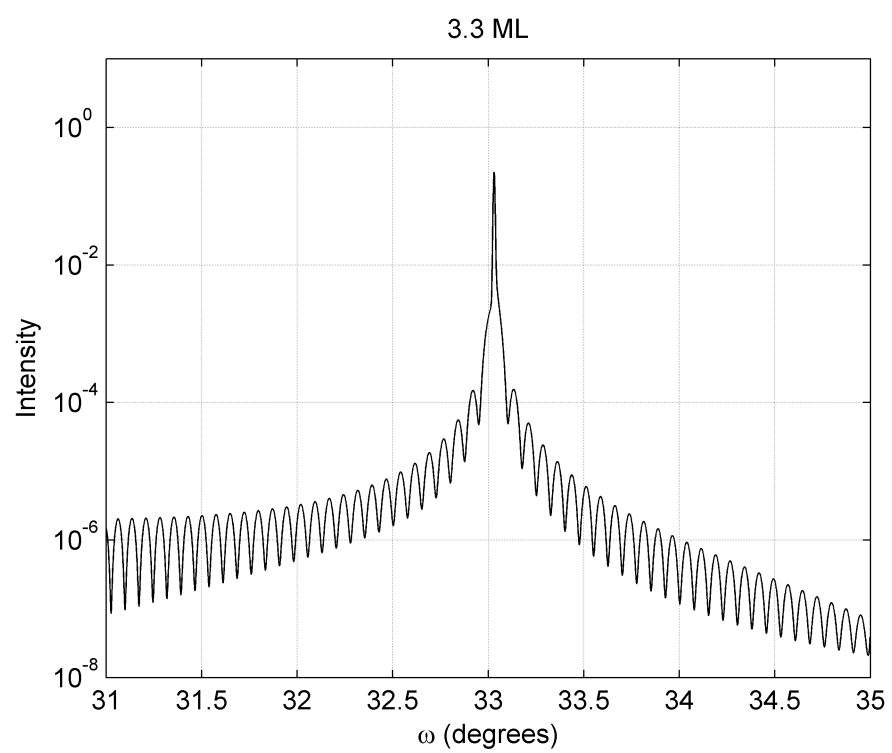


Figure 2.75

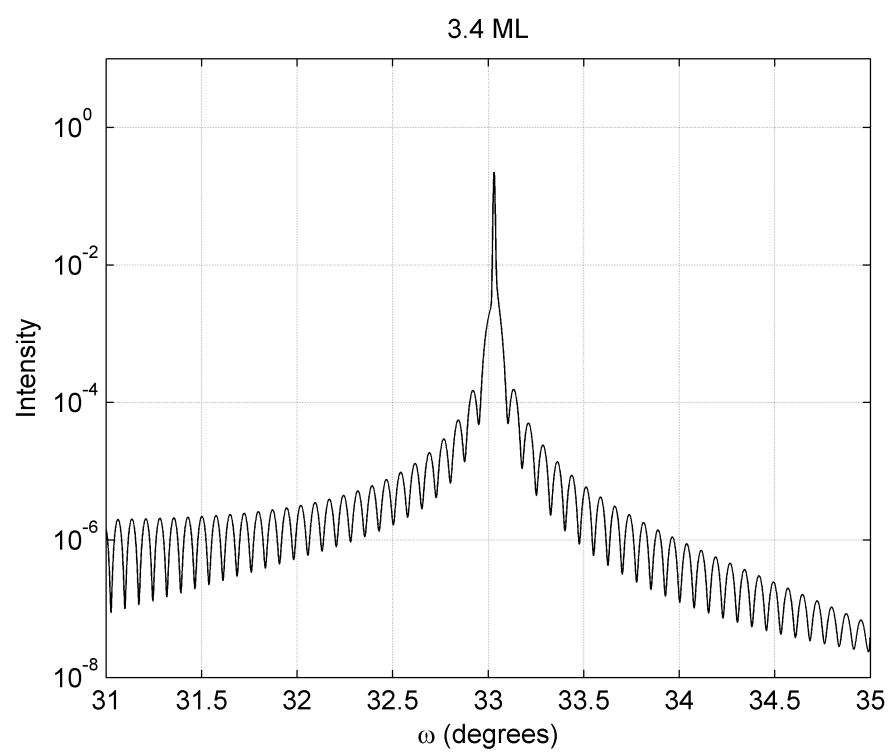


Figure 2.76

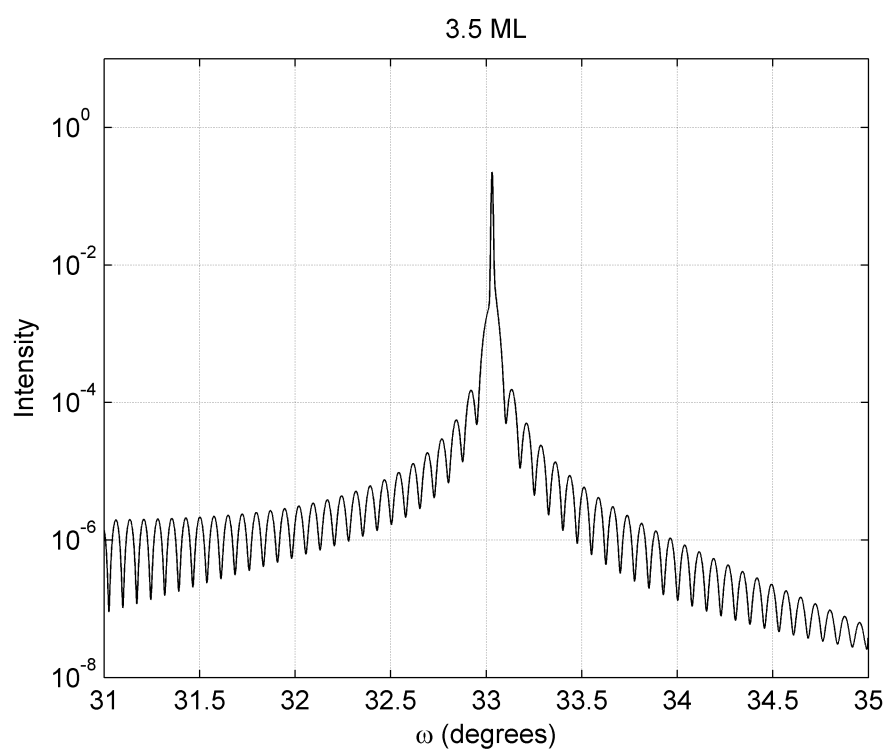


Figure 2.77

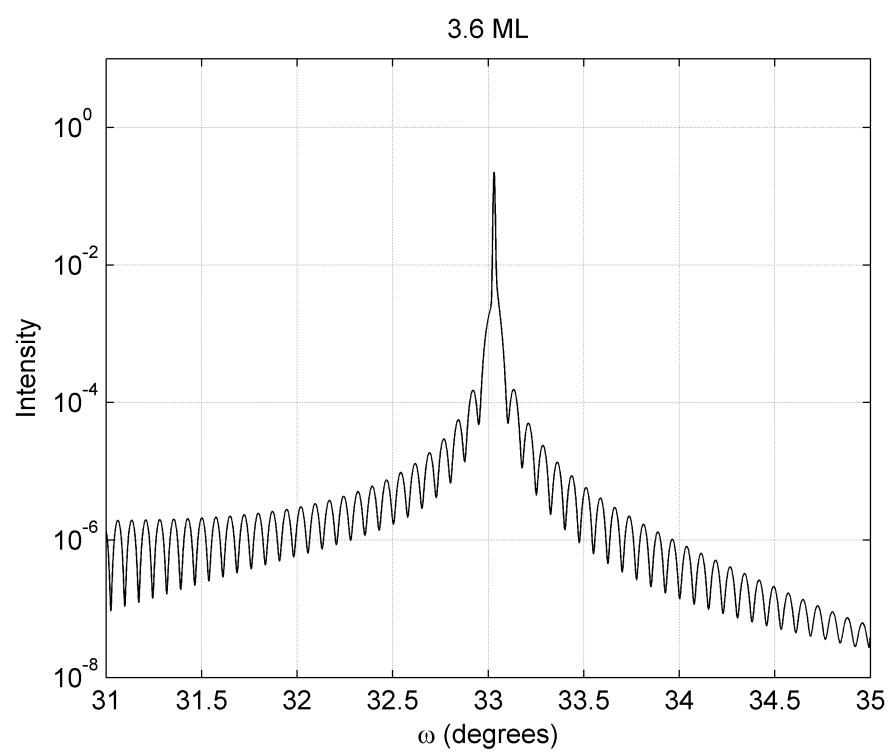


Figure 2.78

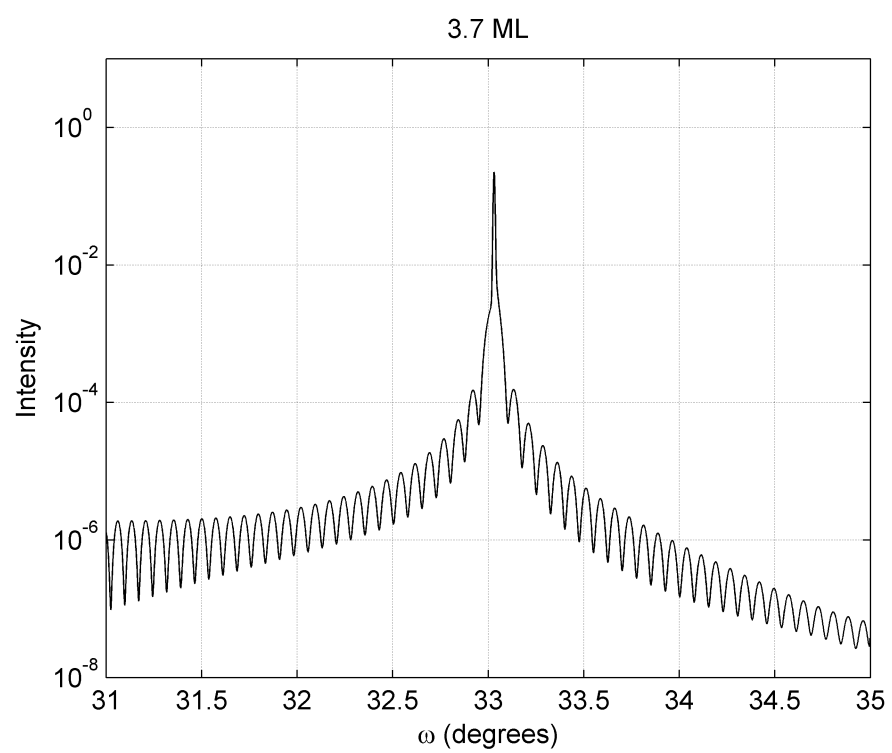


Figure 2.79

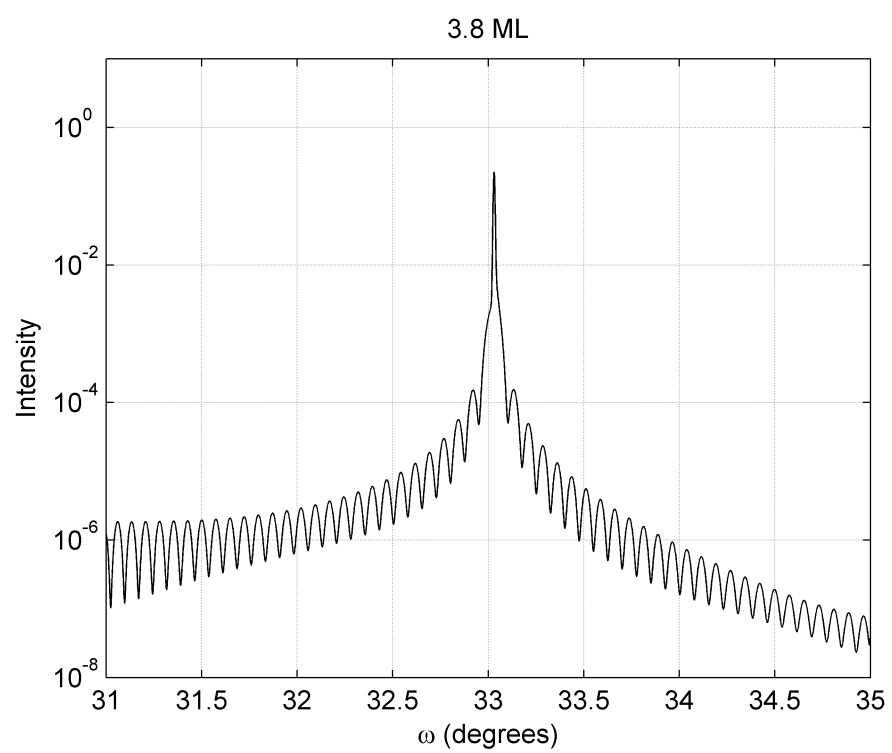


Figure 2.80

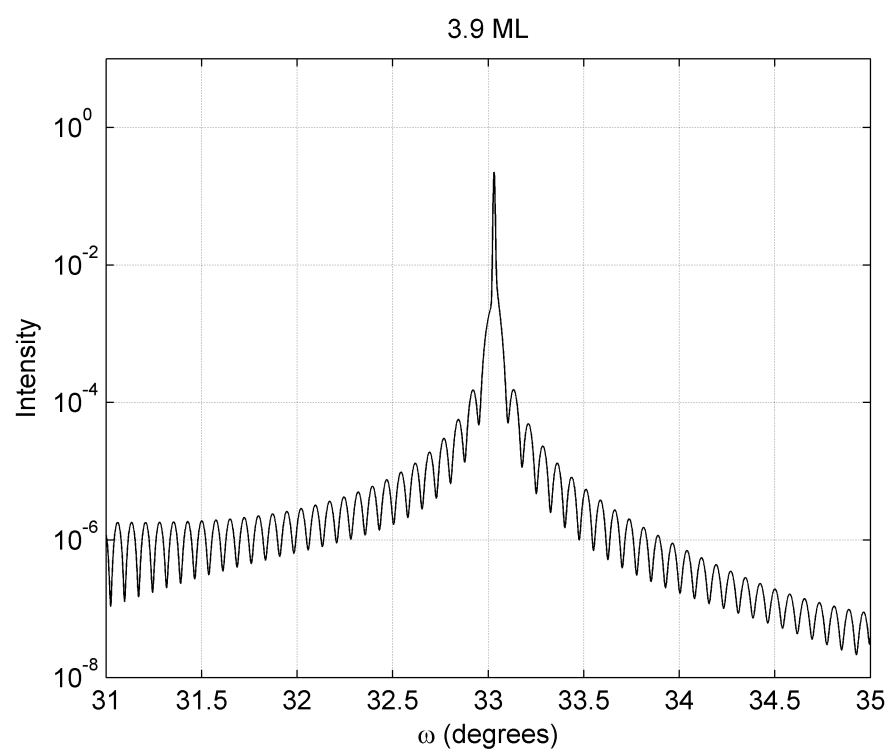


Figure 2.81

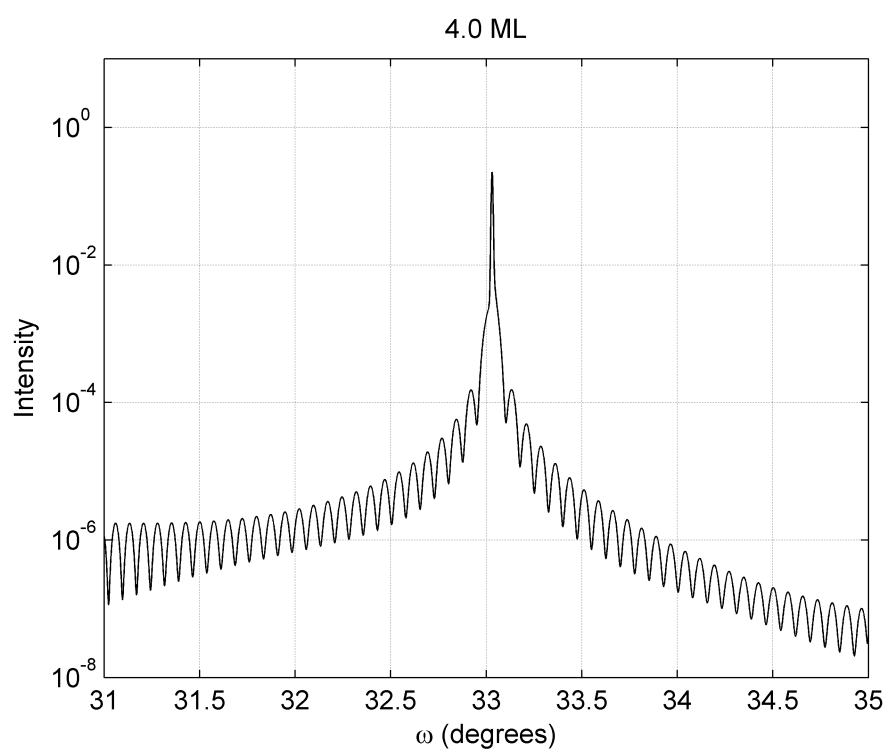


Figure 2.82

2.3 No top InAs layer

Simulations with varying InAs bottom layer thickness and no InAs top layer are shown in figures 2.83–2.123. They are arranged by increasing bottom layer thickness from 0 to 4 monolayers.

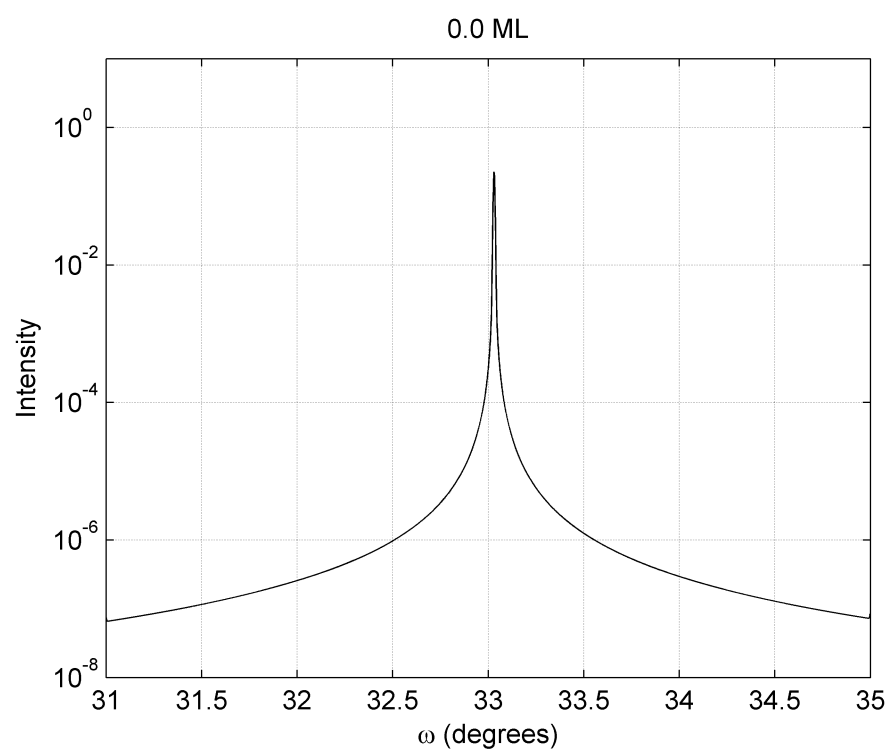


Figure 2.83

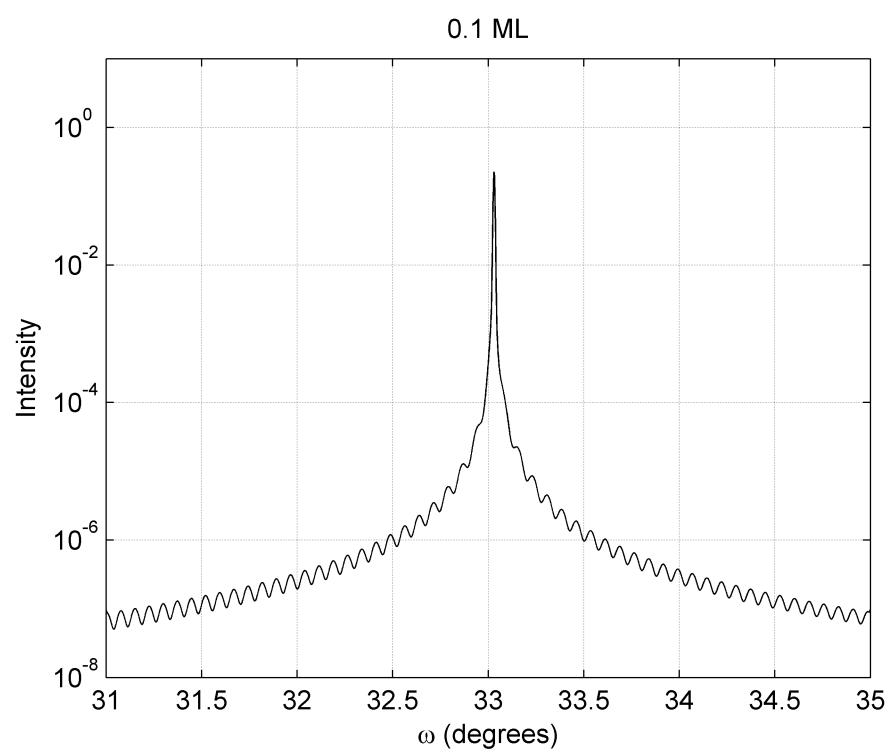


Figure 2.84

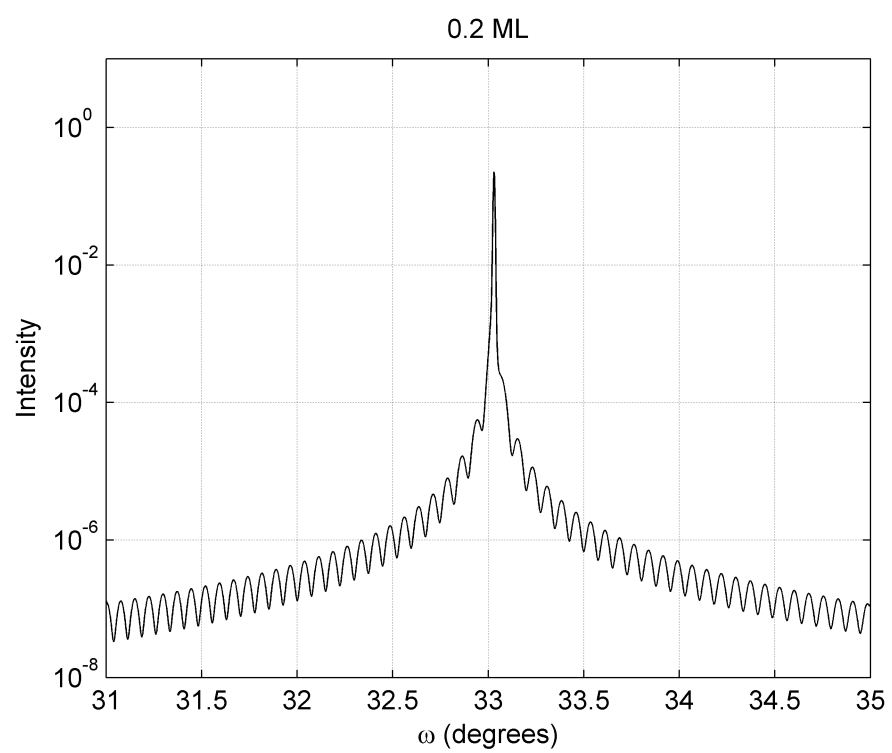


Figure 2.85

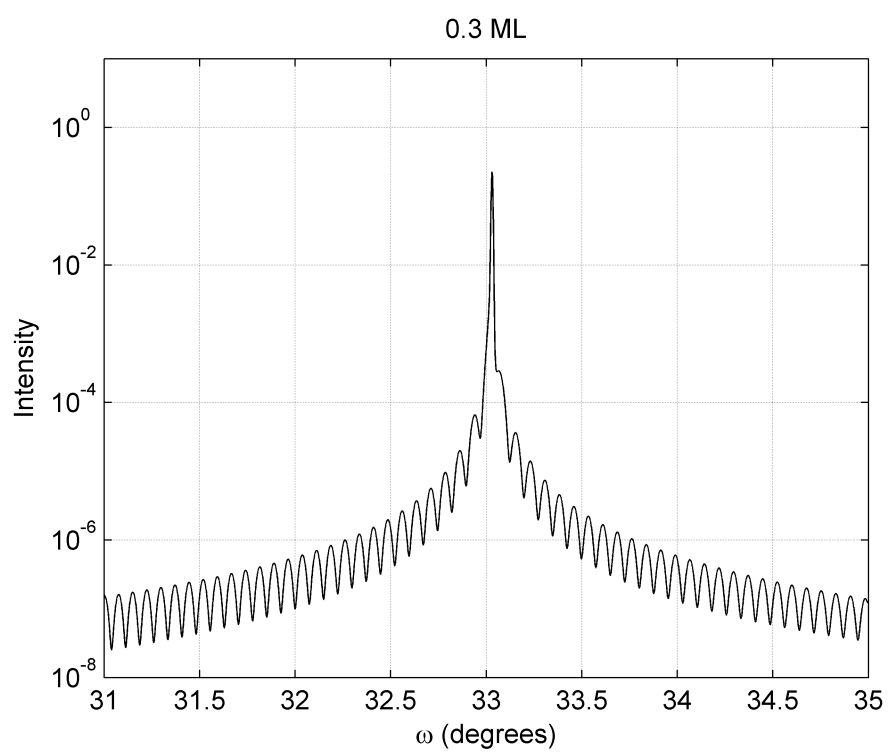


Figure 2.86

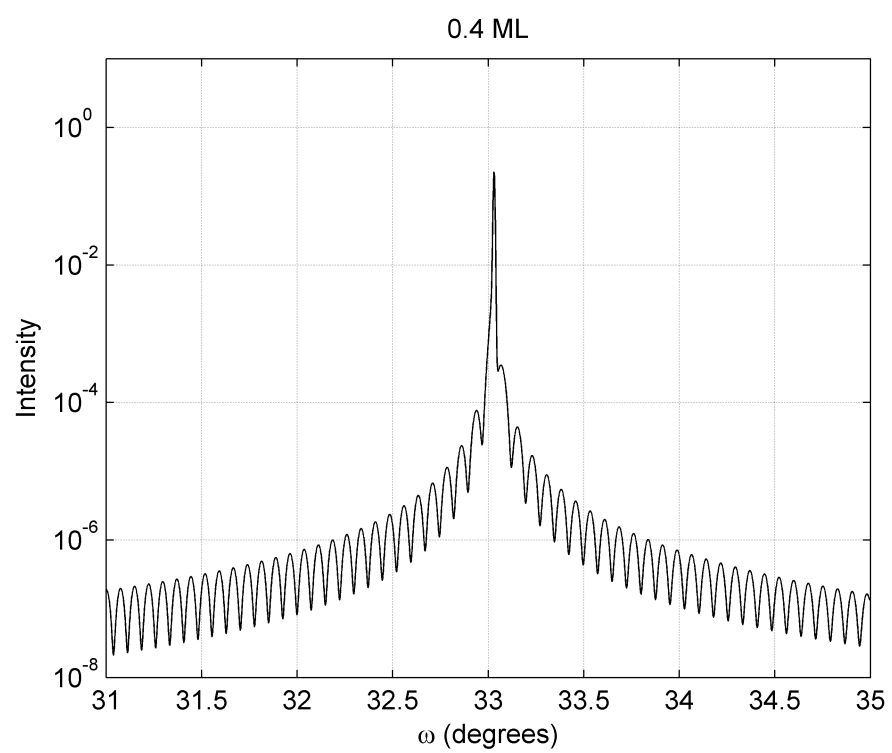


Figure 2.87

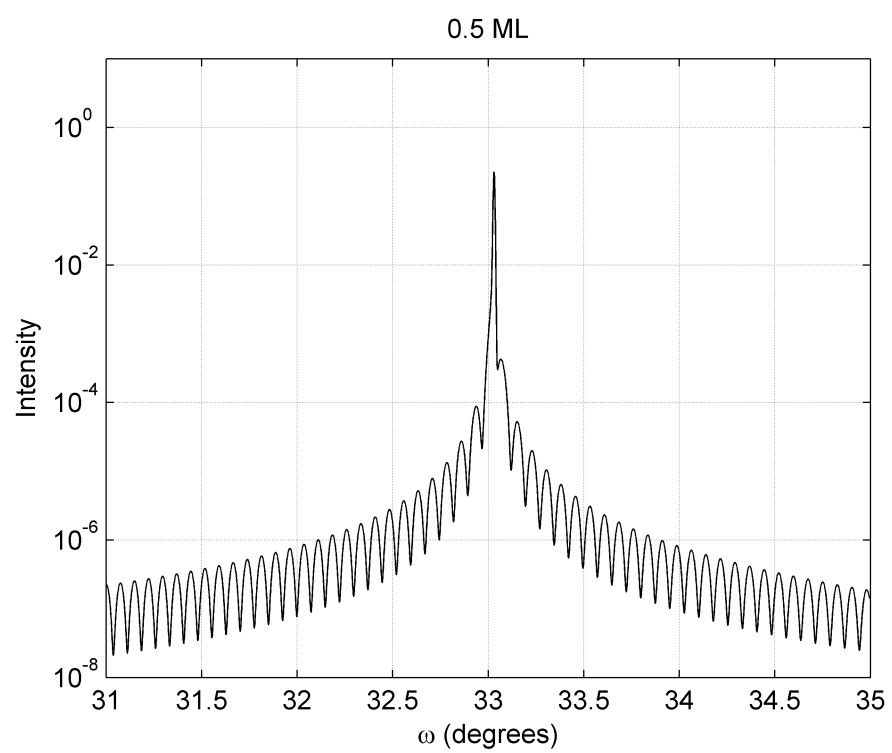


Figure 2.88

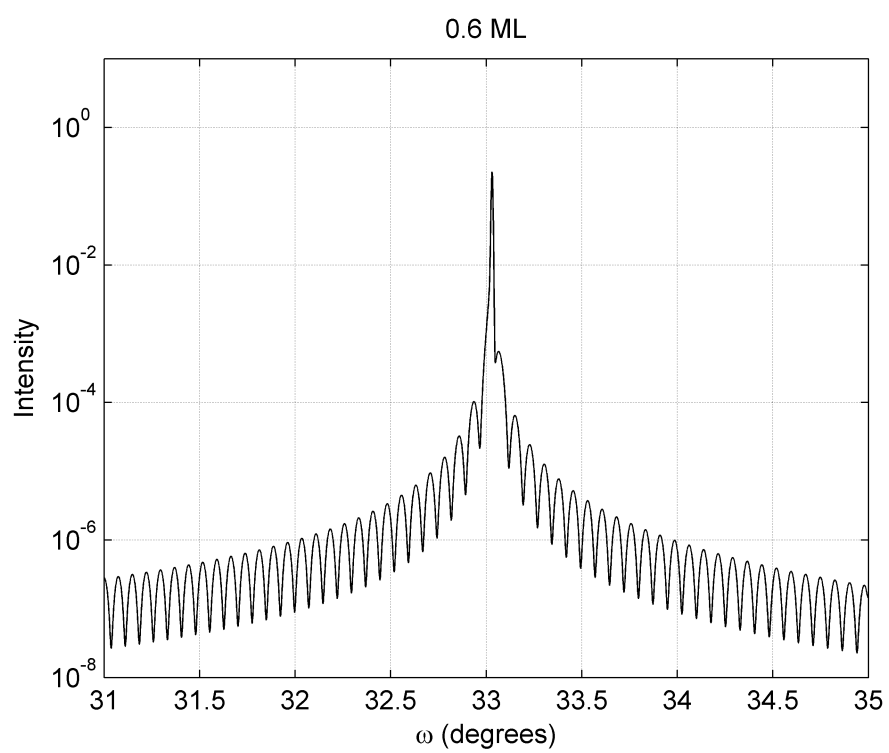


Figure 2.89

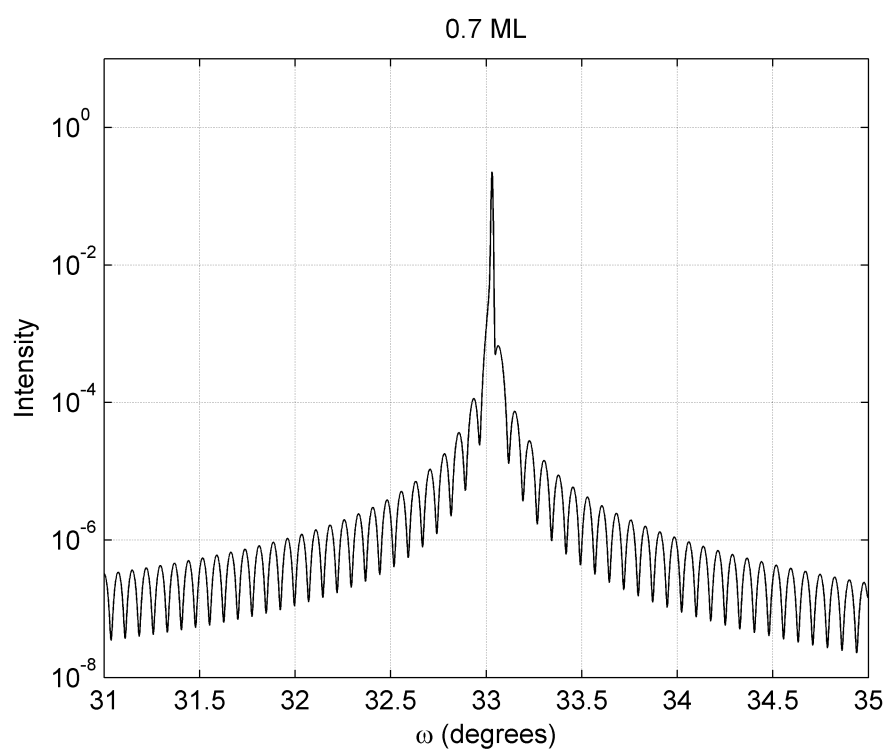


Figure 2.90

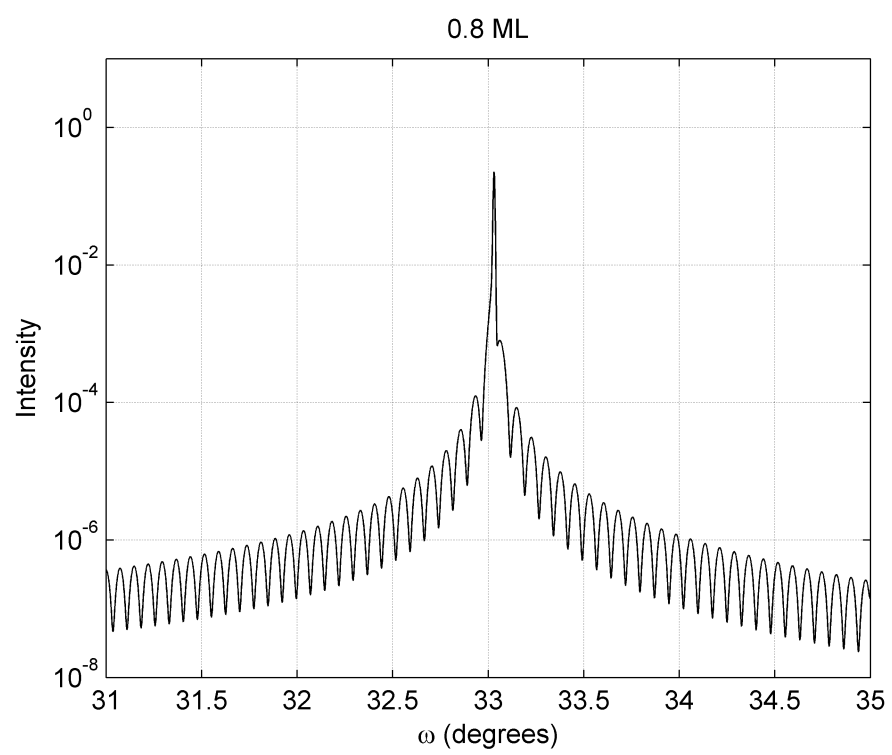


Figure 2.91

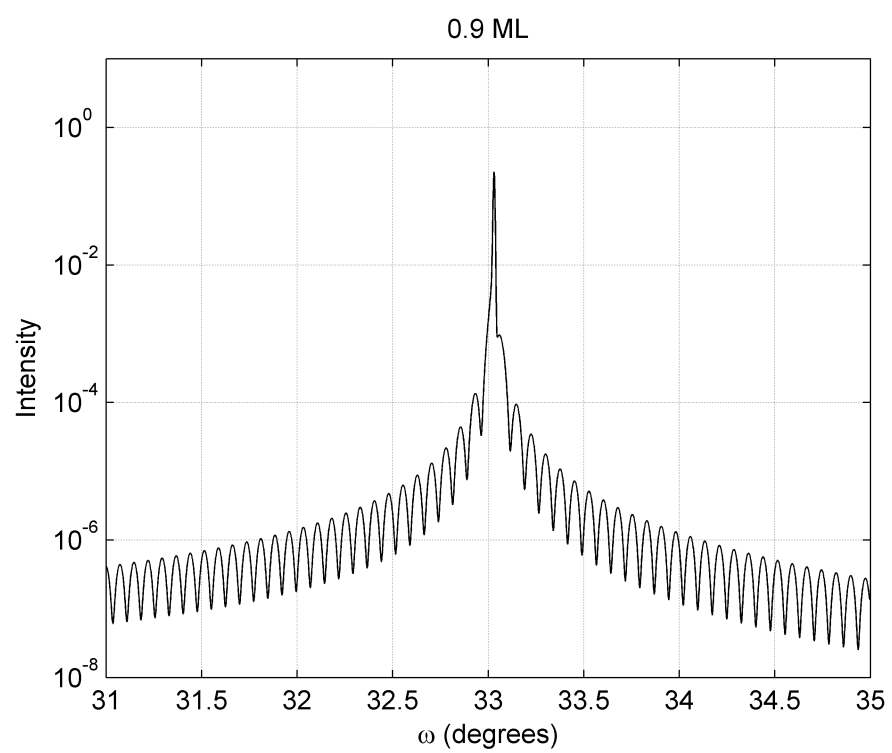


Figure 2.92

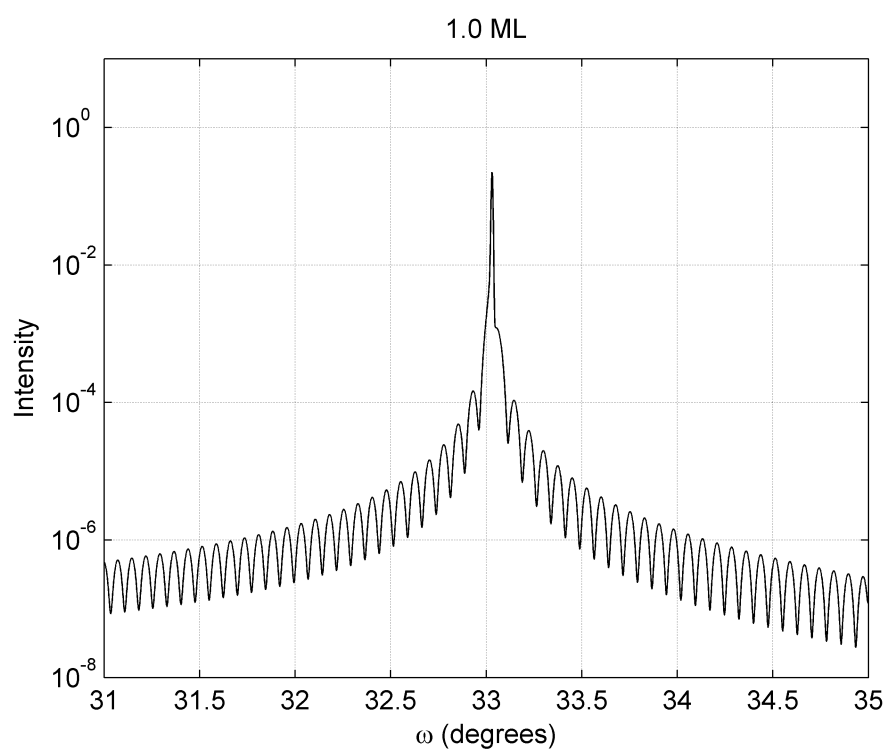


Figure 2.93

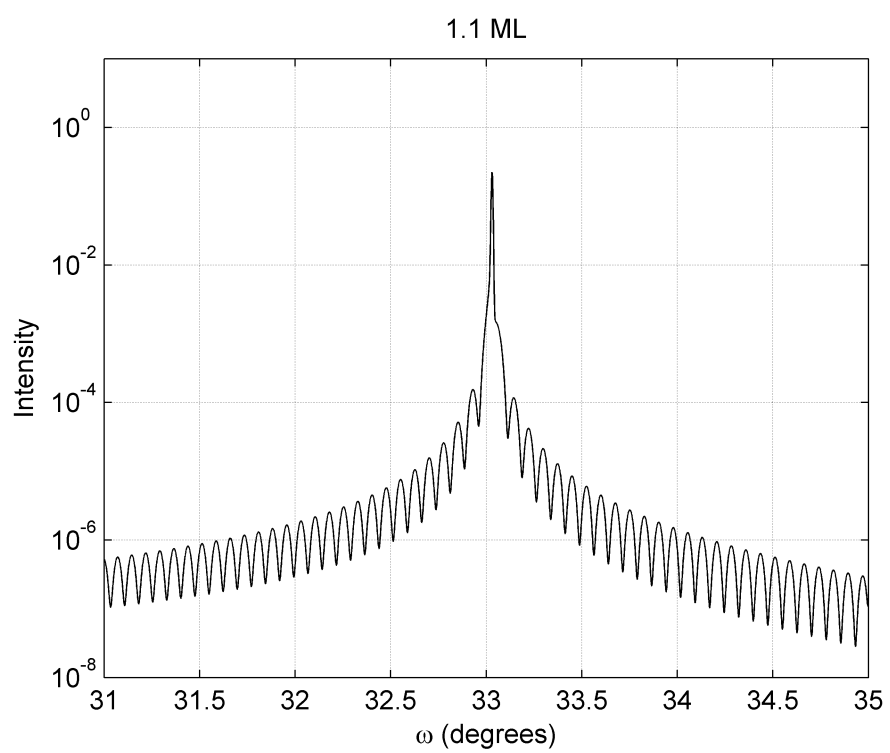


Figure 2.94

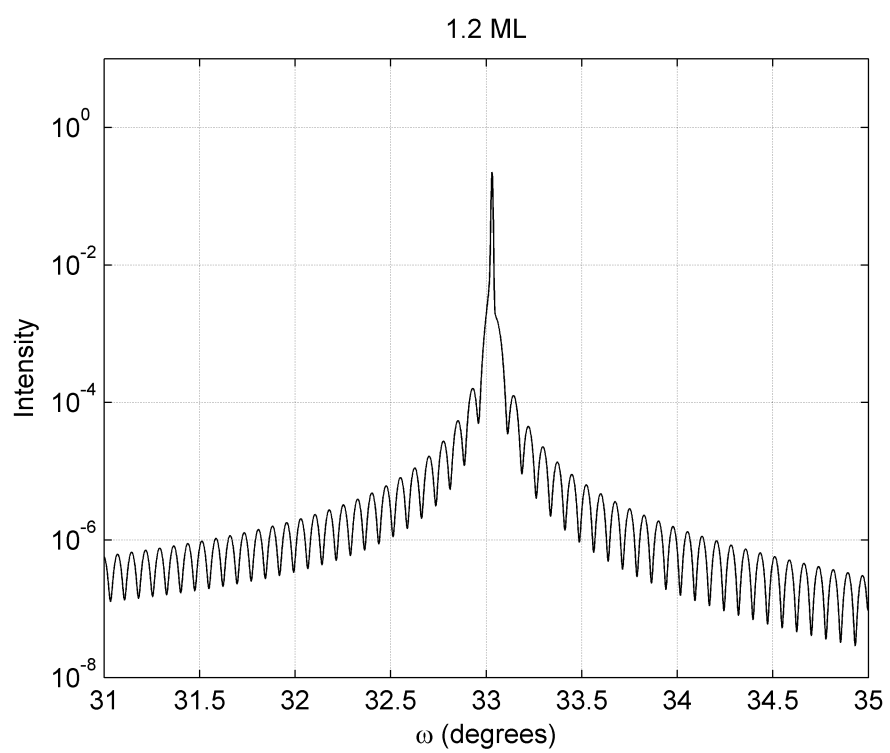


Figure 2.95

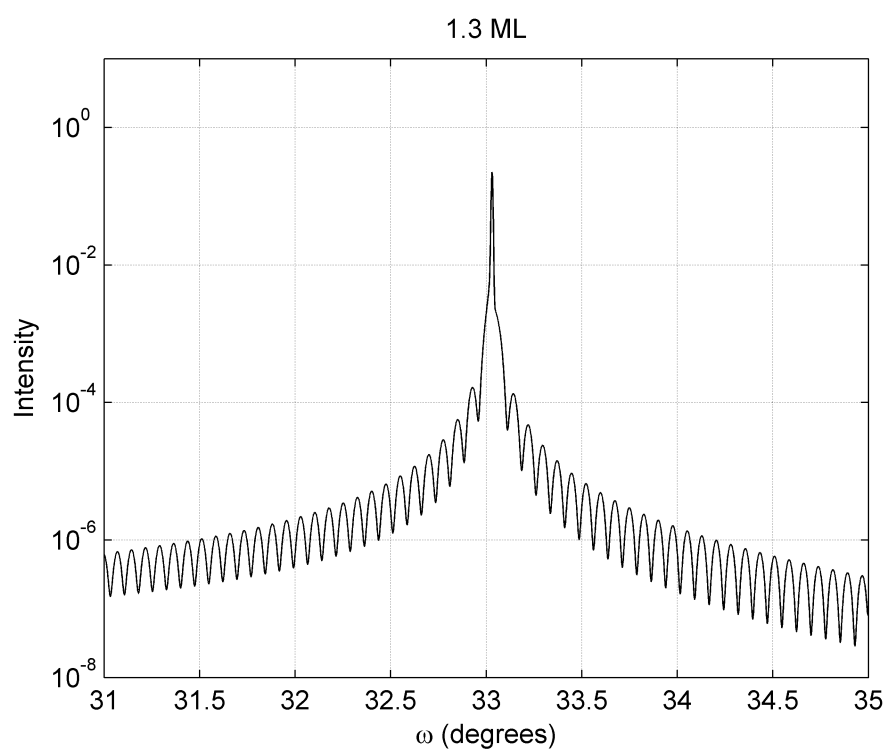


Figure 2.96

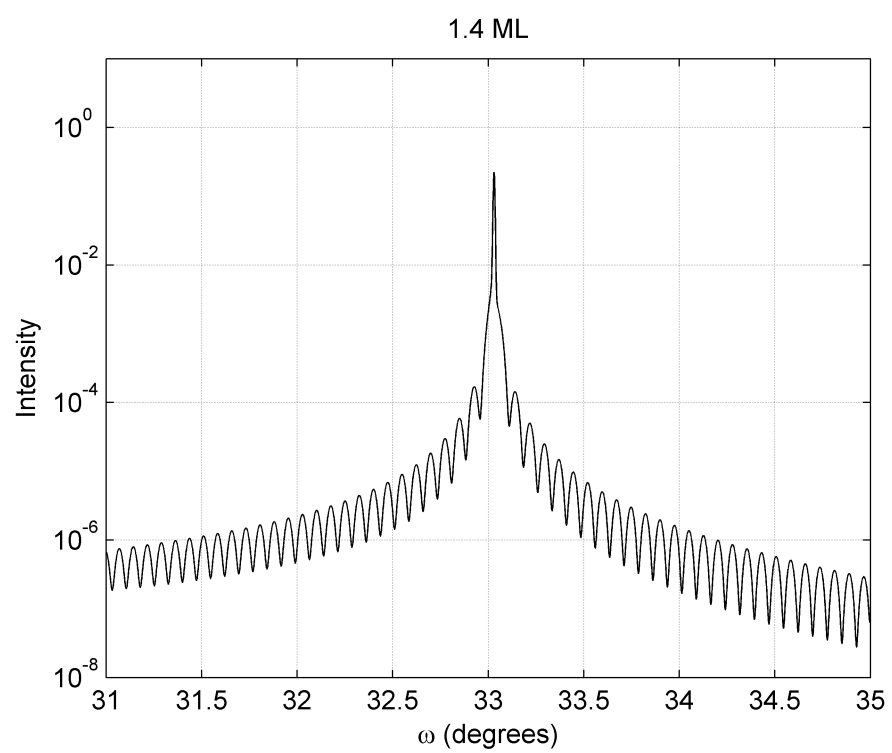


Figure 2.97

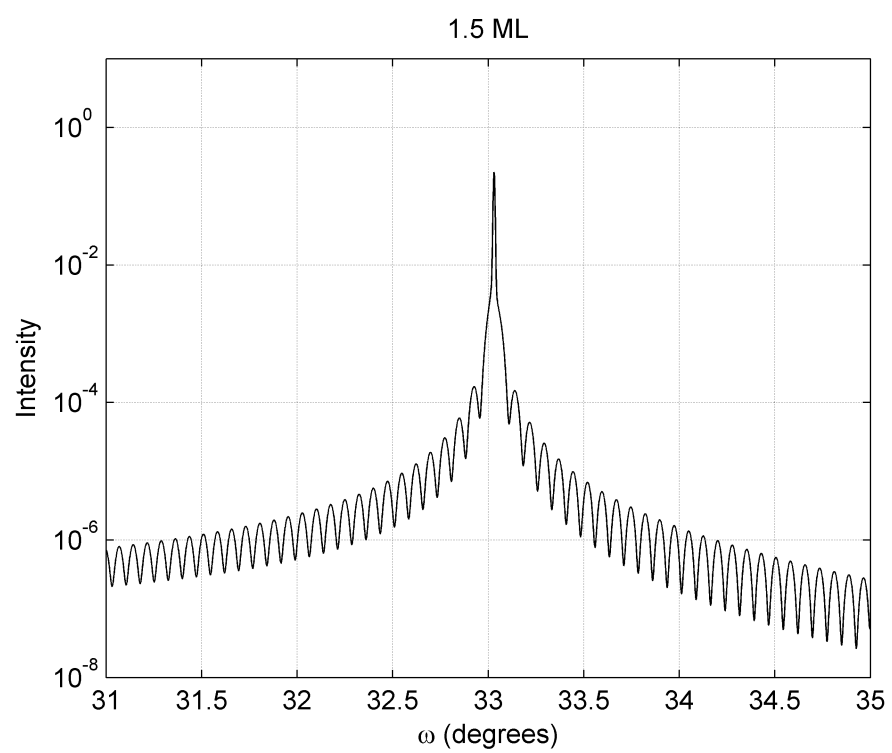


Figure 2.98

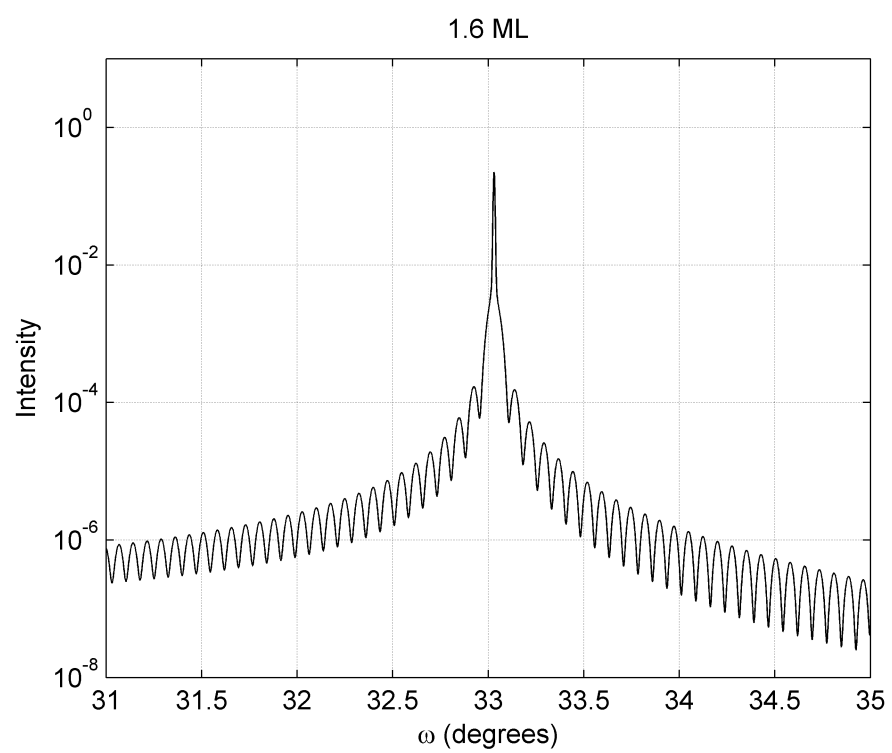


Figure 2.99

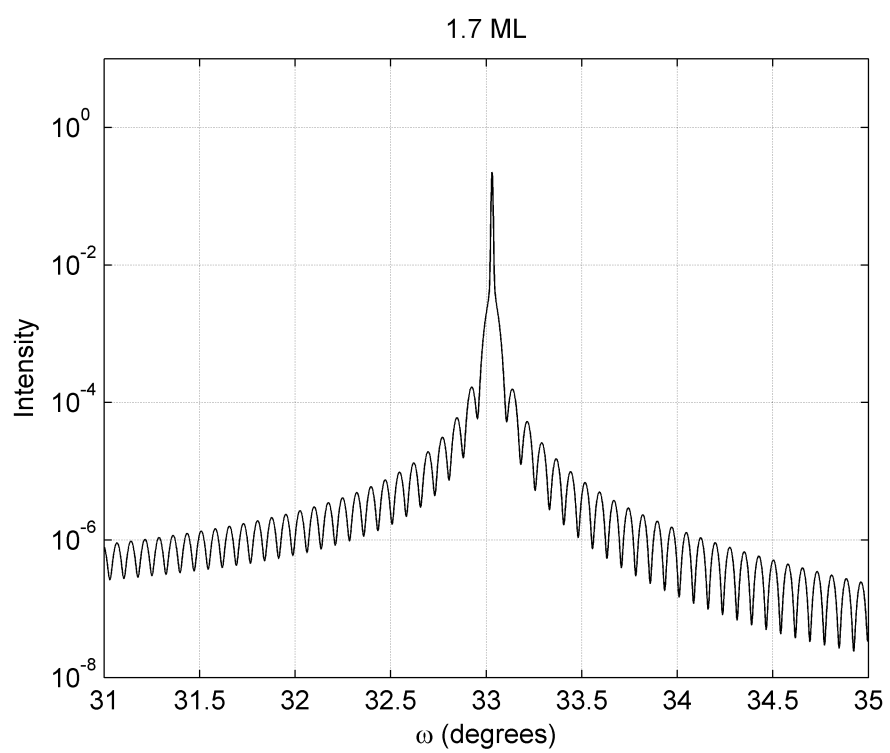


Figure 2.100

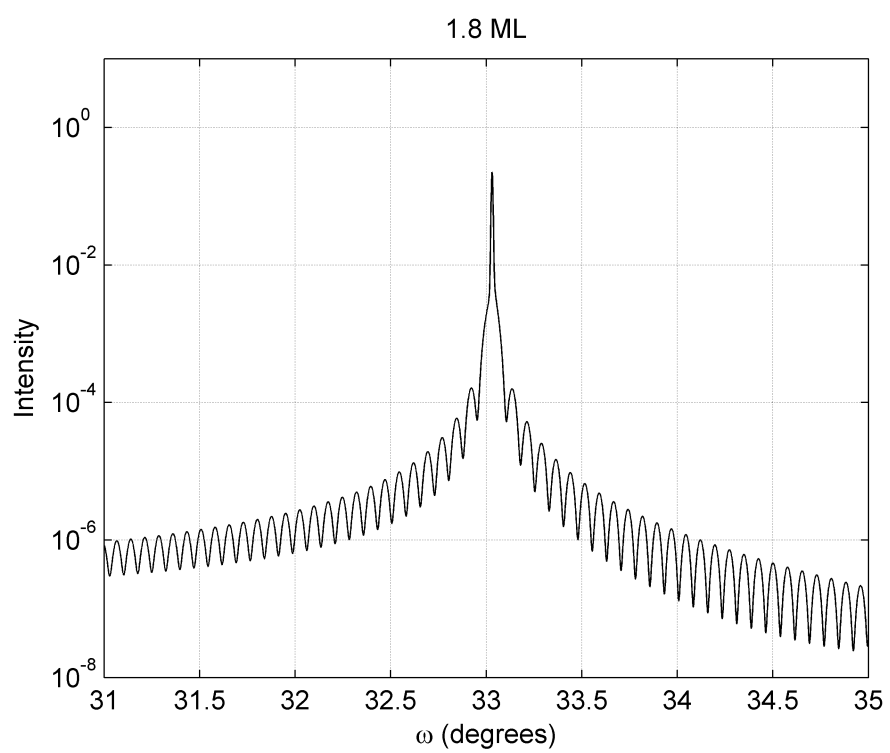


Figure 2.101

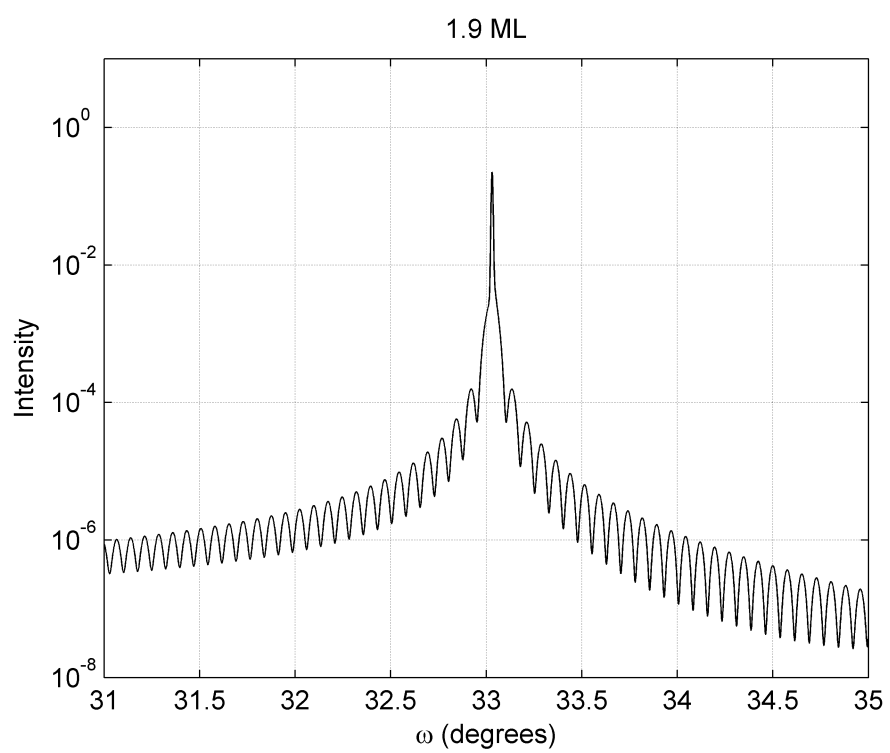


Figure 2.102

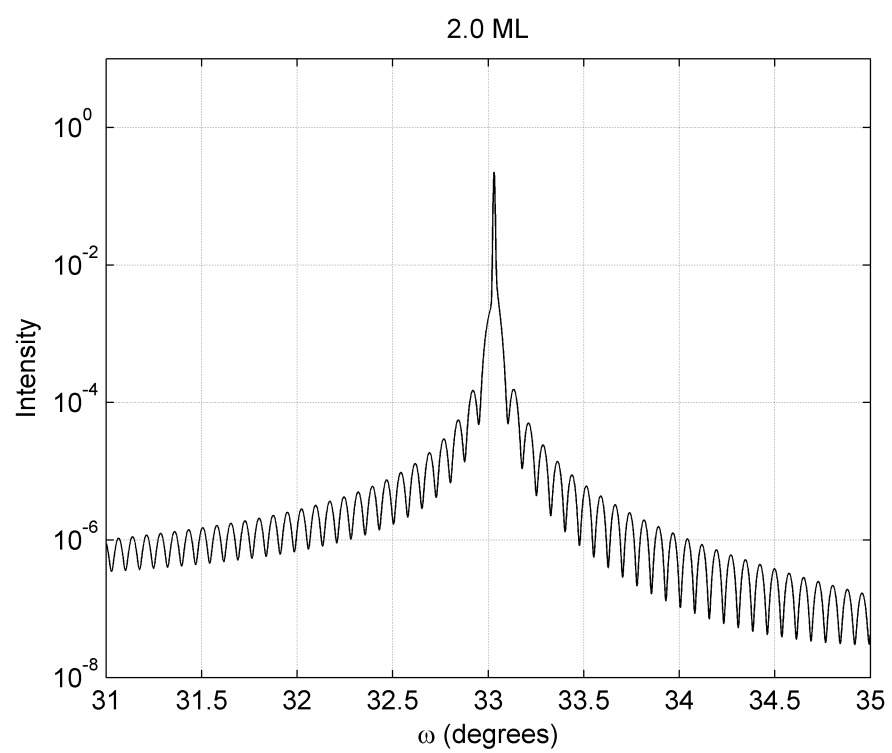


Figure 2.103

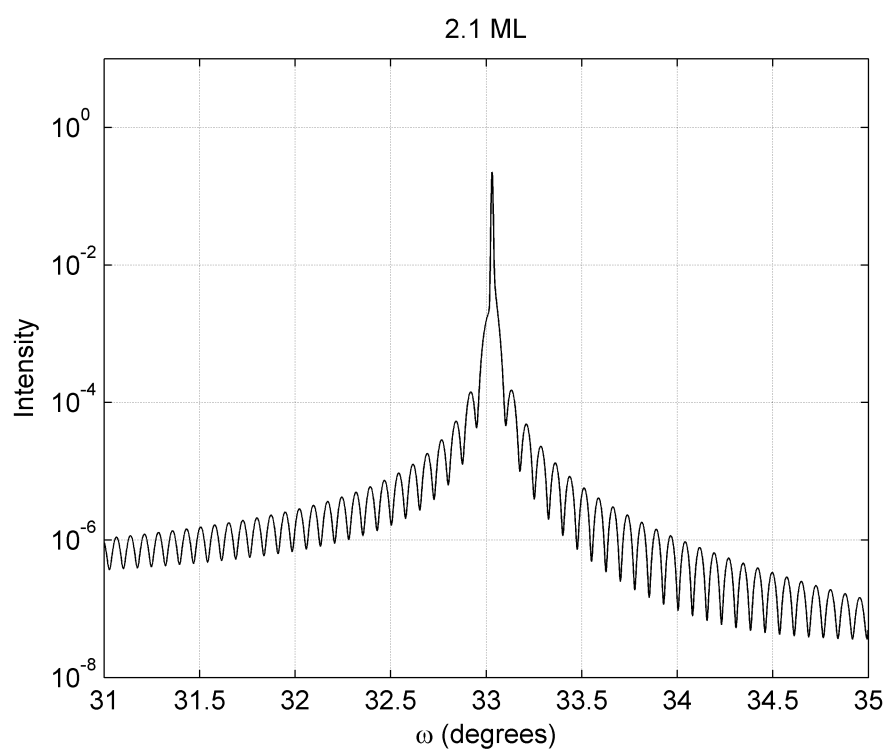


Figure 2.104

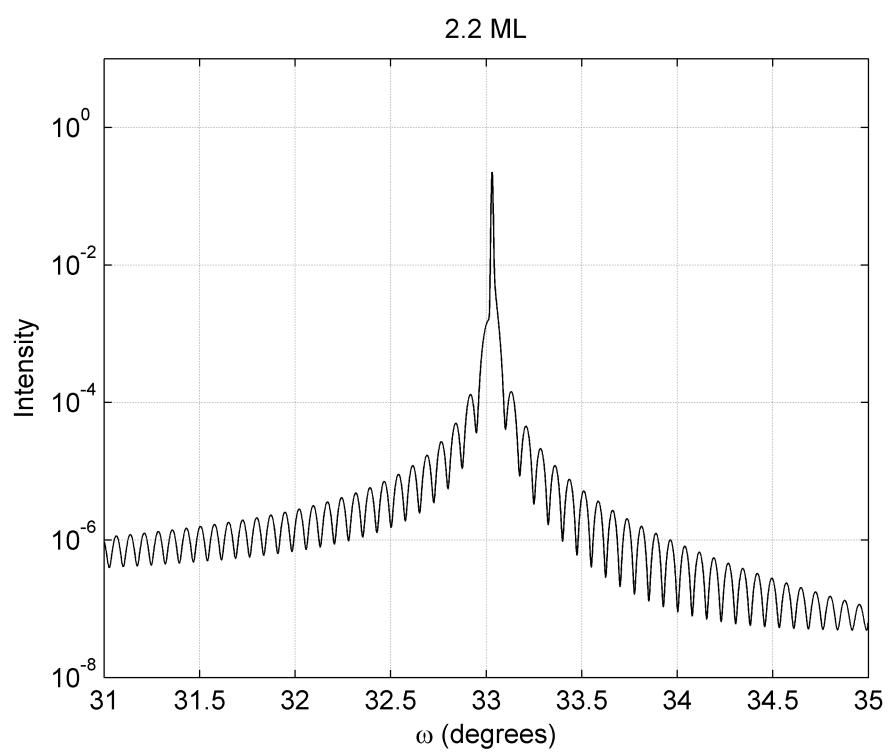


Figure 2.105

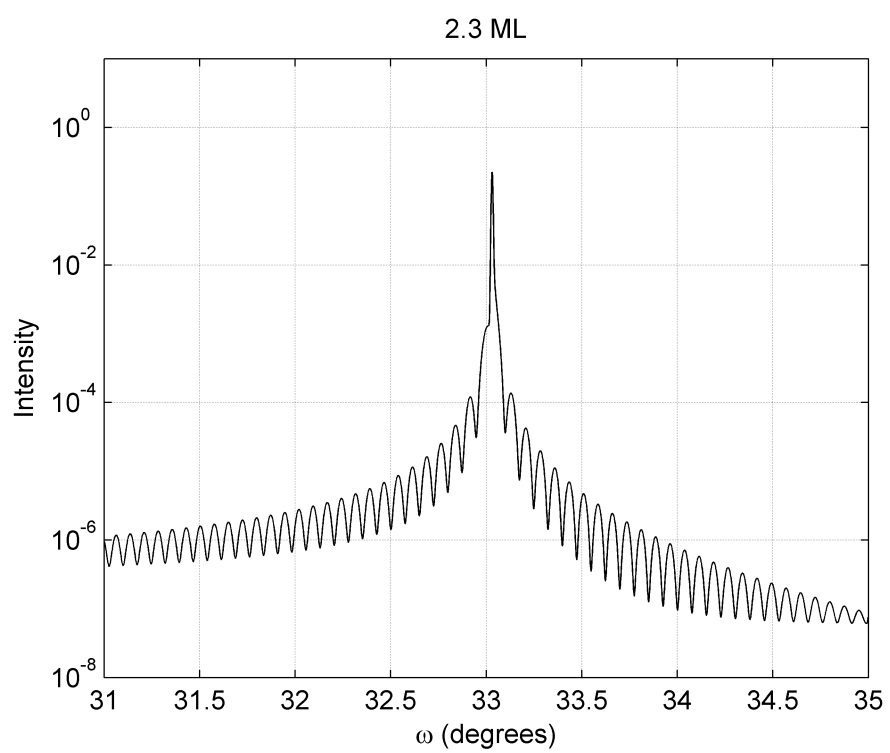


Figure 2.106

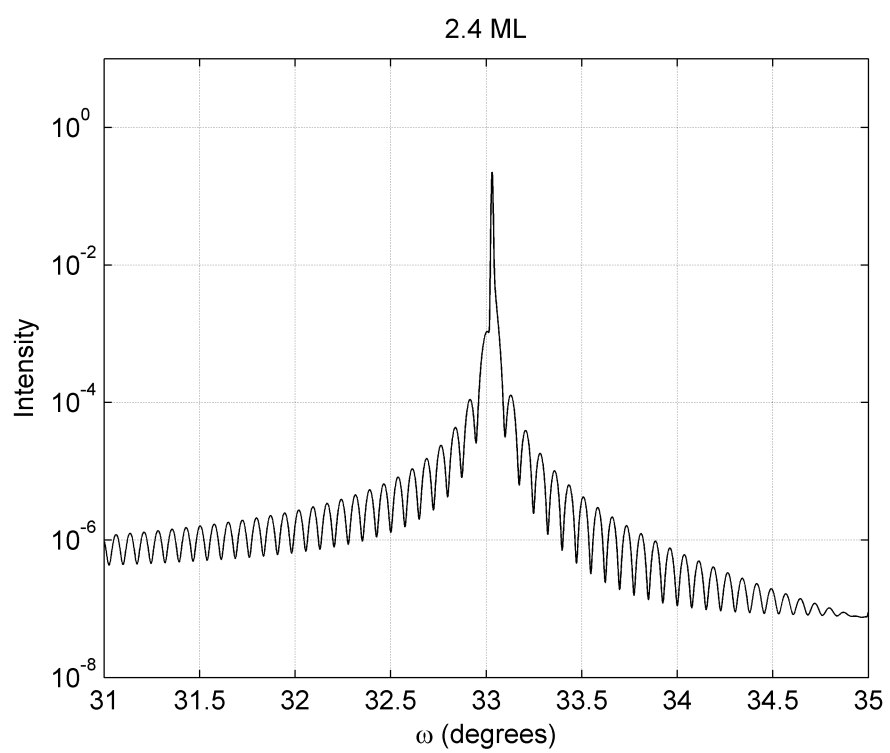


Figure 2.107

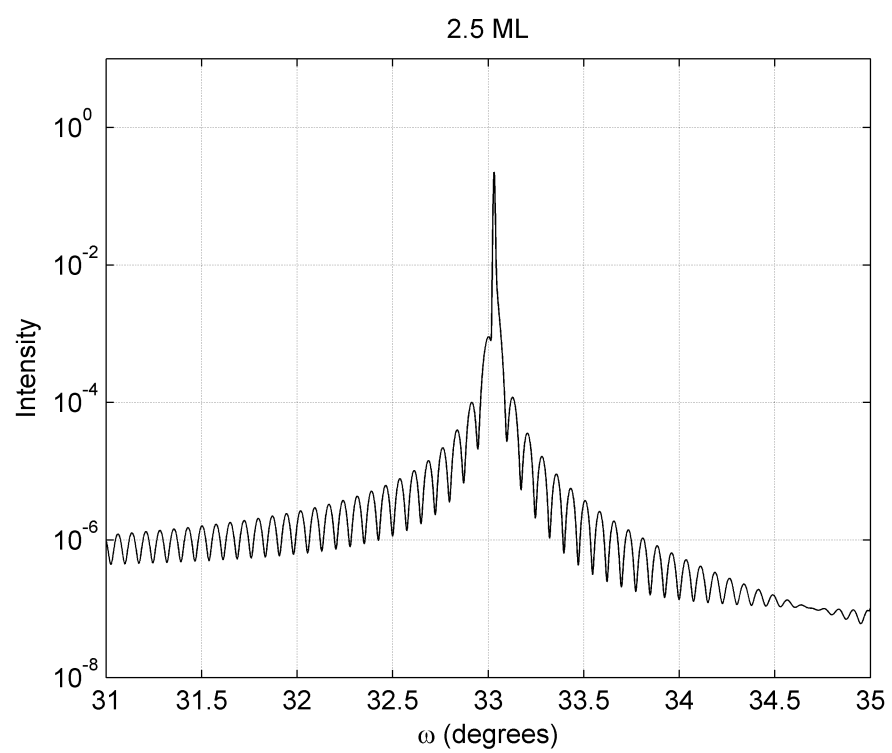


Figure 2.108

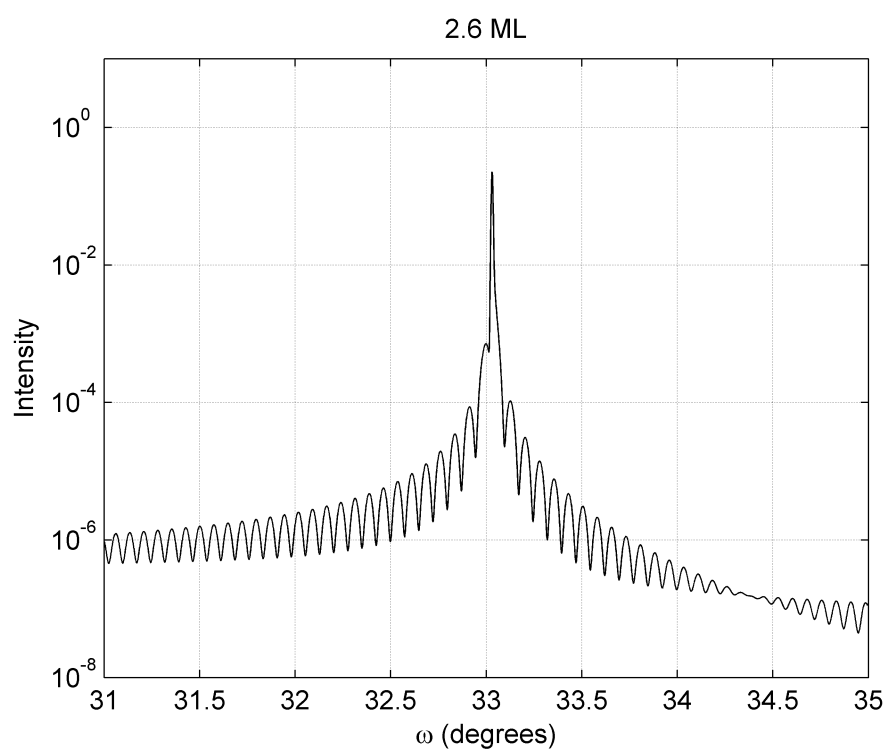


Figure 2.109

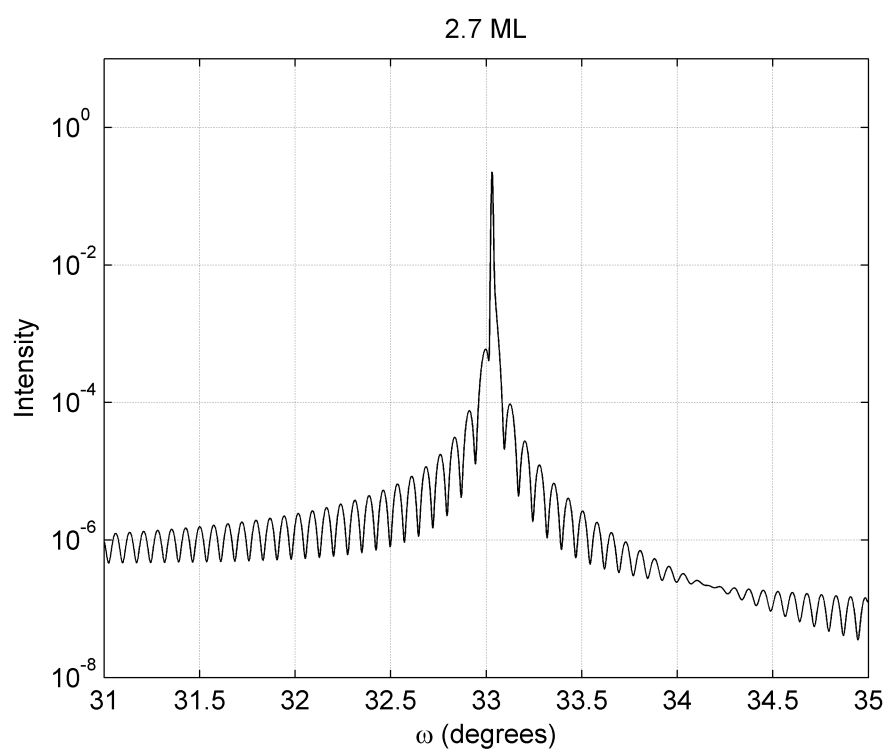


Figure 2.110

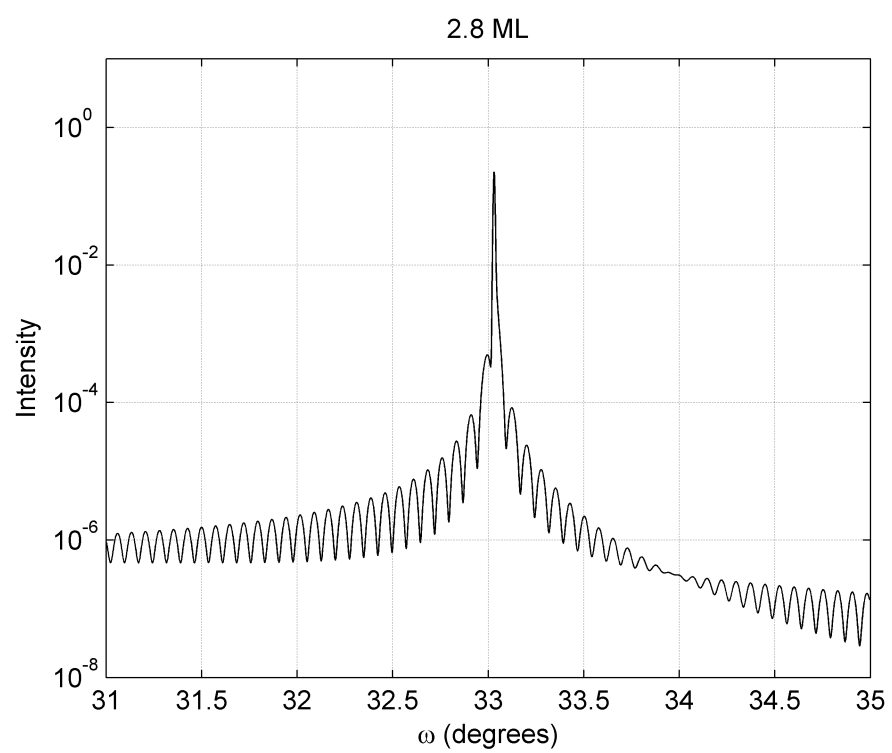


Figure 2.111

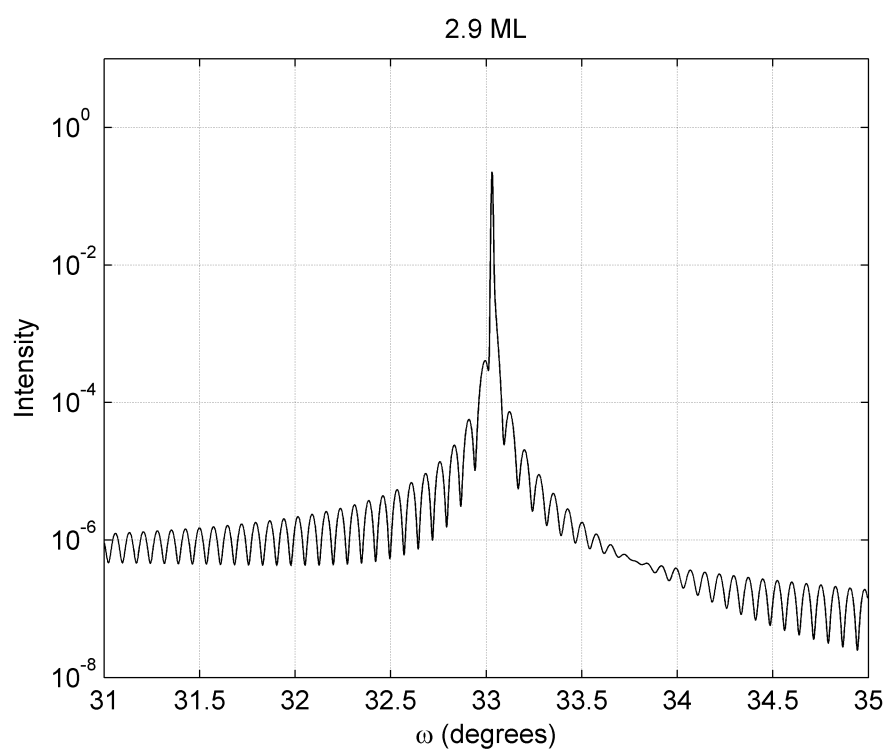


Figure 2.112

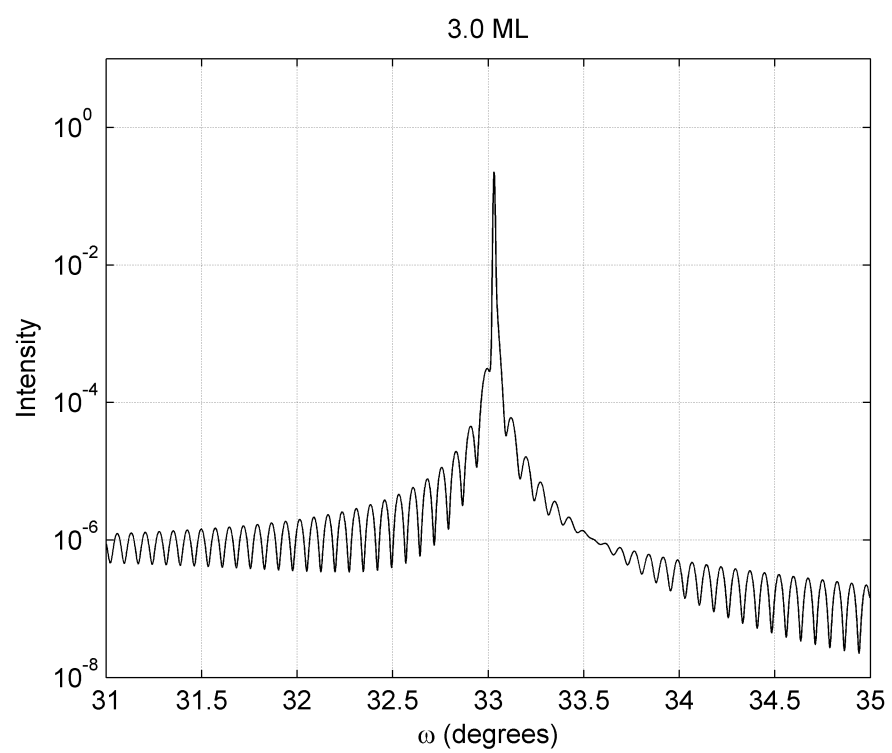


Figure 2.113

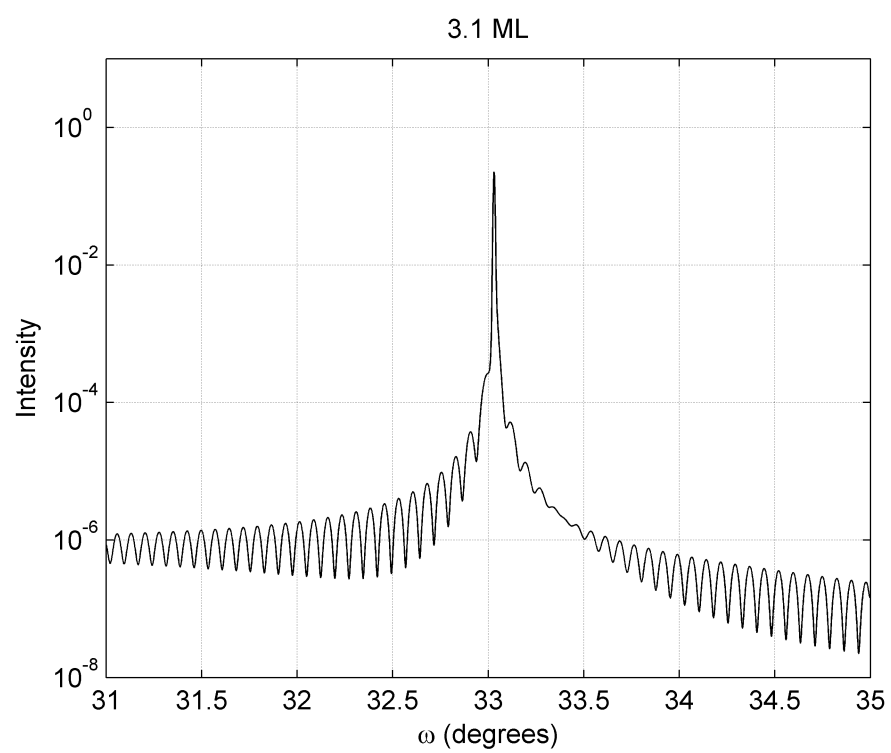


Figure 2.114

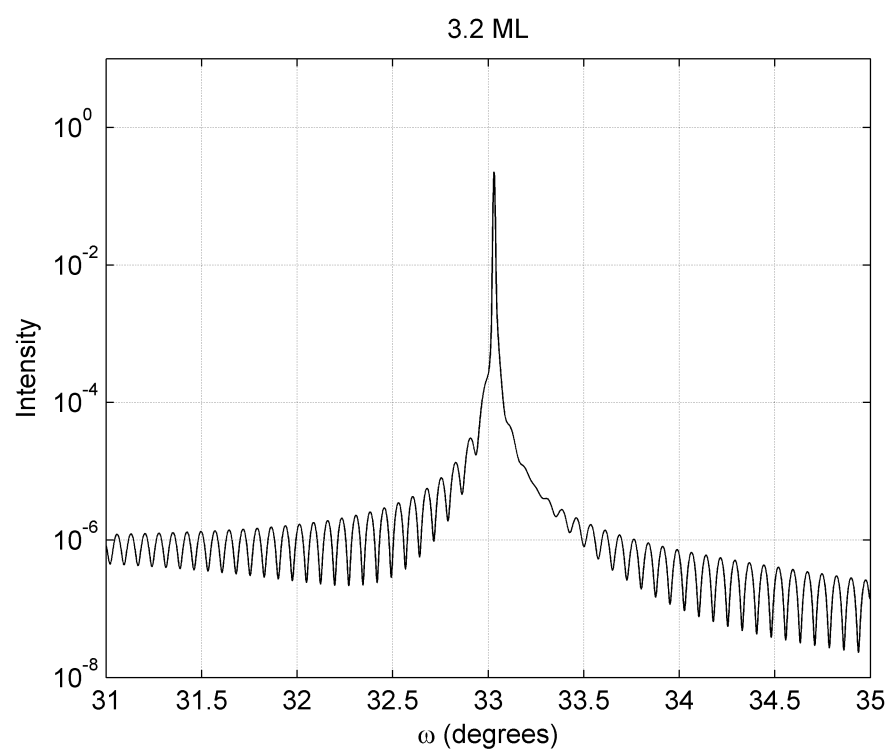


Figure 2.115

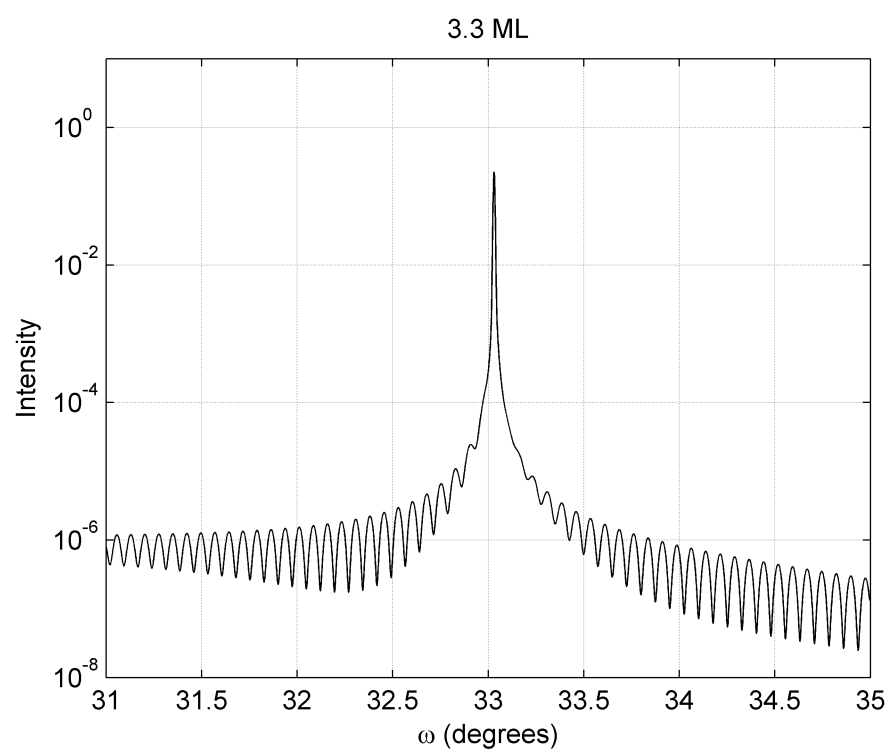


Figure 2.116

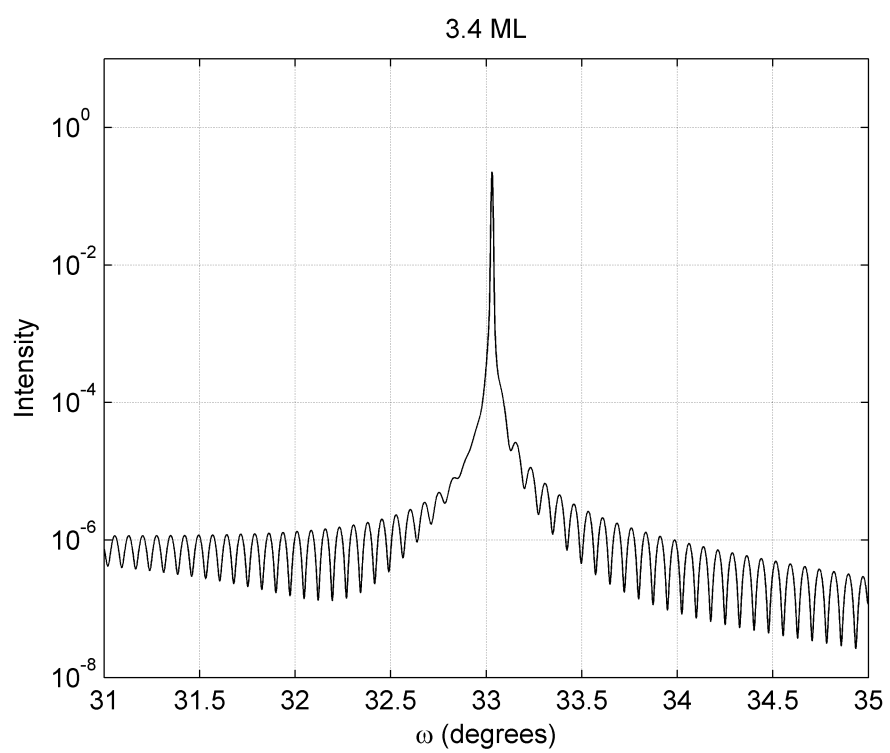


Figure 2.117

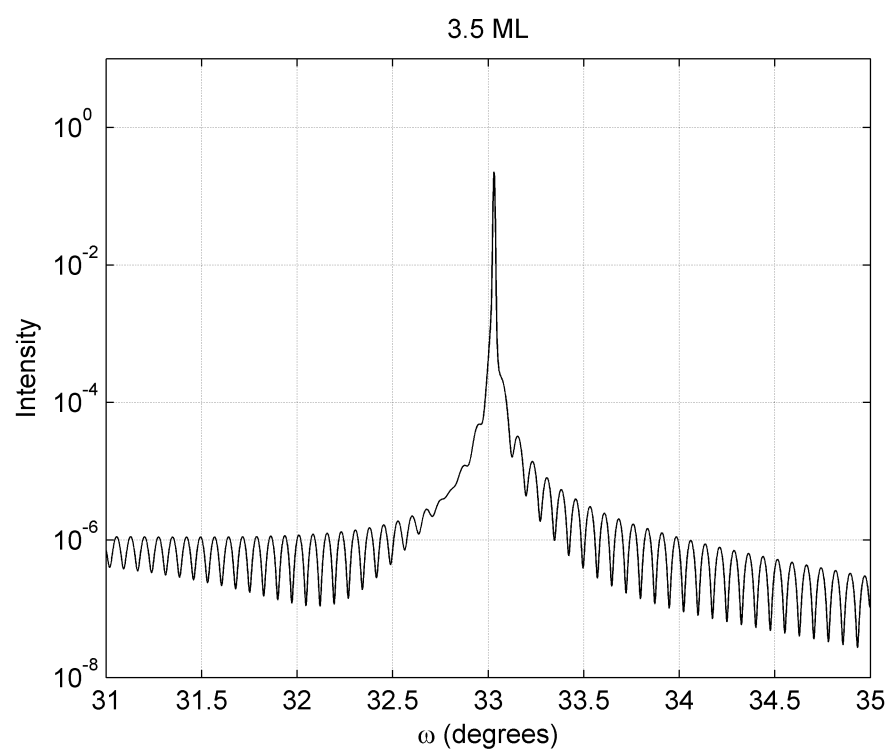


Figure 2.118

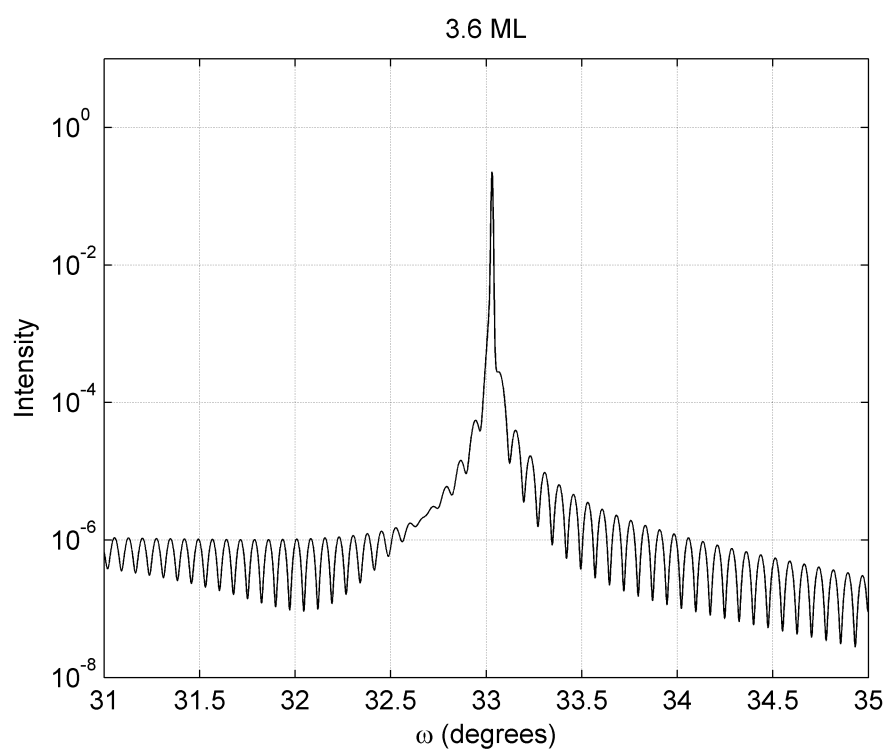


Figure 2.119

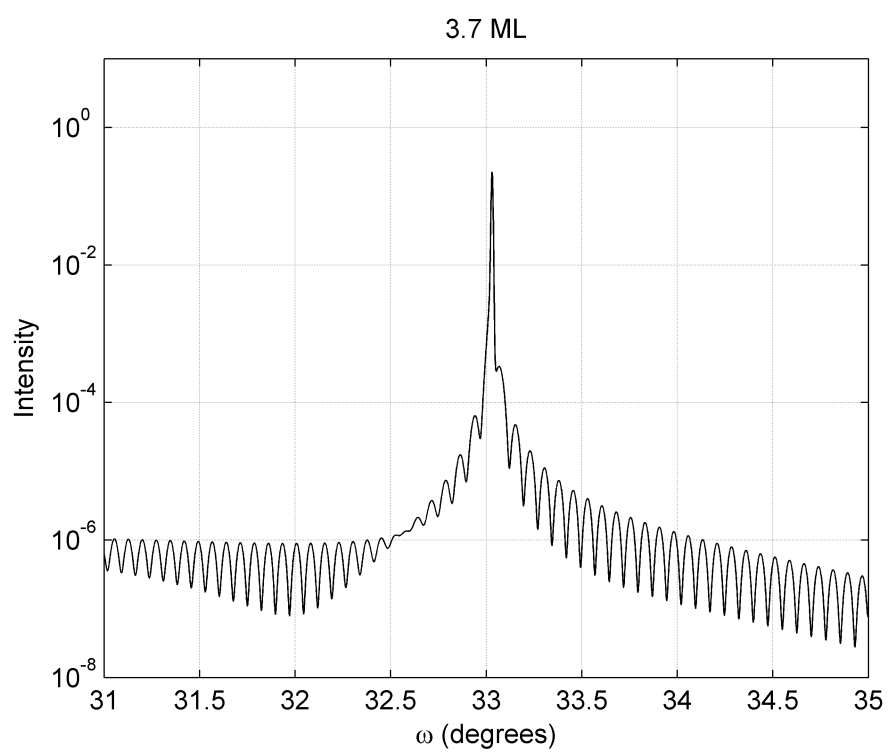


Figure 2.120

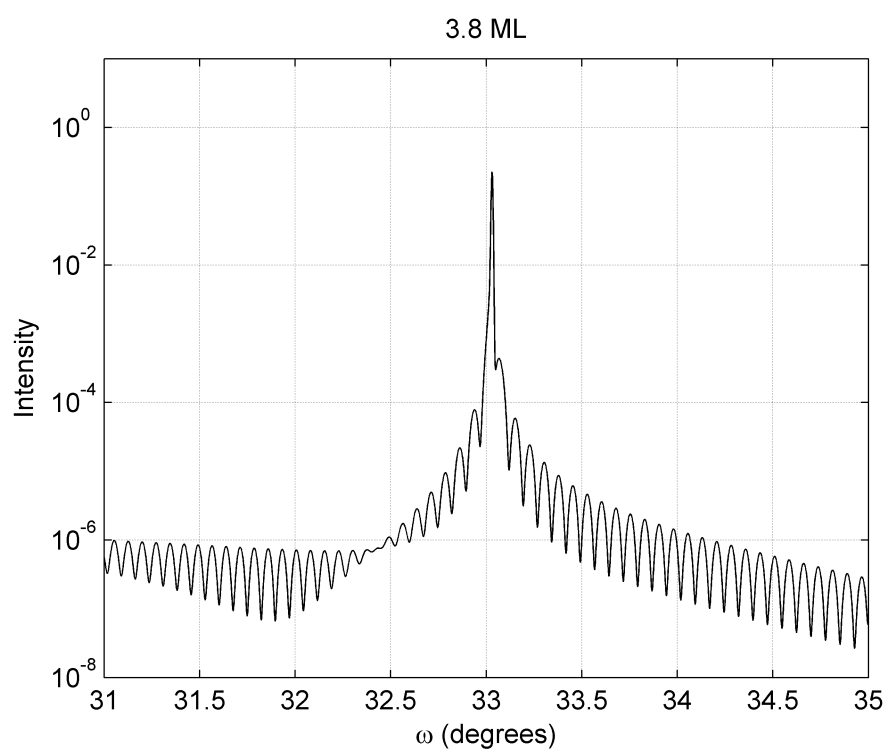


Figure 2.121

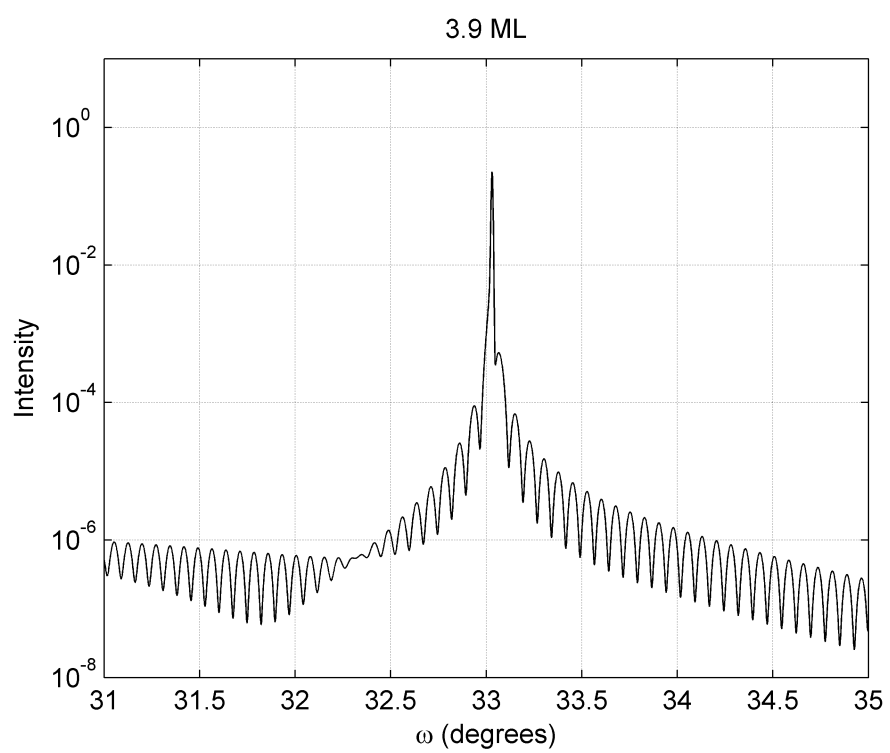


Figure 2.122

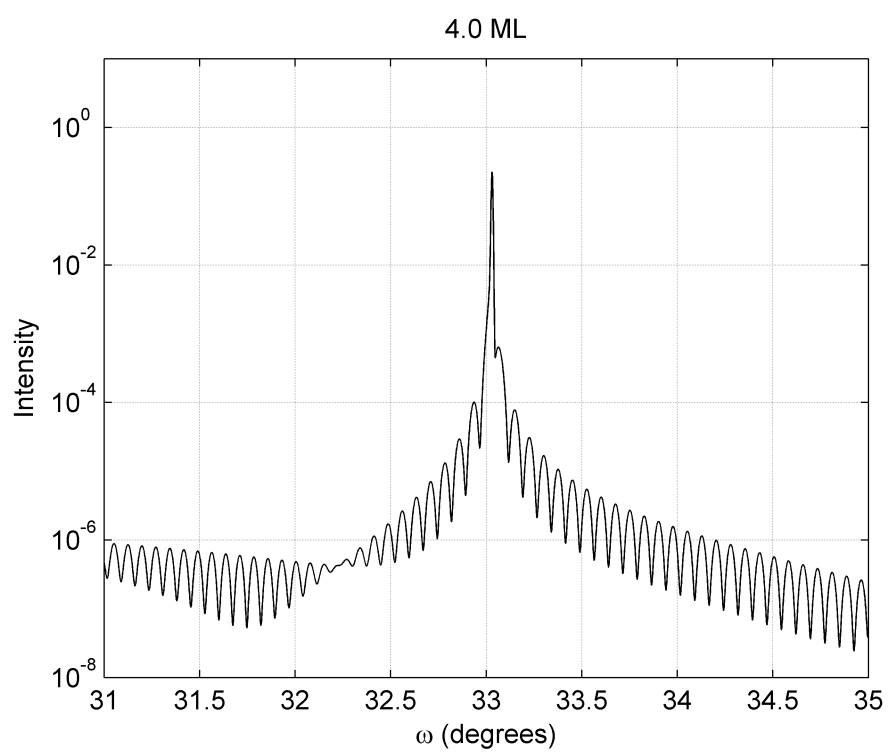


Figure 2.123

2.4 Both top and bottom InAs layer

Simulations with varying InAs bottom and top layer thicknesses are shown in figures 2.124–2.164. They are arranged by increasing layer thicknesses from 0 to 4 monolayers.

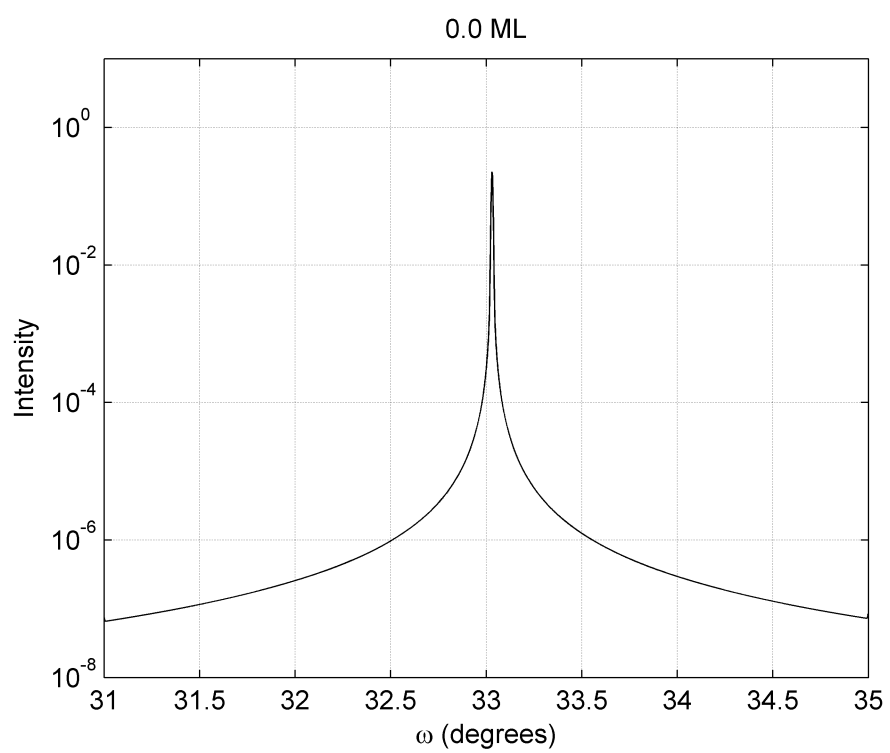


Figure 2.124

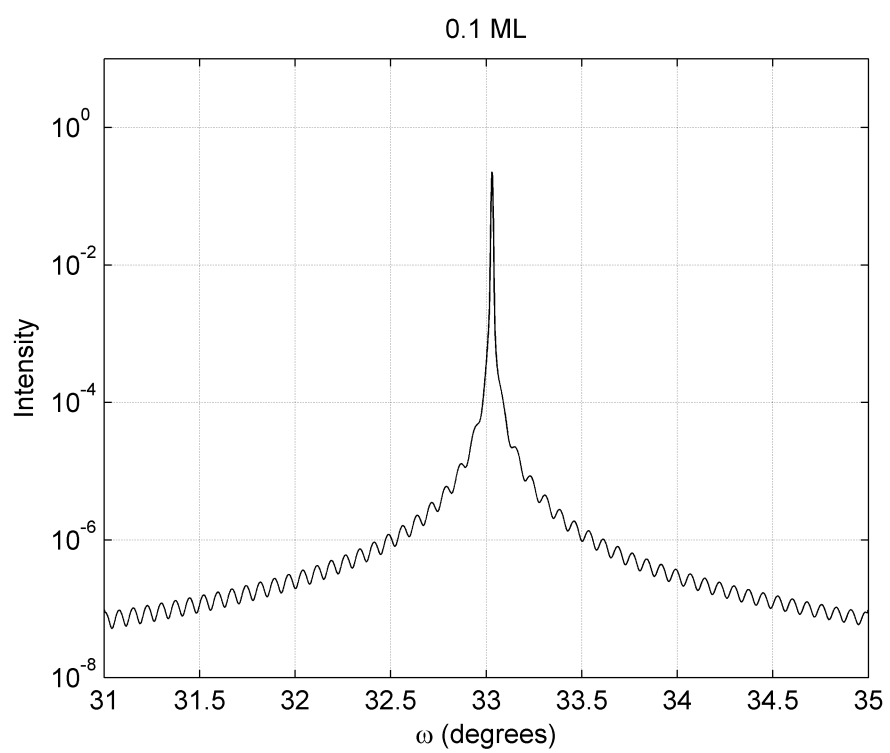


Figure 2.125

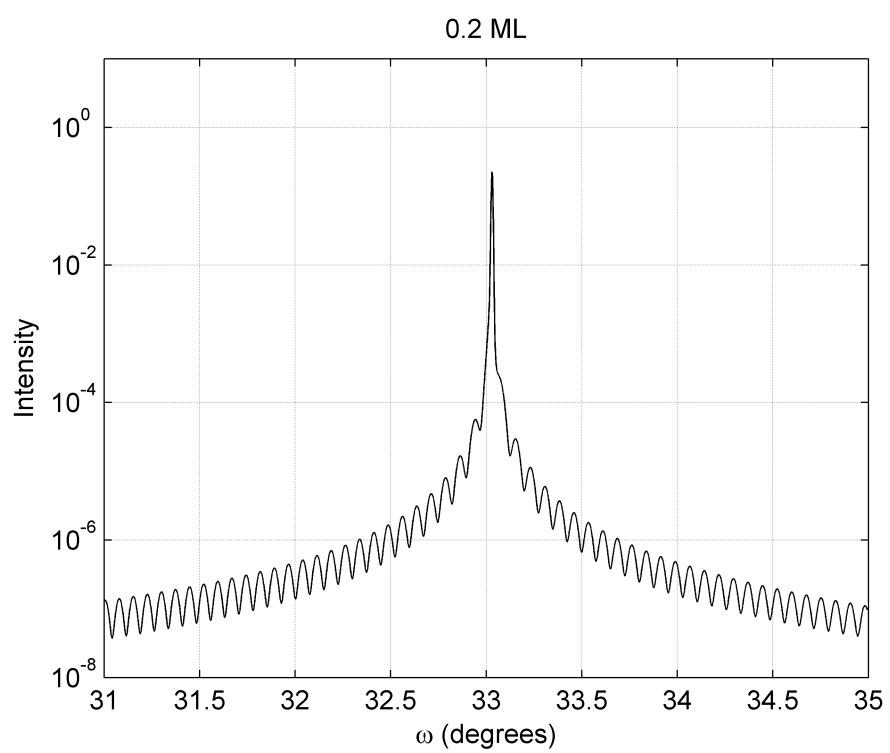


Figure 2.126

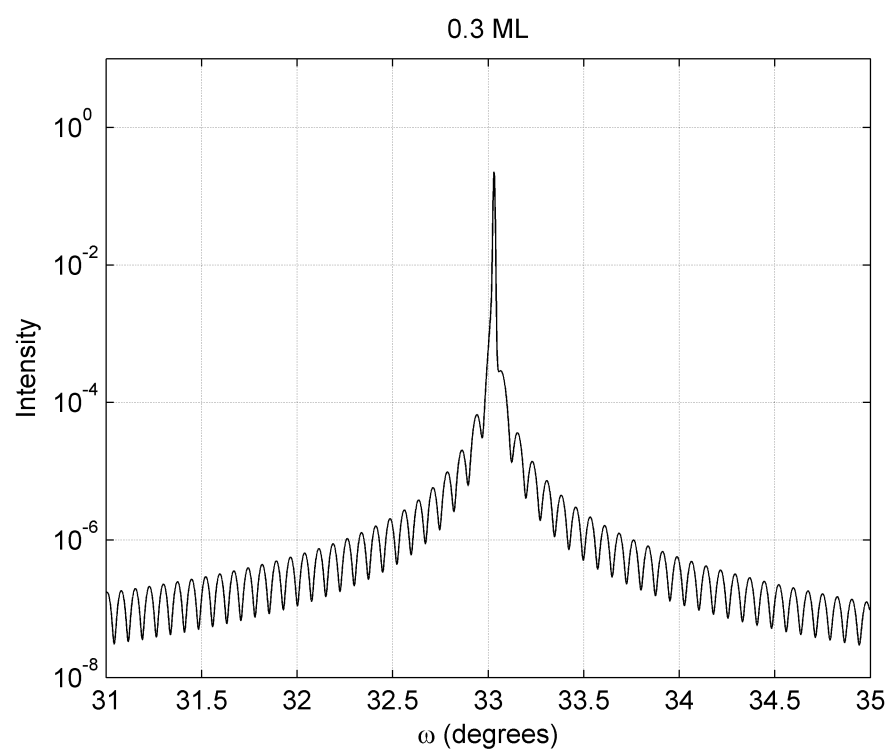


Figure 2.127

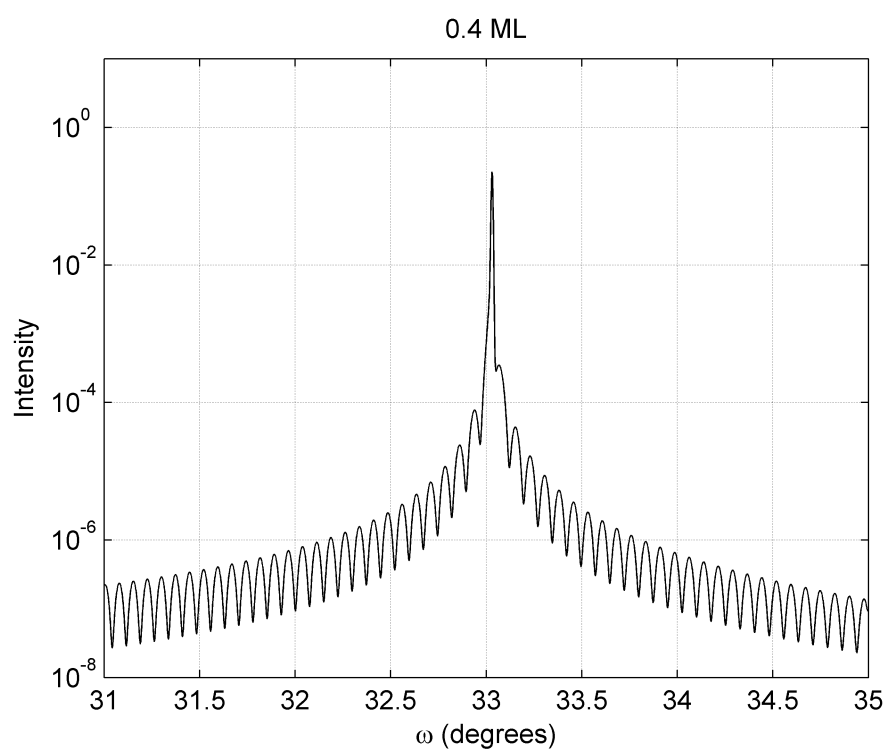


Figure 2.128

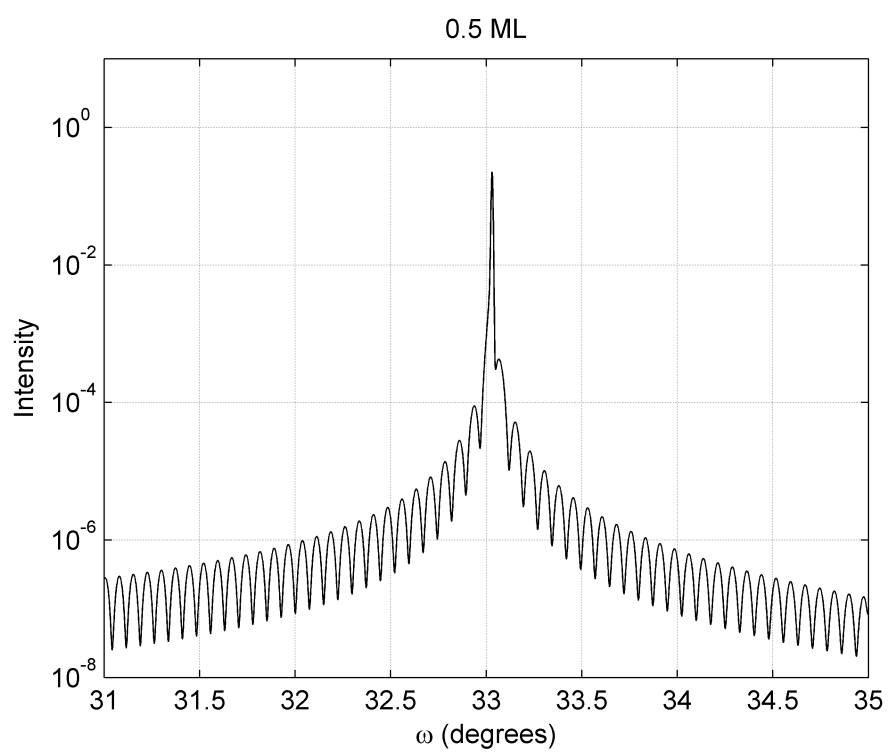


Figure 2.129

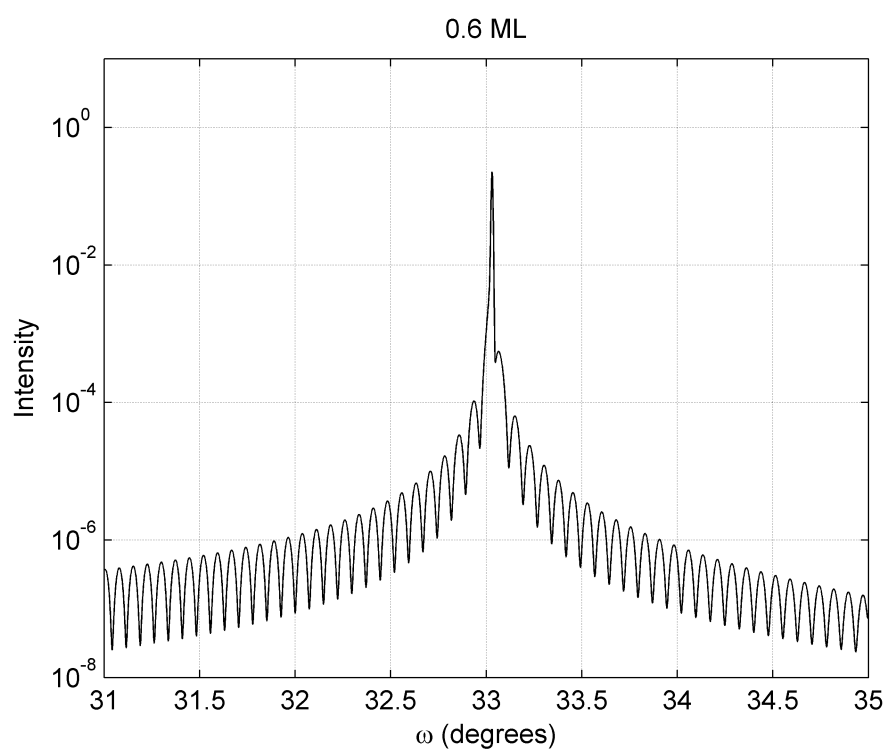


Figure 2.130

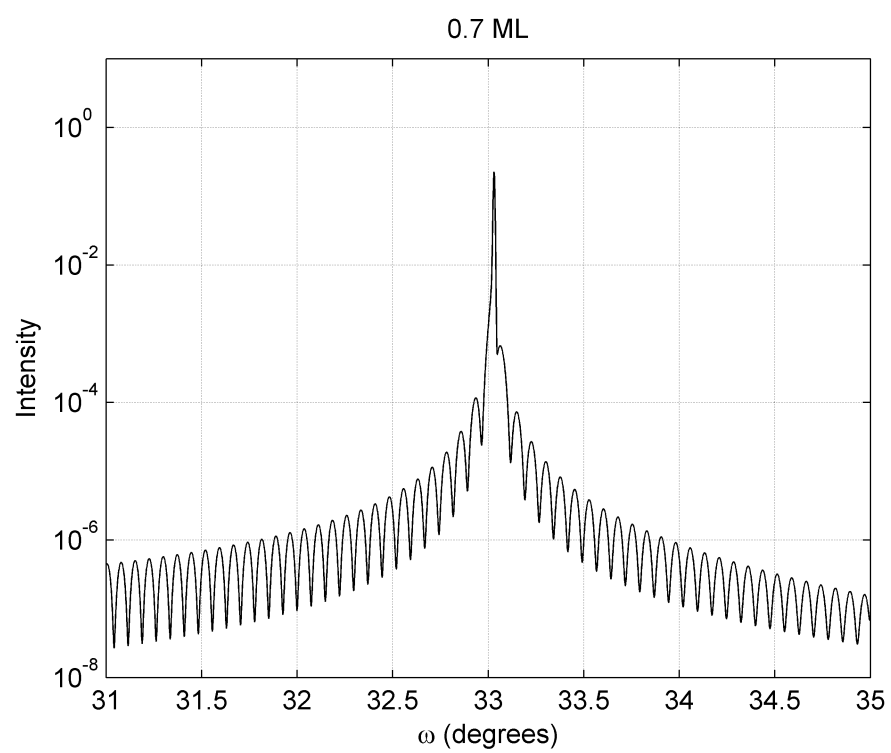


Figure 2.131

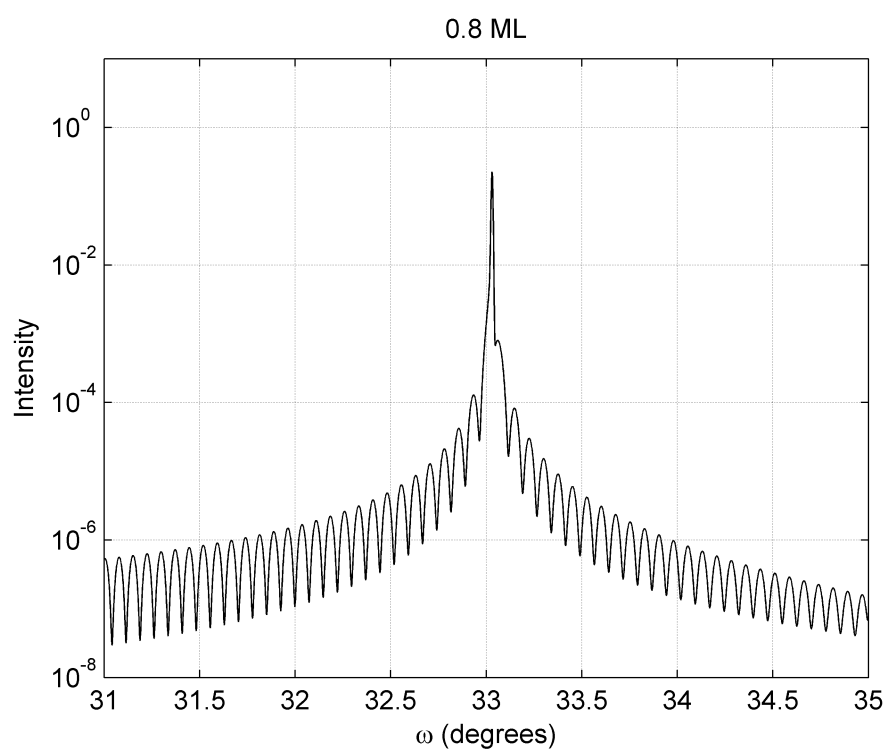


Figure 2.132

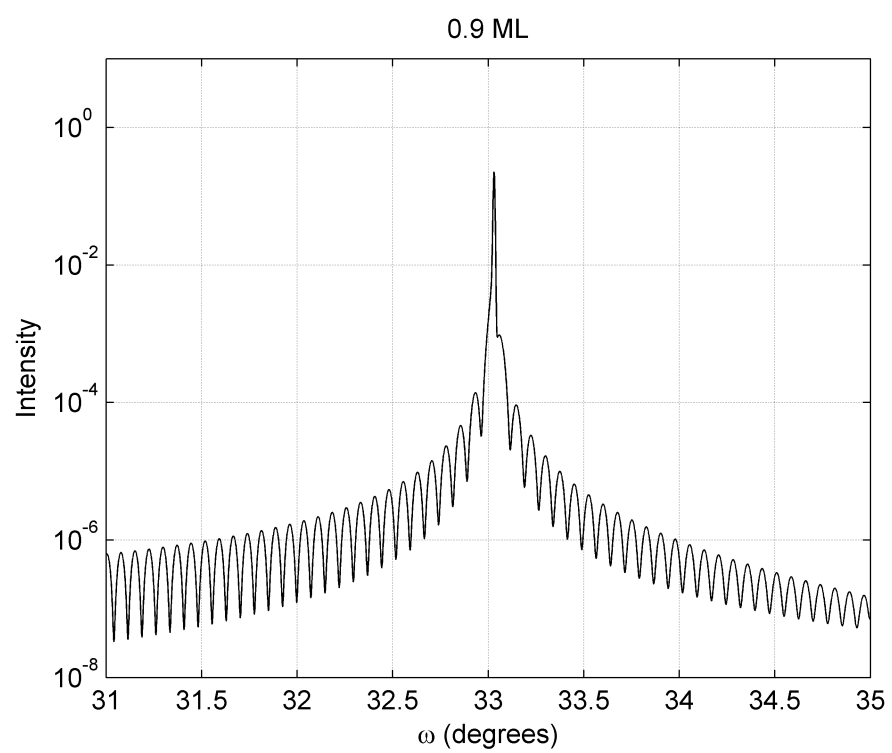


Figure 2.133

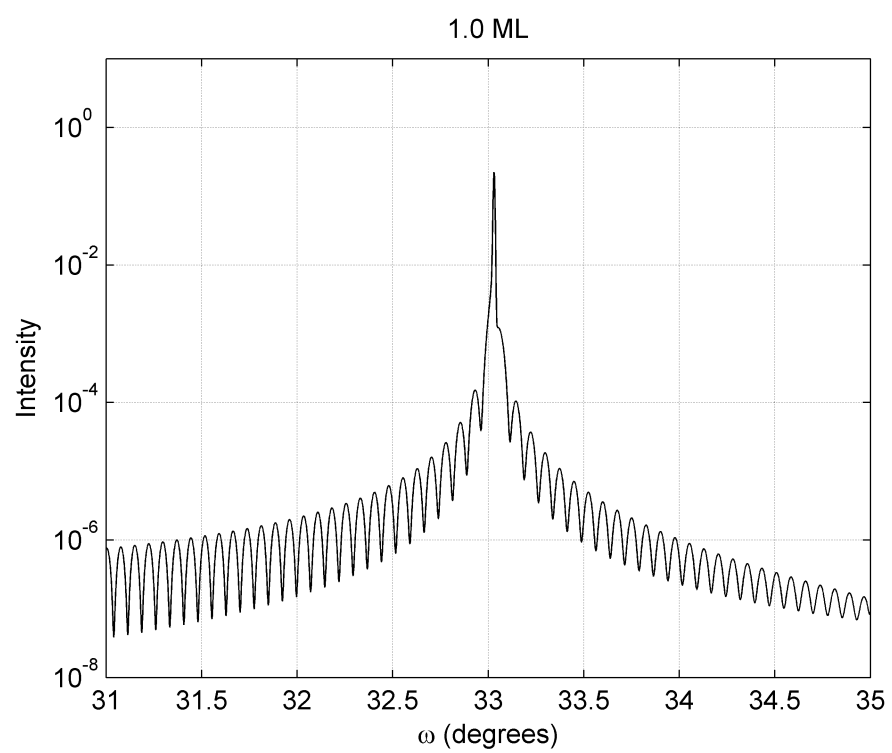


Figure 2.134

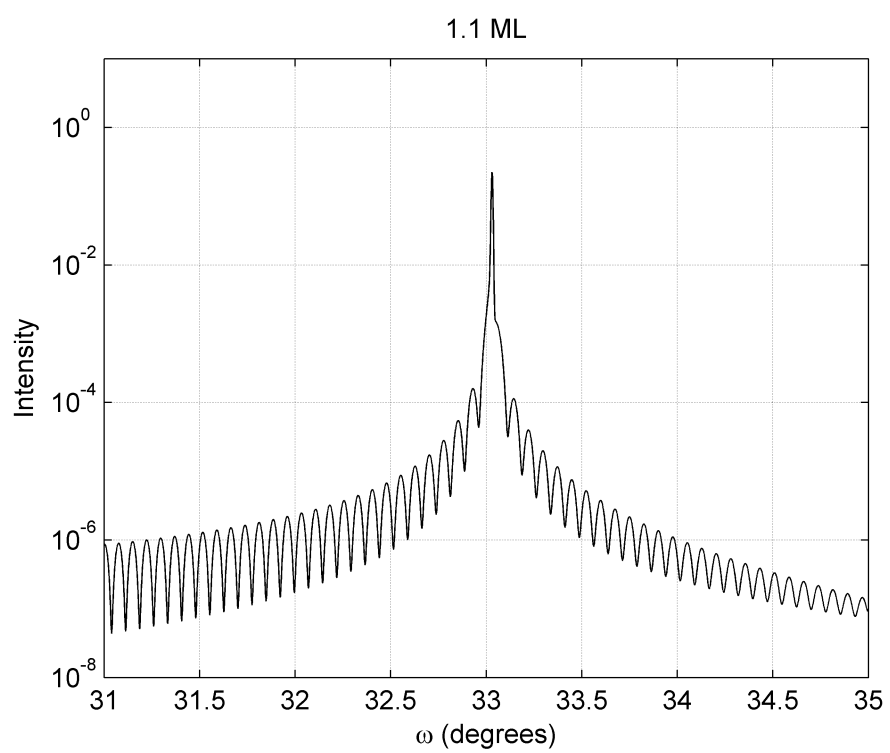


Figure 2.135

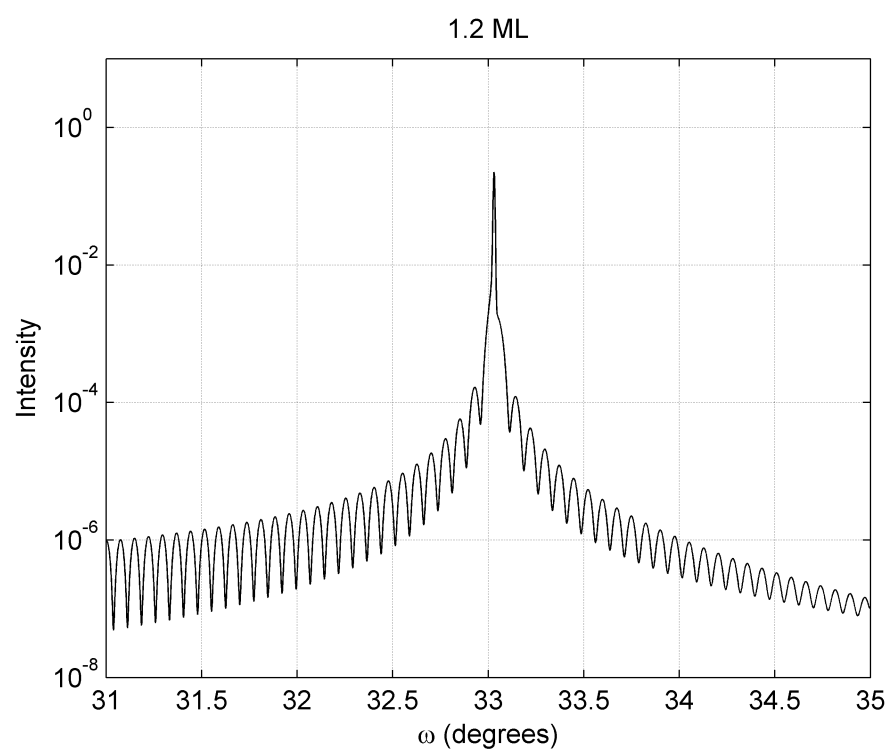


Figure 2.136

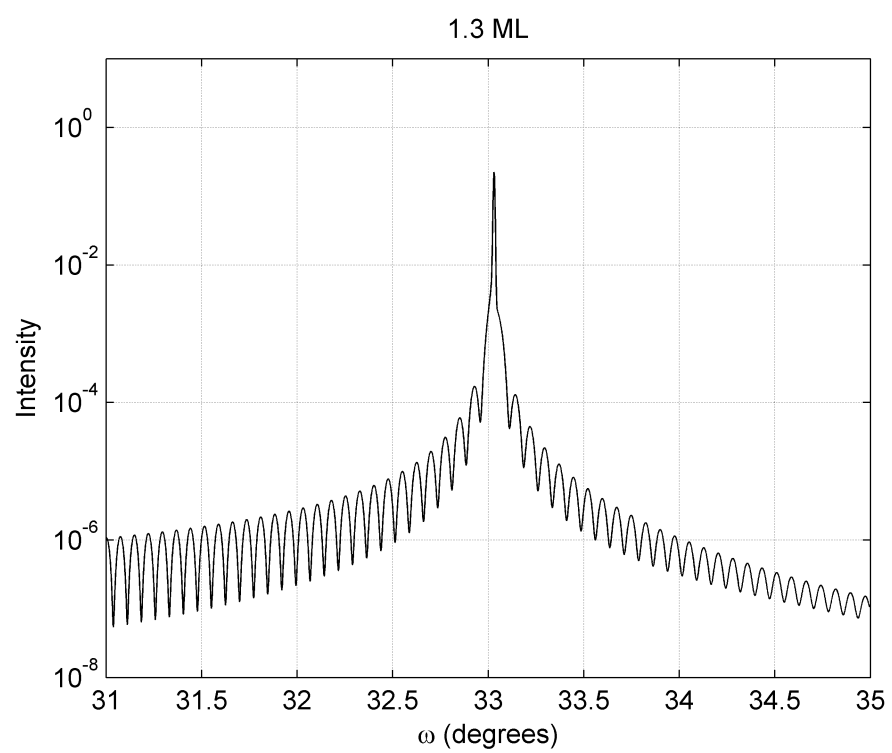


Figure 2.137

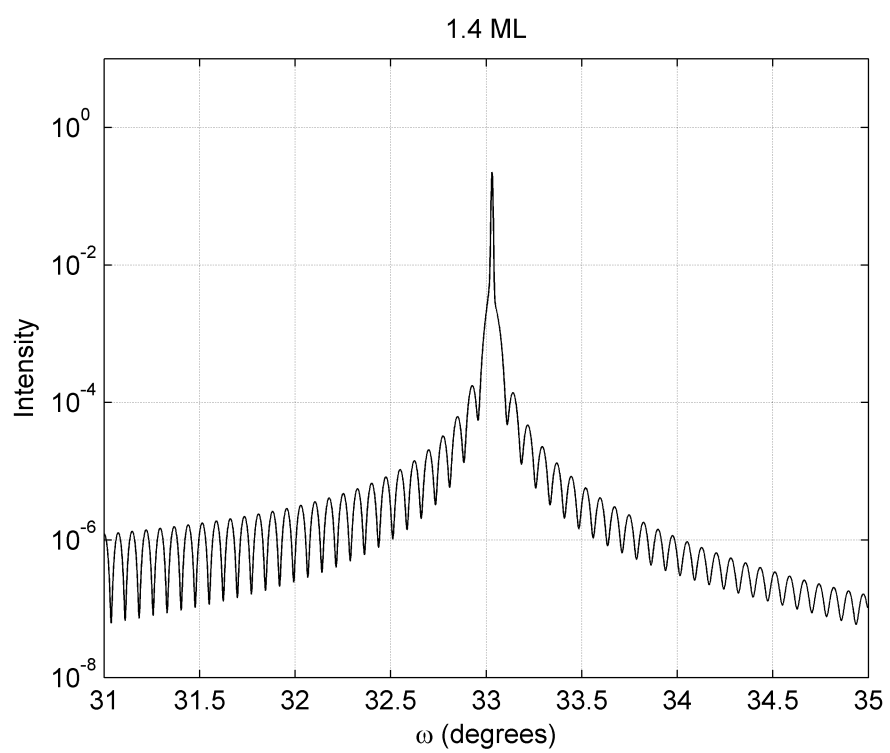


Figure 2.138

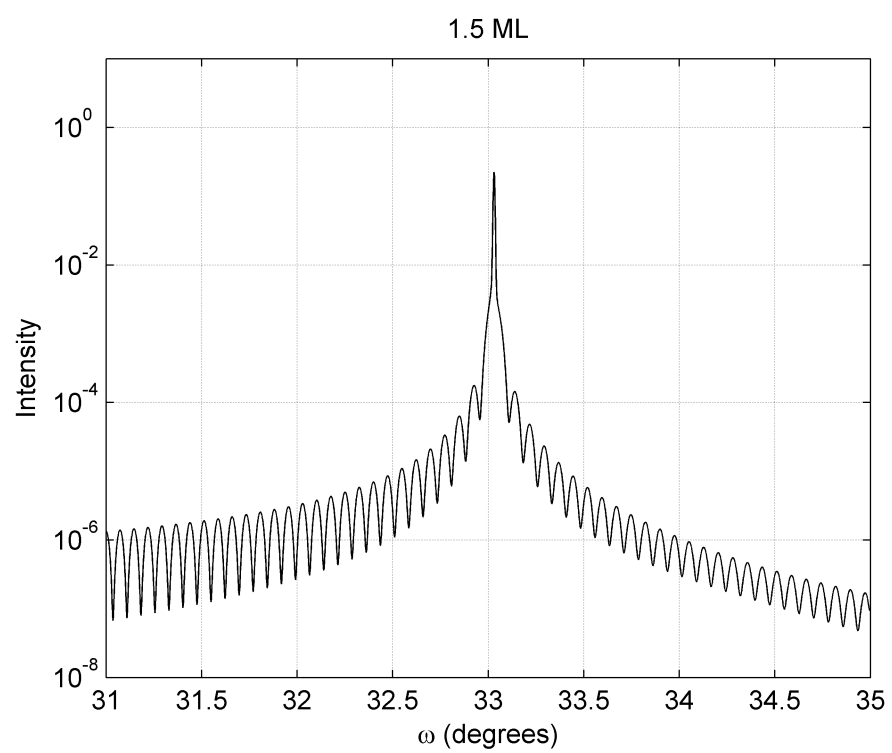


Figure 2.139

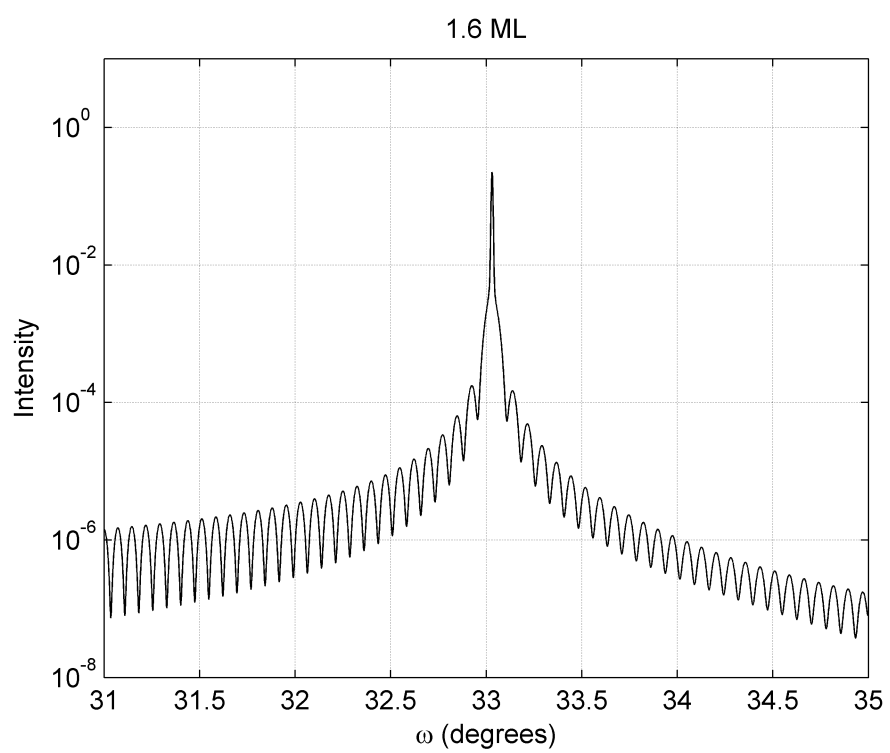


Figure 2.140

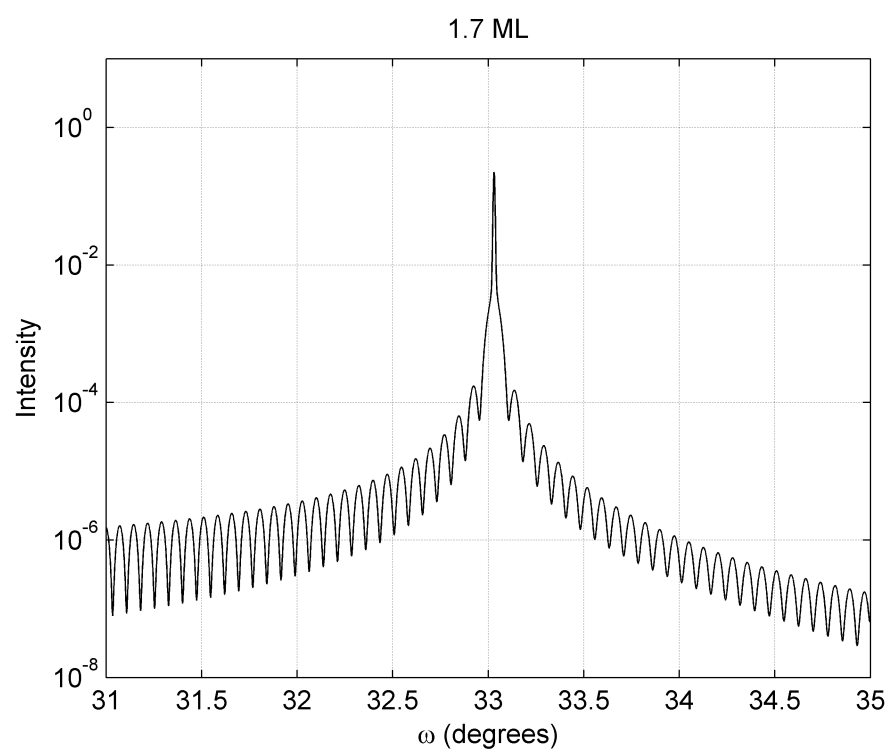


Figure 2.141

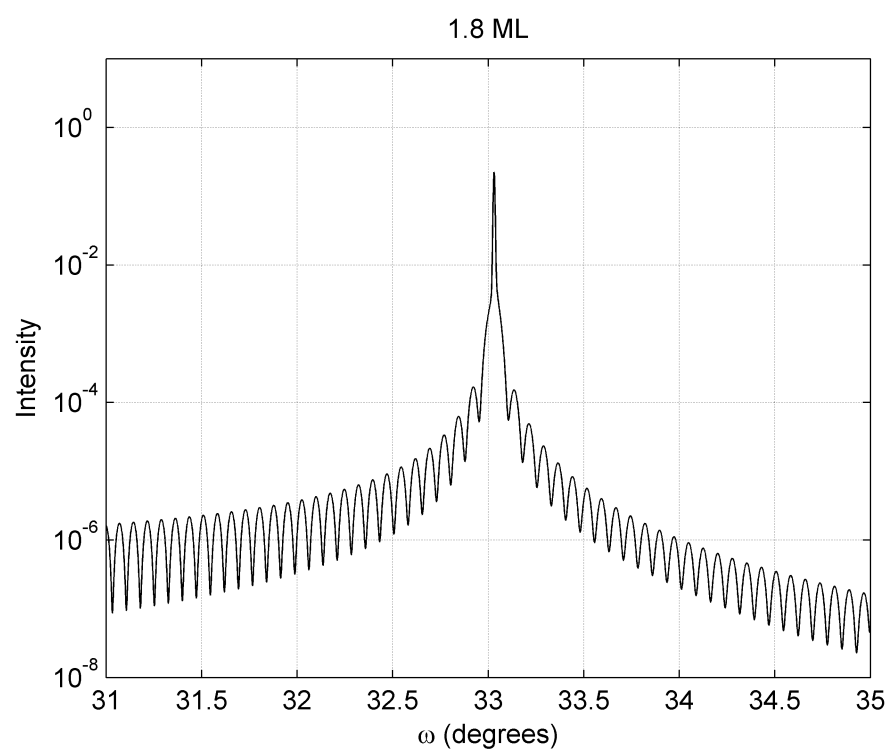


Figure 2.142

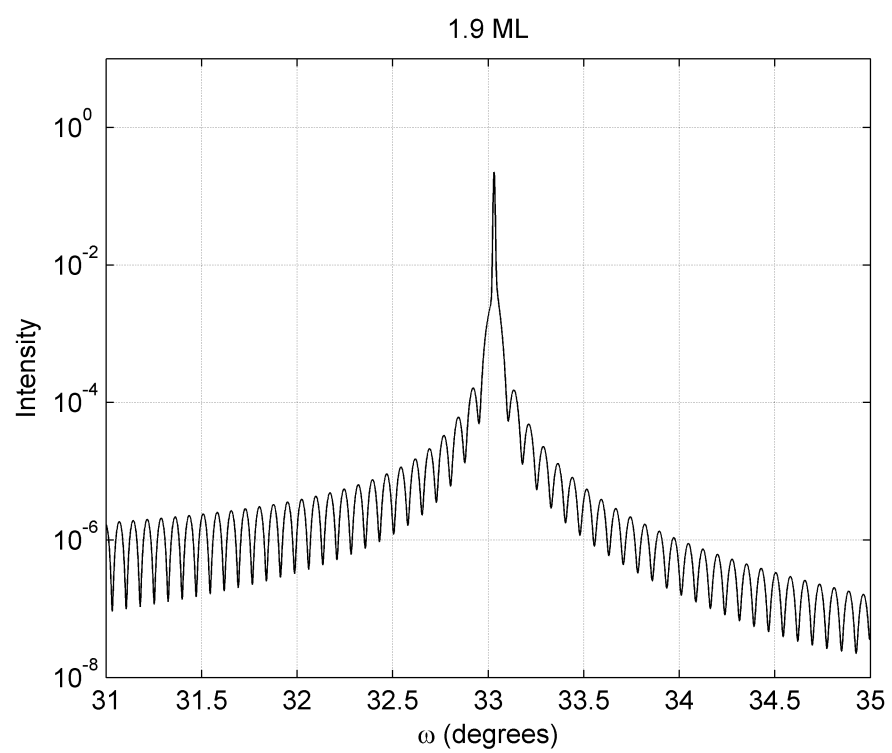


Figure 2.143

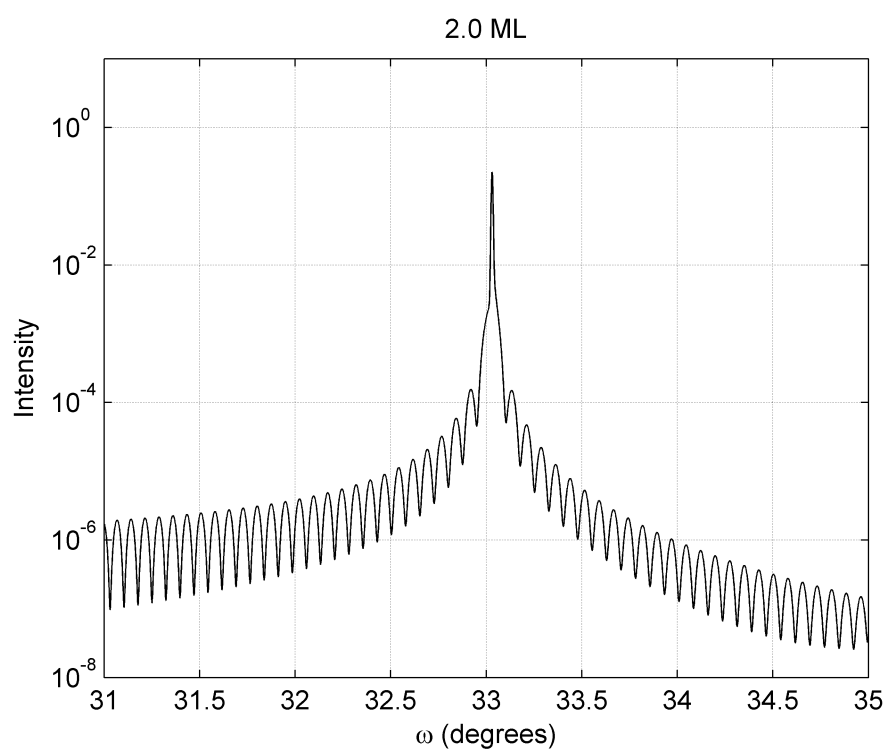


Figure 2.144

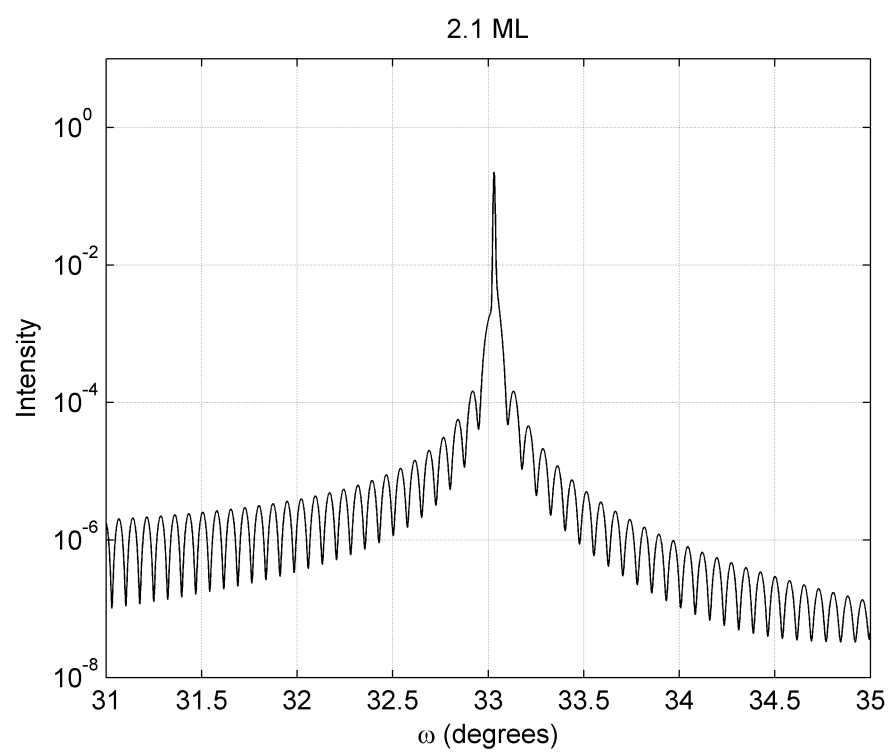


Figure 2.145

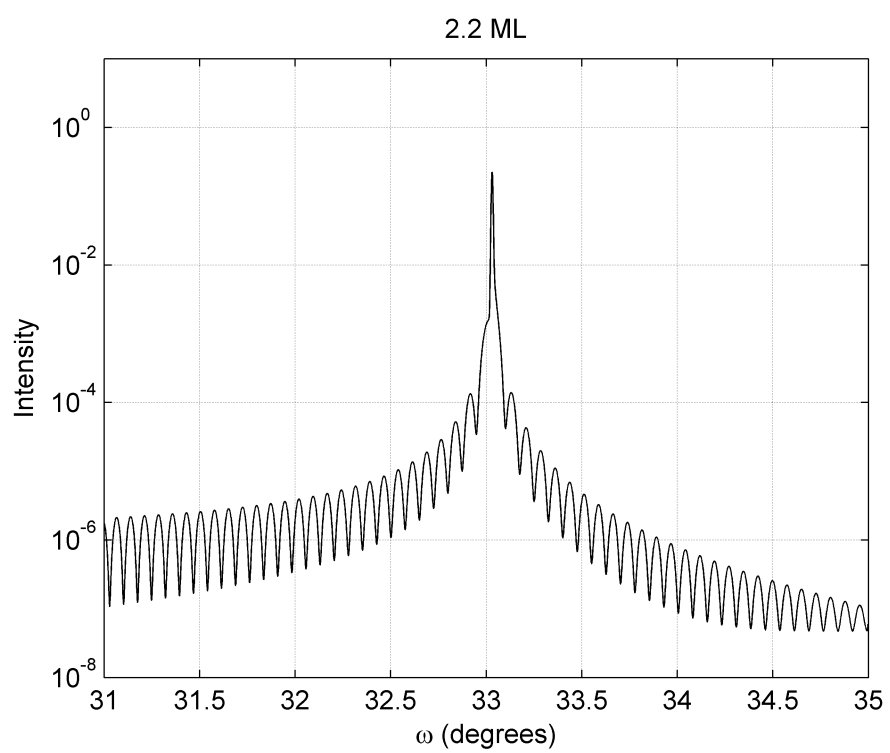


Figure 2.146

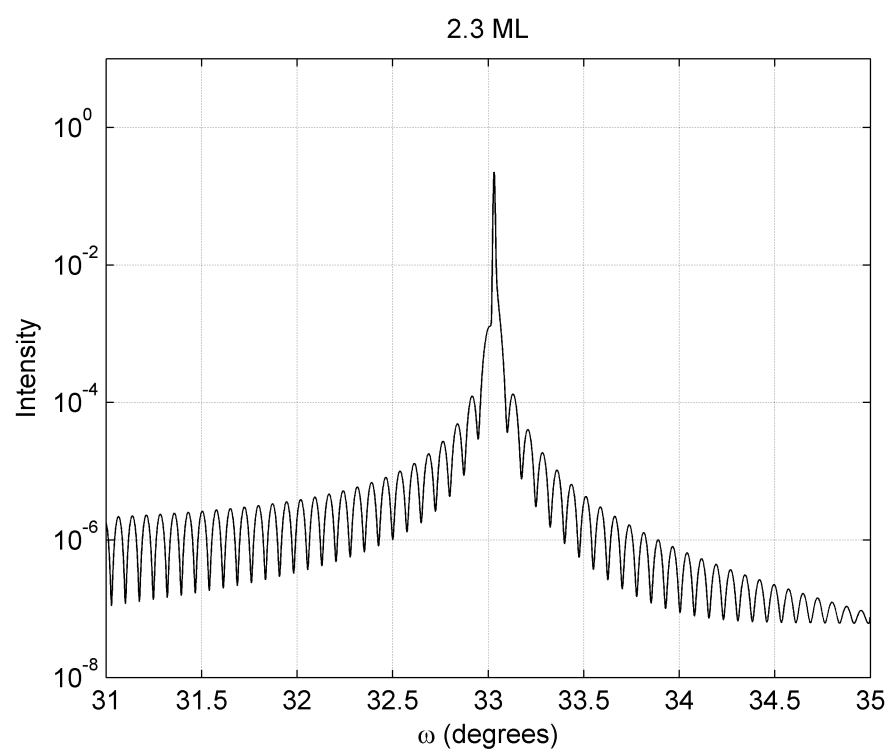


Figure 2.147

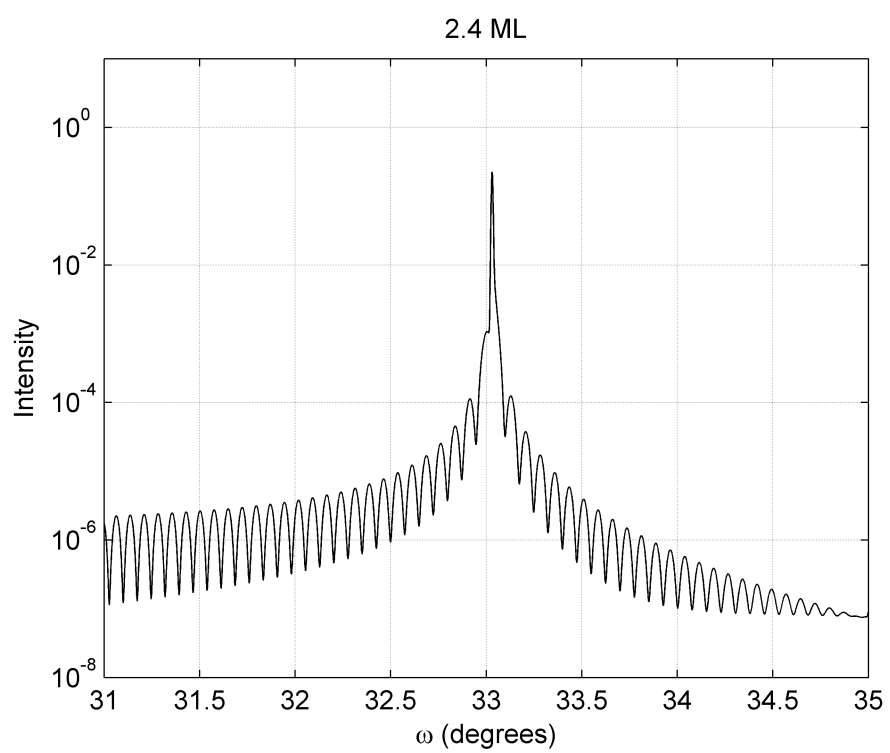


Figure 2.148

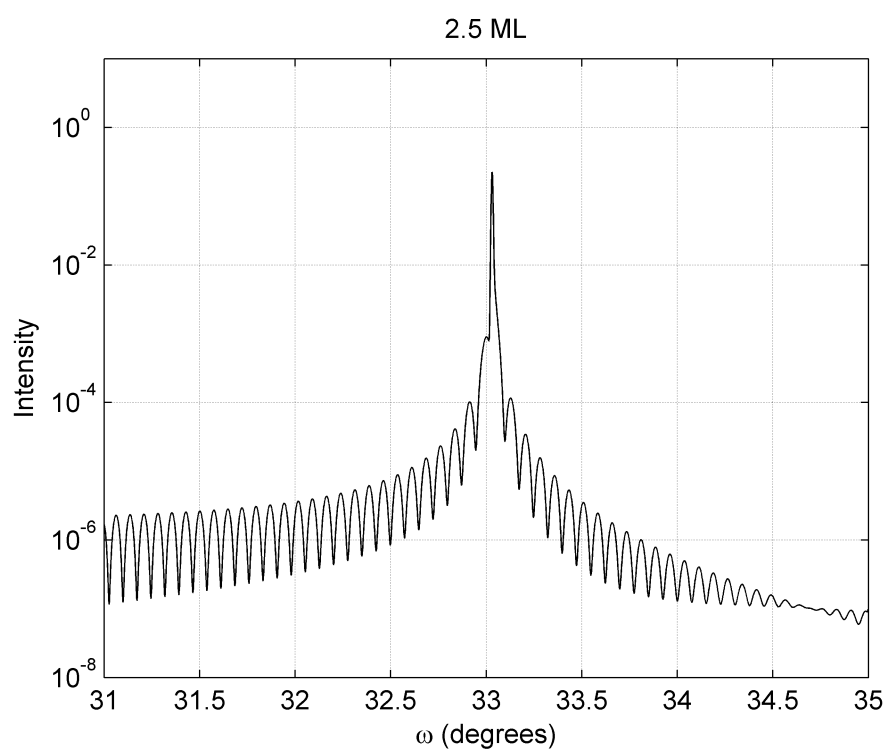


Figure 2.149

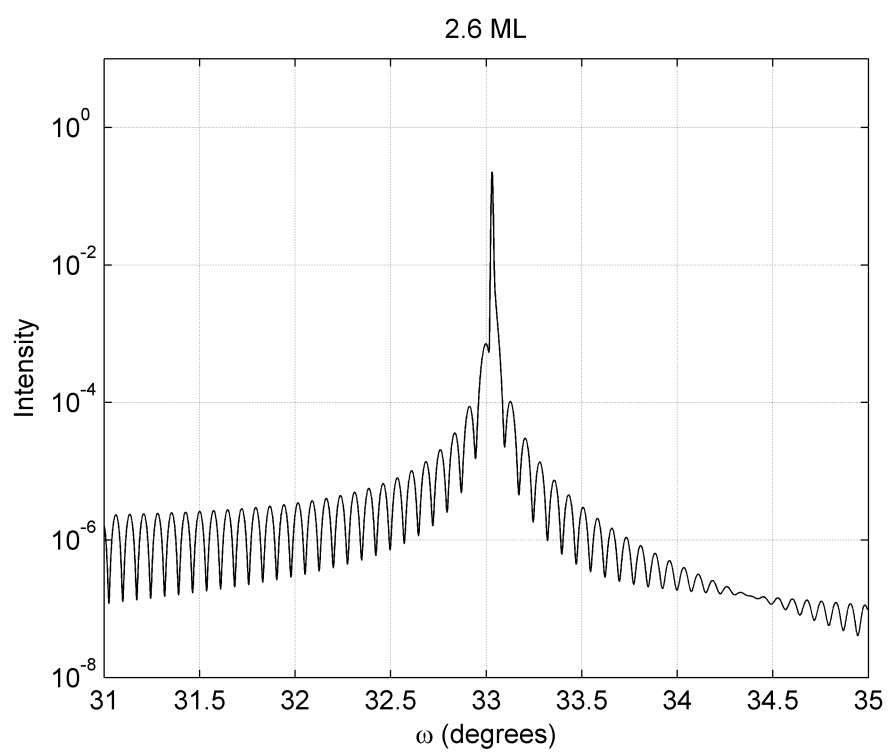


Figure 2.150

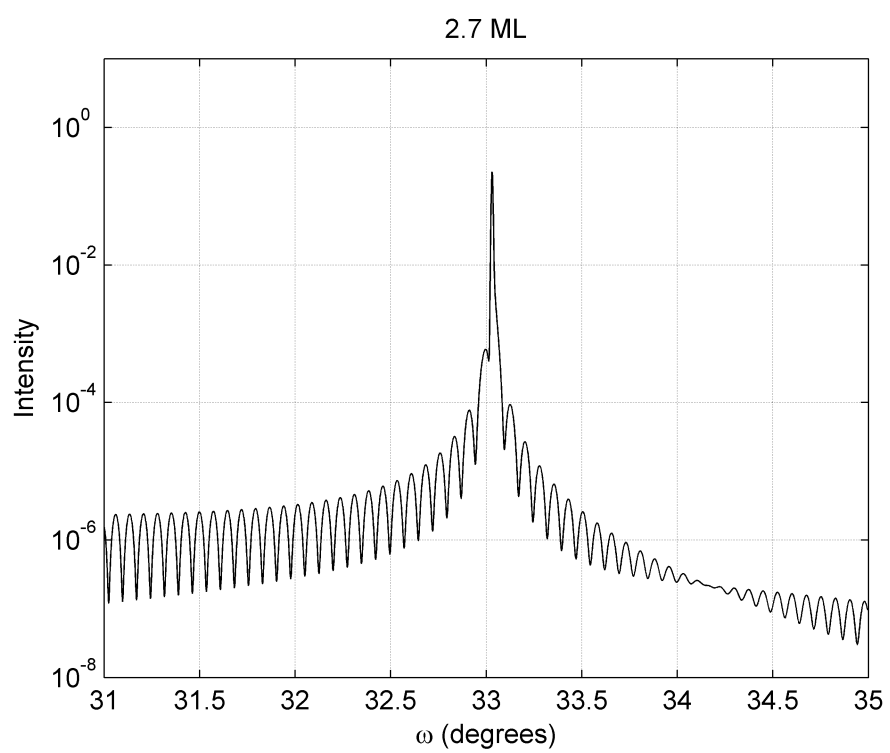


Figure 2.151

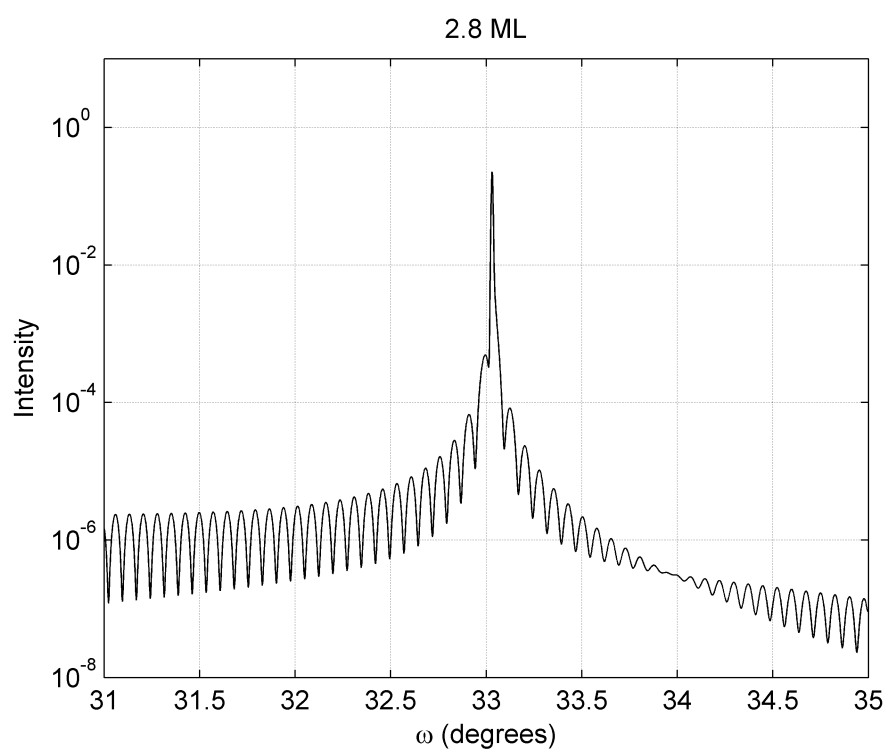


Figure 2.152

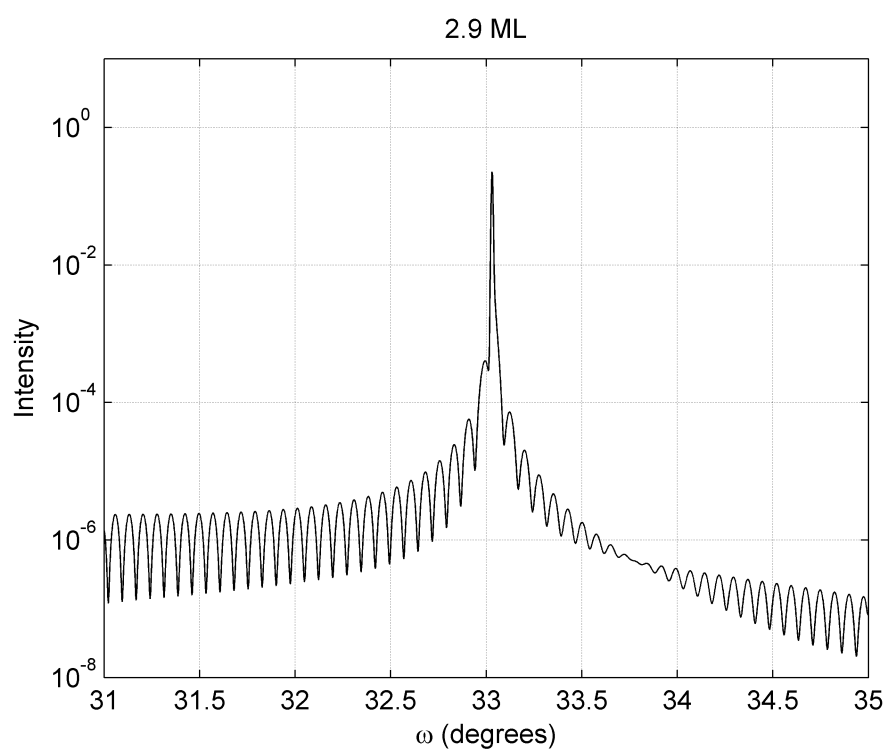


Figure 2.153

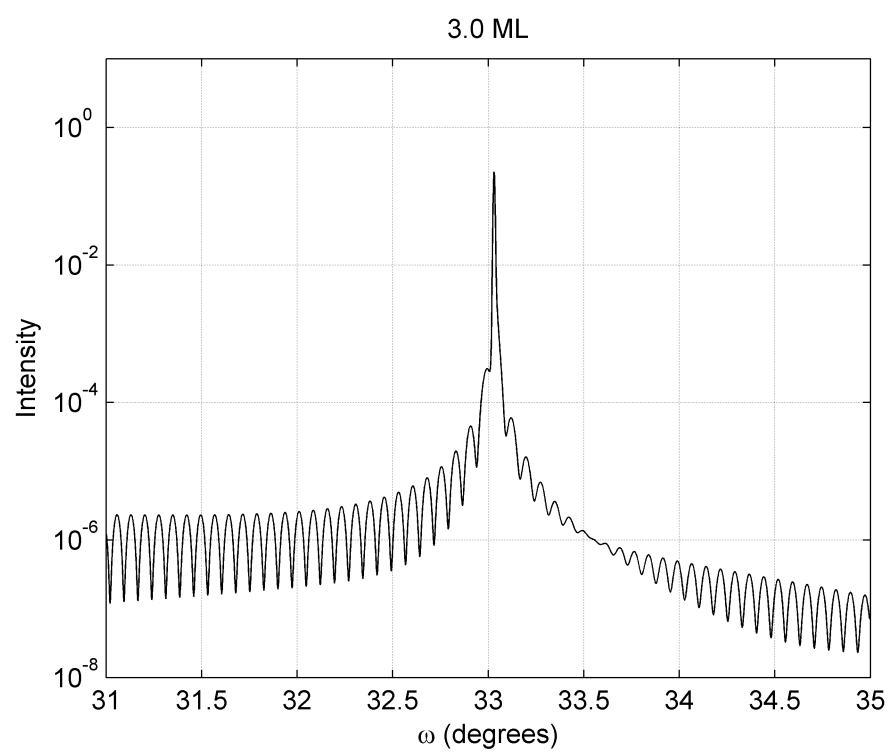


Figure 2.154

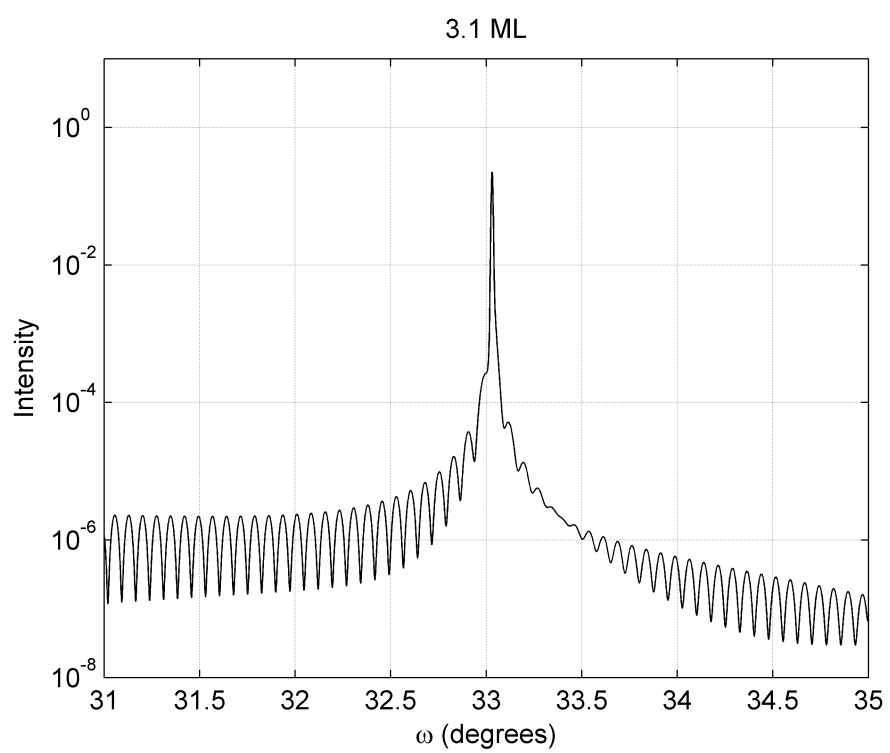


Figure 2.155

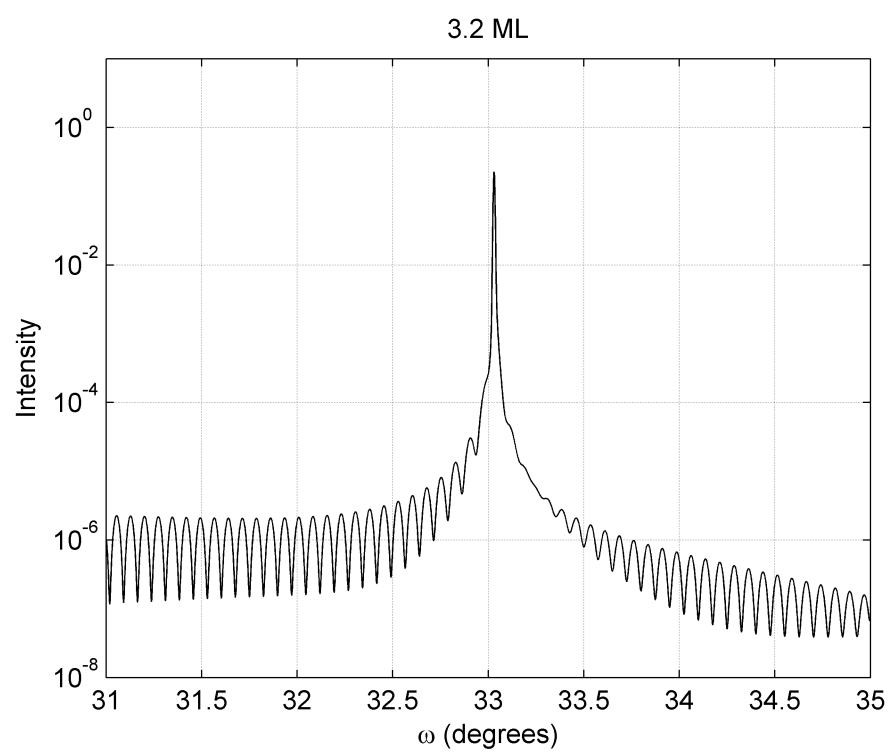


Figure 2.156

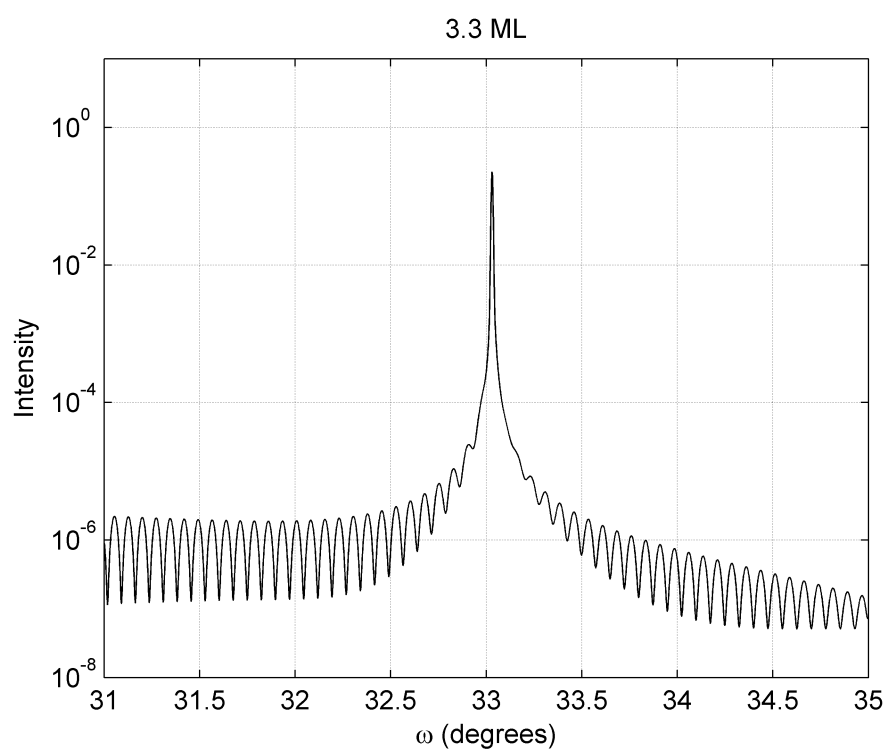


Figure 2.157

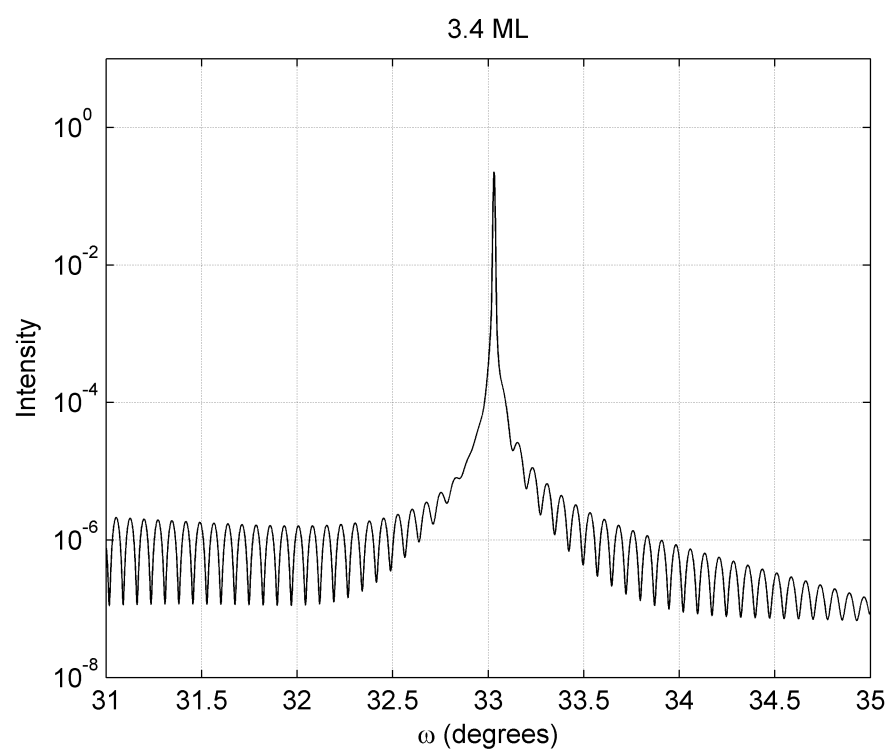


Figure 2.158

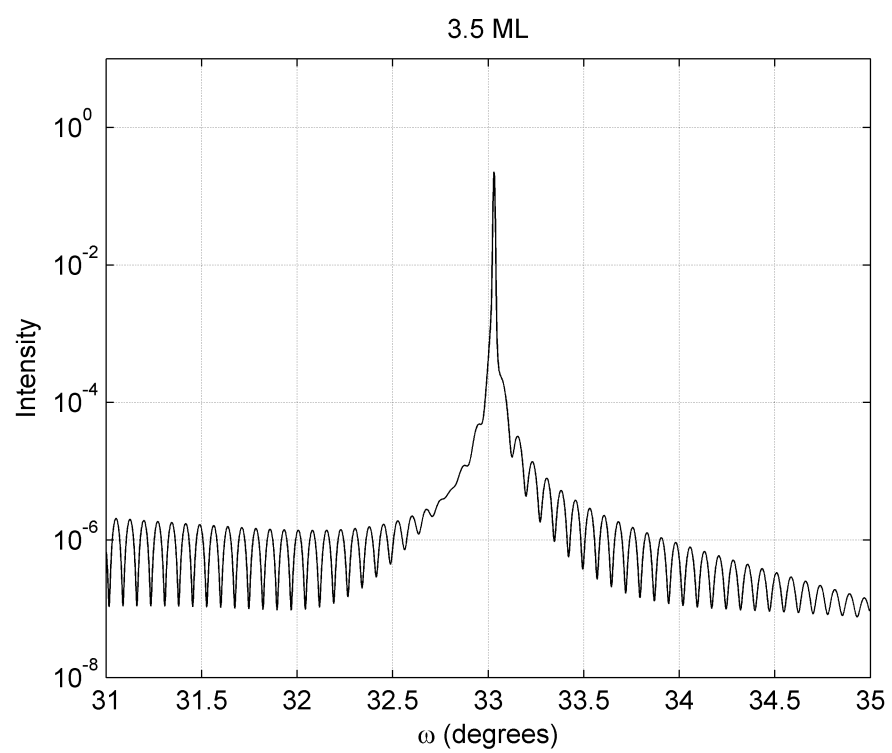


Figure 2.159

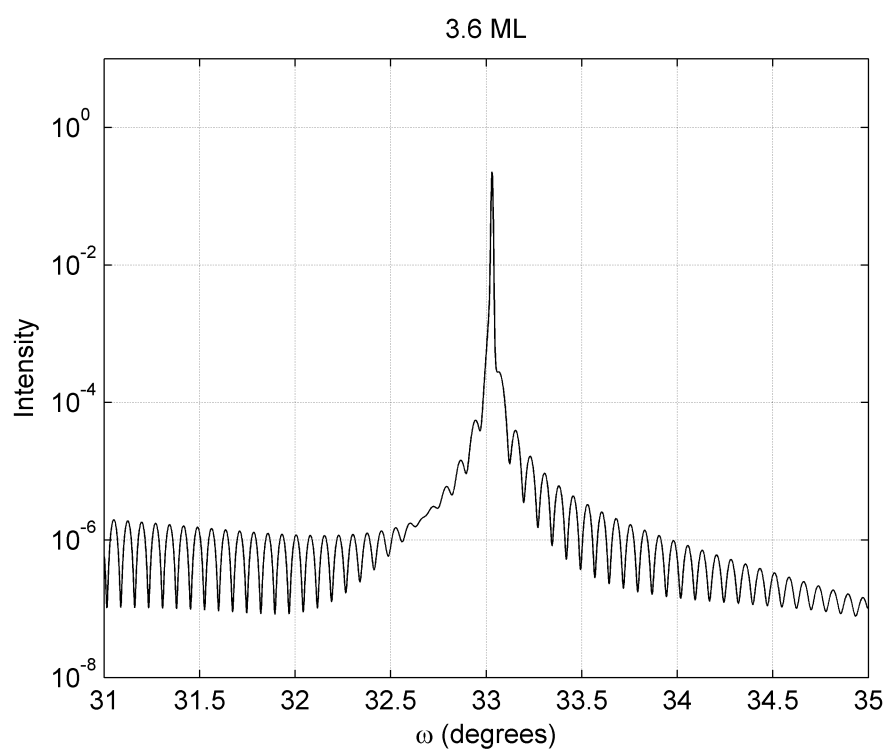


Figure 2.160

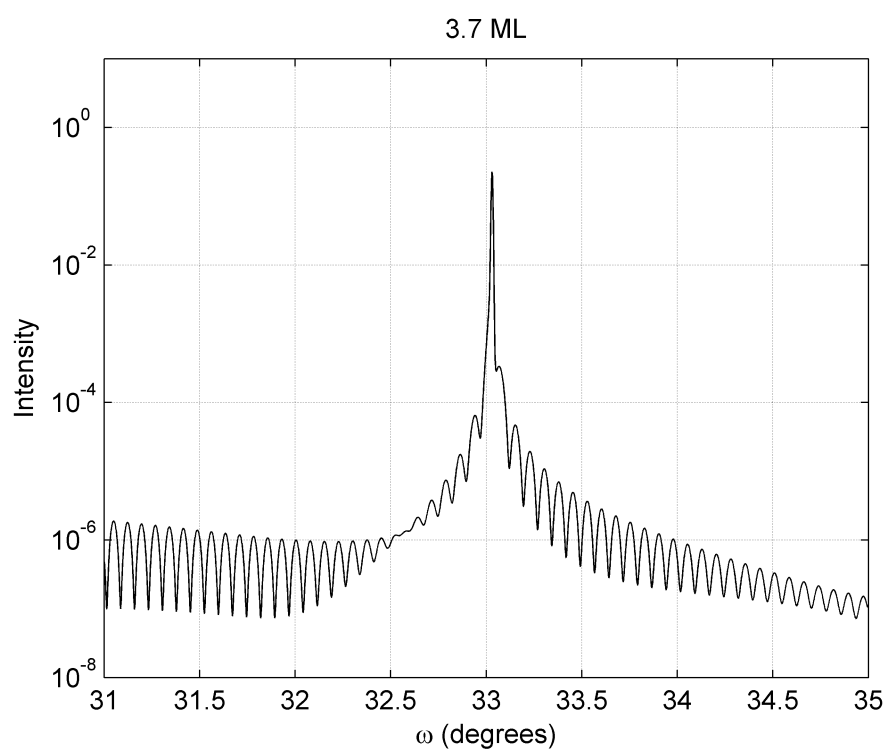


Figure 2.161

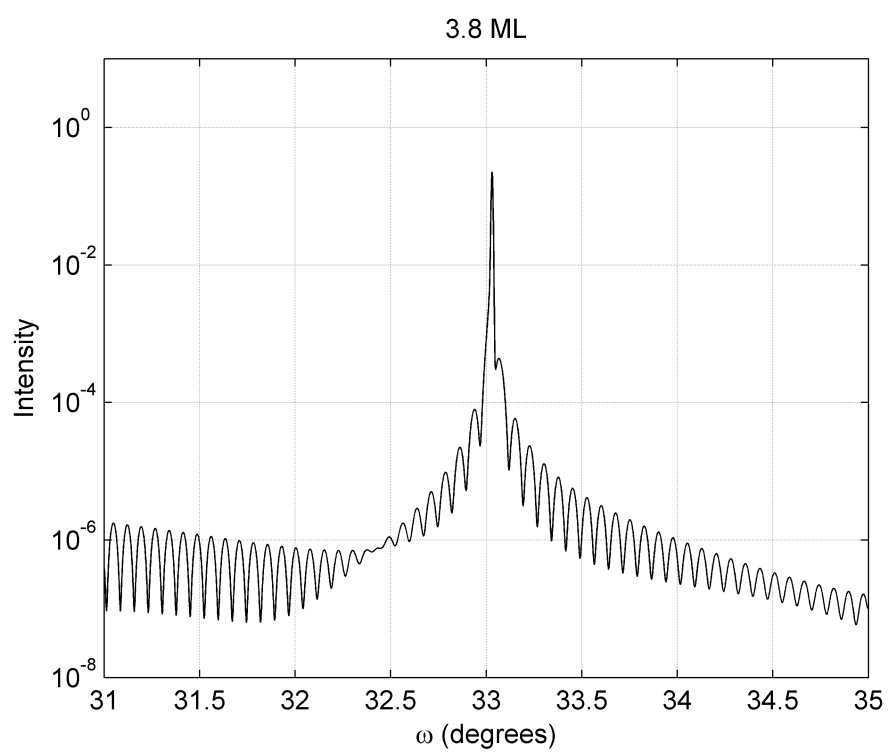


Figure 2.162

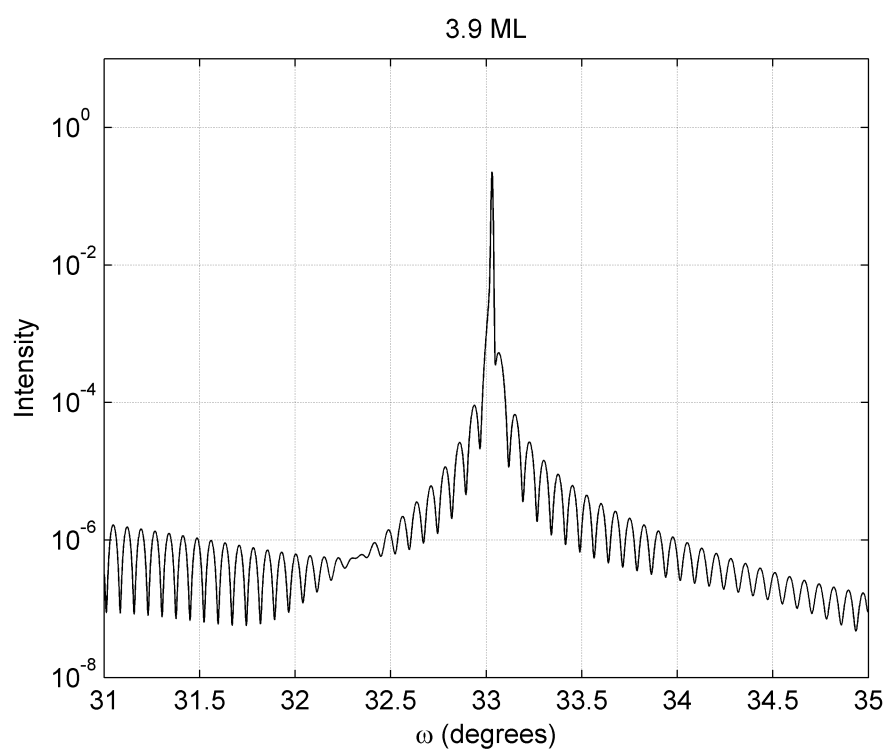


Figure 2.163

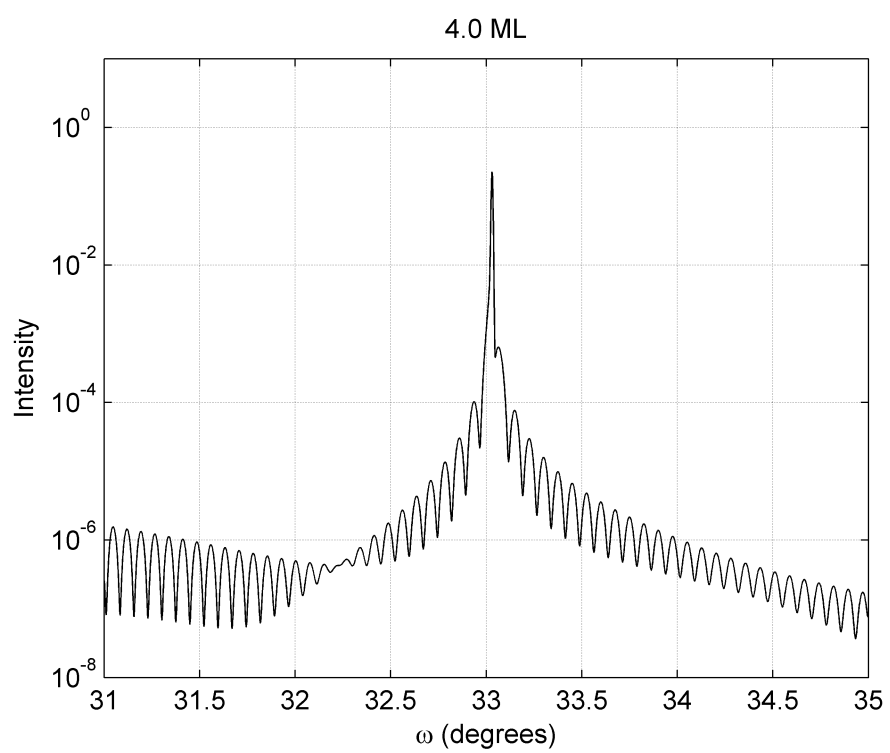


Figure 2.164

Chapter 3

Segregation

3.1 Case 1

3.1.1 Layers of segregation

Simulations of segregation with up to 9 layers are shown in figures 3.1–3.9.

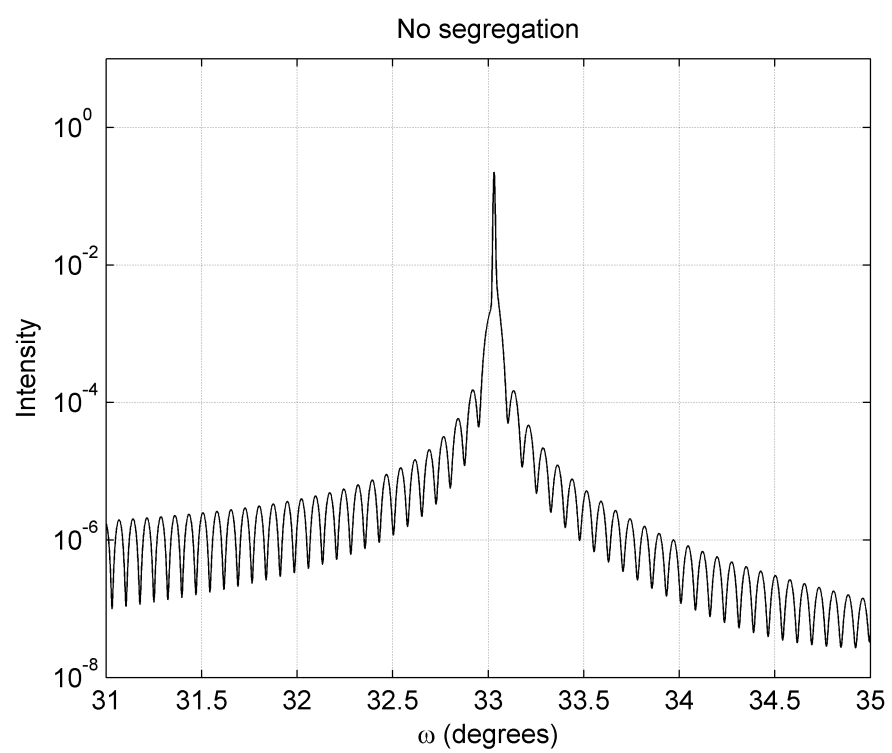


Figure 3.1

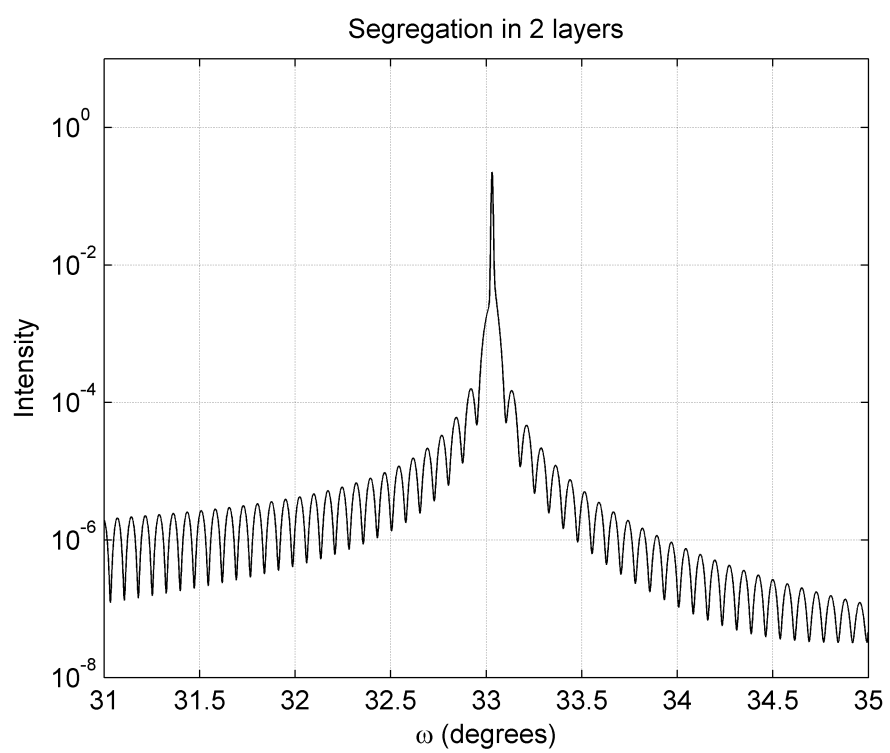


Figure 3.2

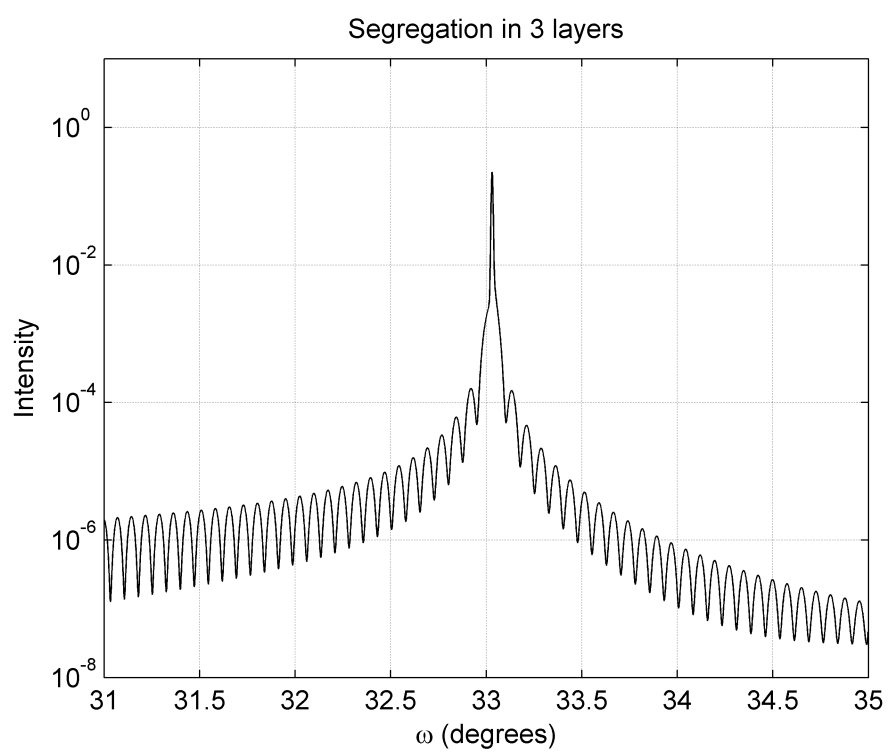


Figure 3.3

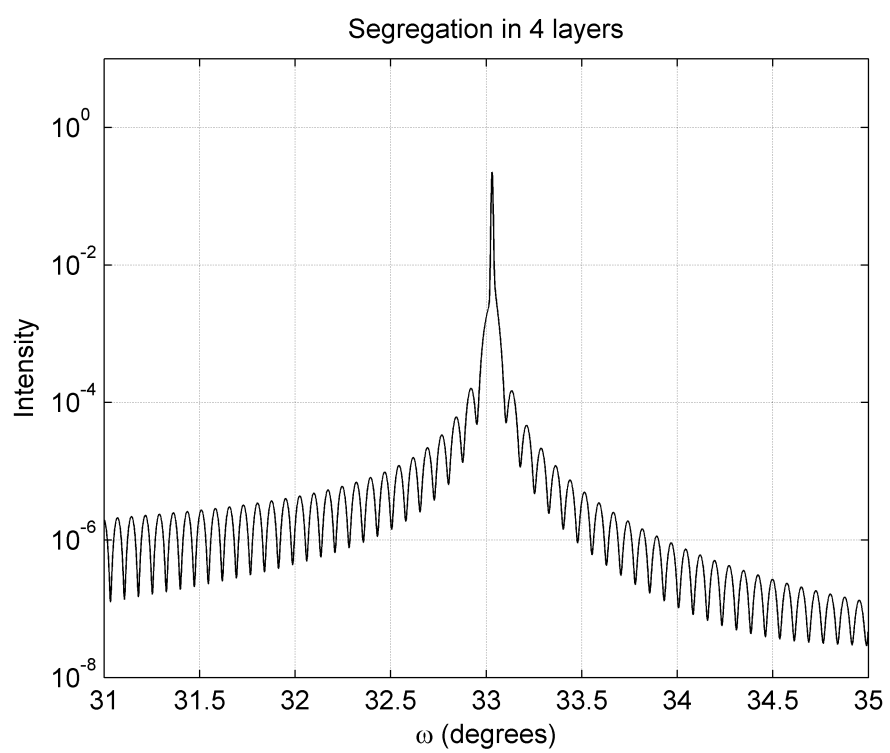


Figure 3.4

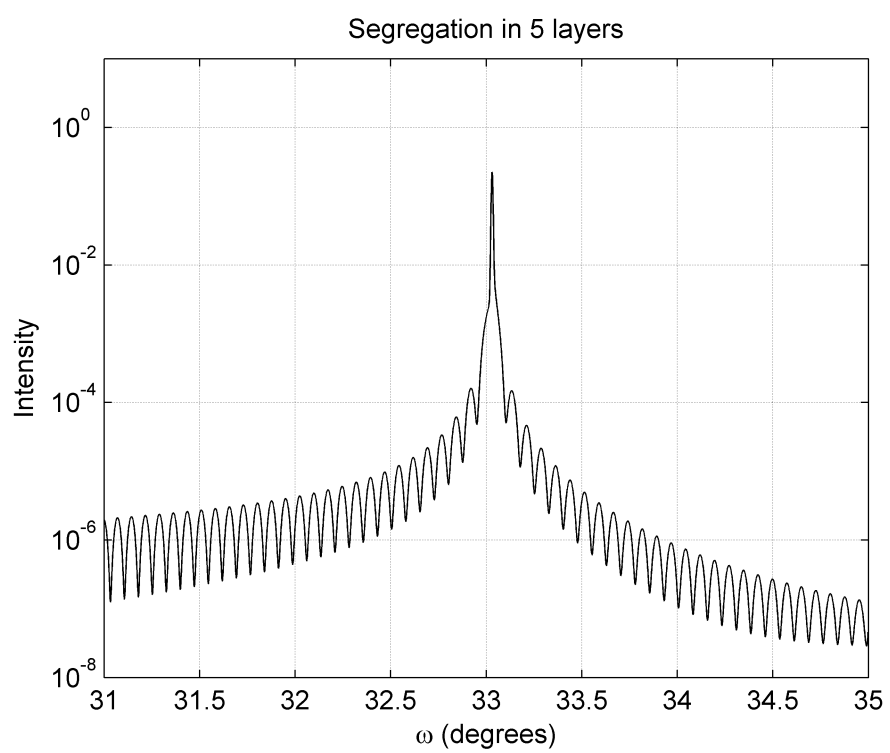


Figure 3.5

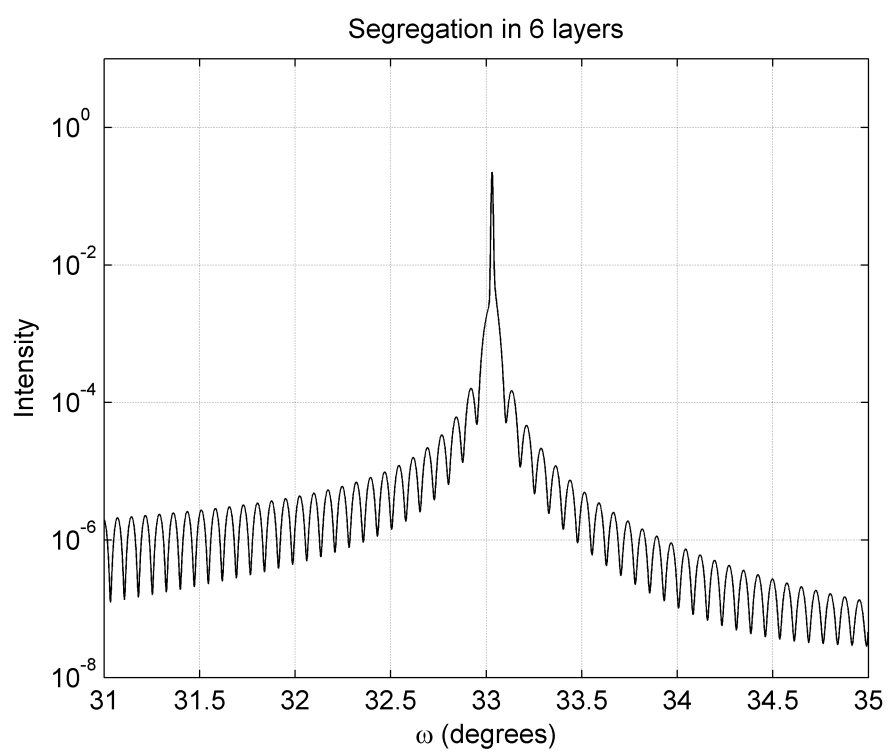


Figure 3.6

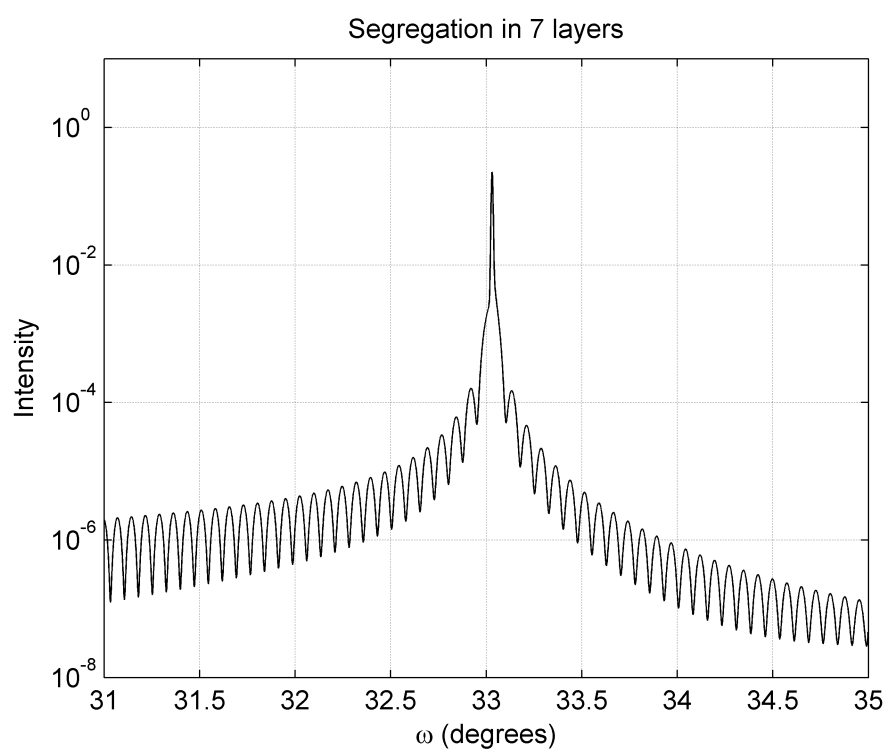


Figure 3.7

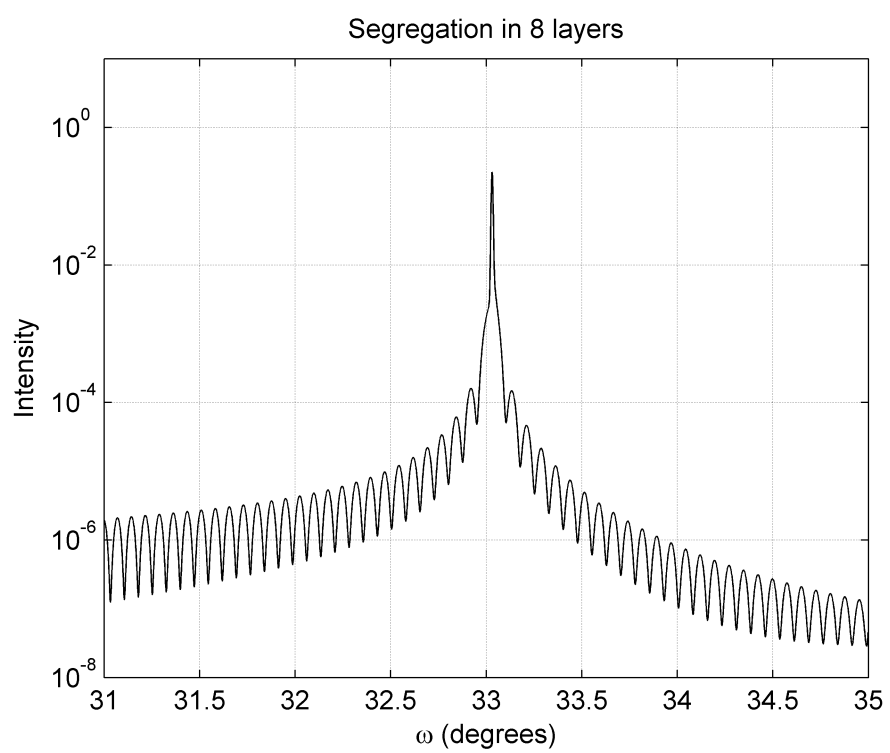


Figure 3.8

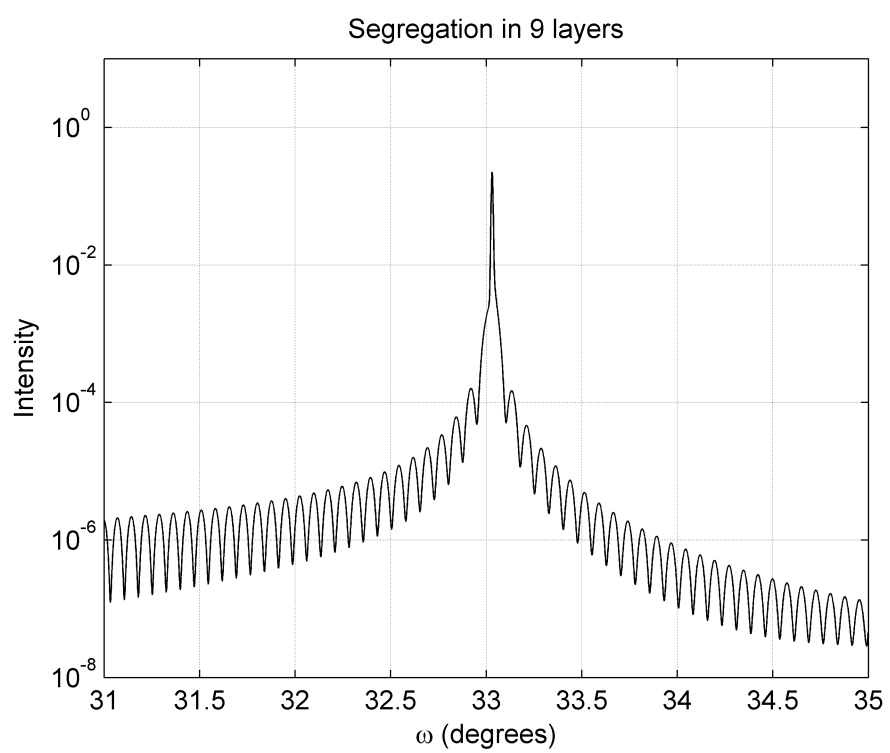


Figure 3.9

3.1.2 Segregation rate

Simulations with first no segregation, then an increasing segregation rate from 5% to 50% are shown in figures 3.10–3.20.

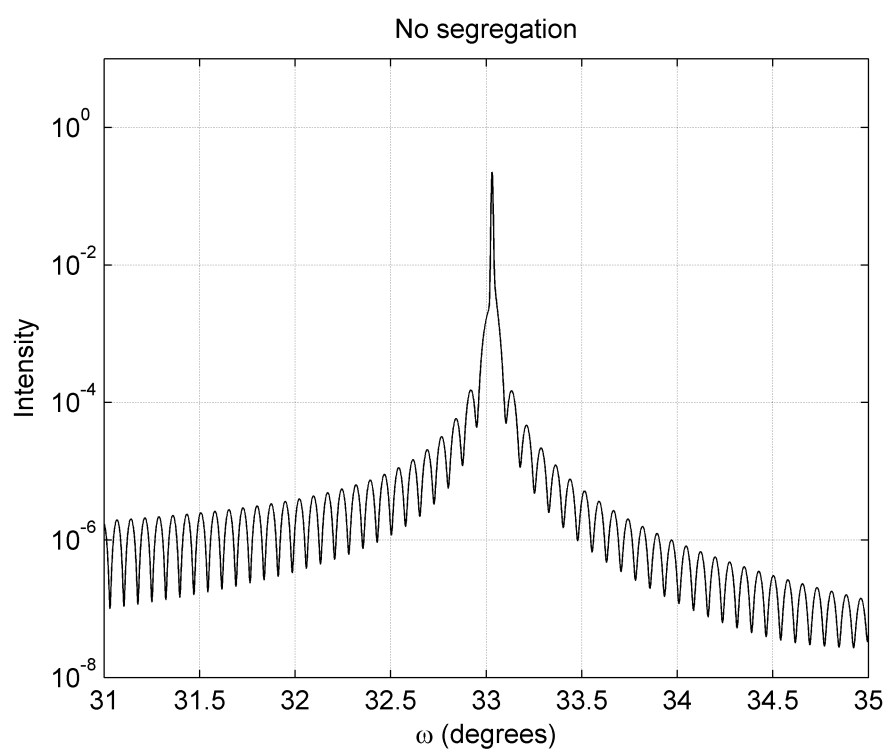


Figure 3.10

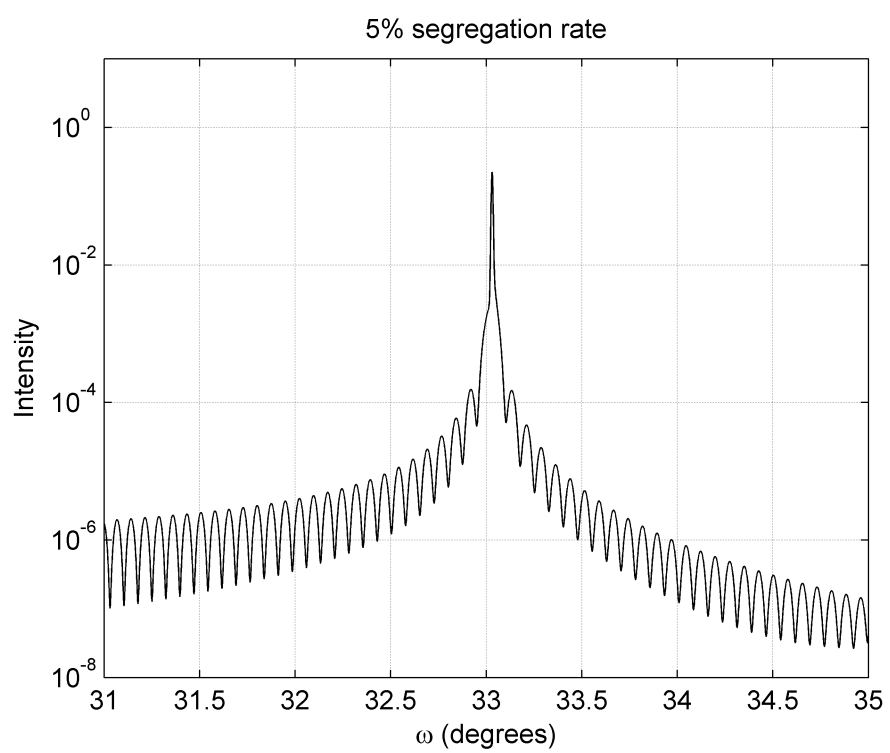


Figure 3.11

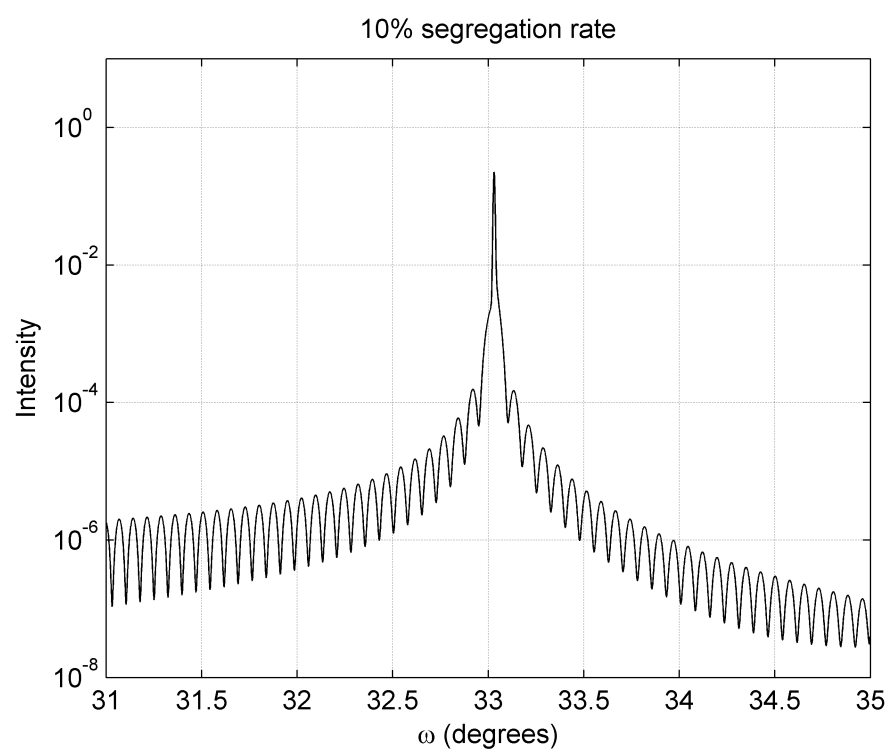


Figure 3.12

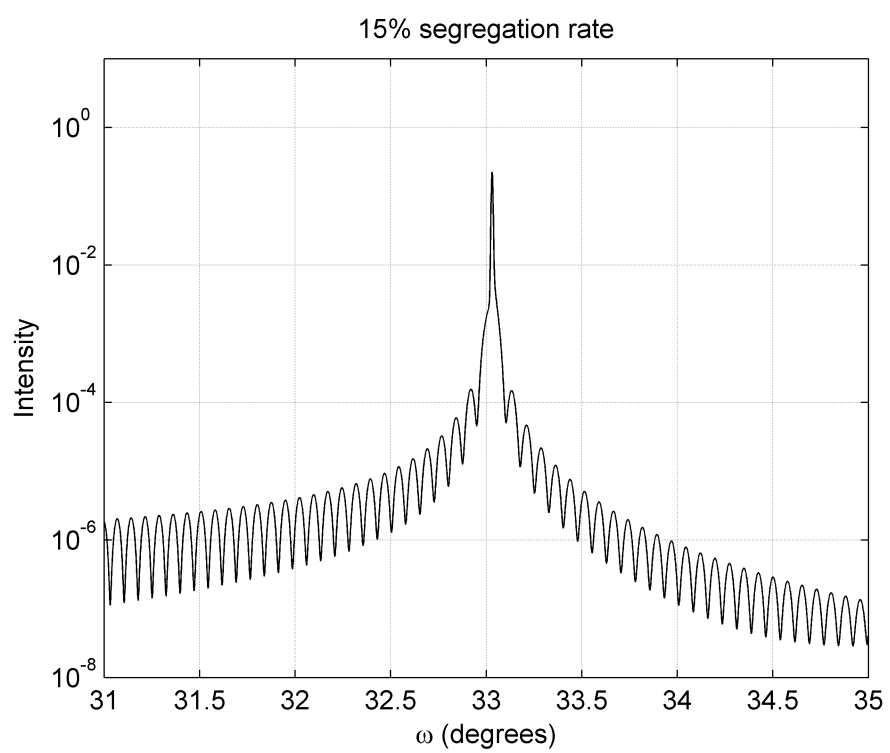


Figure 3.13

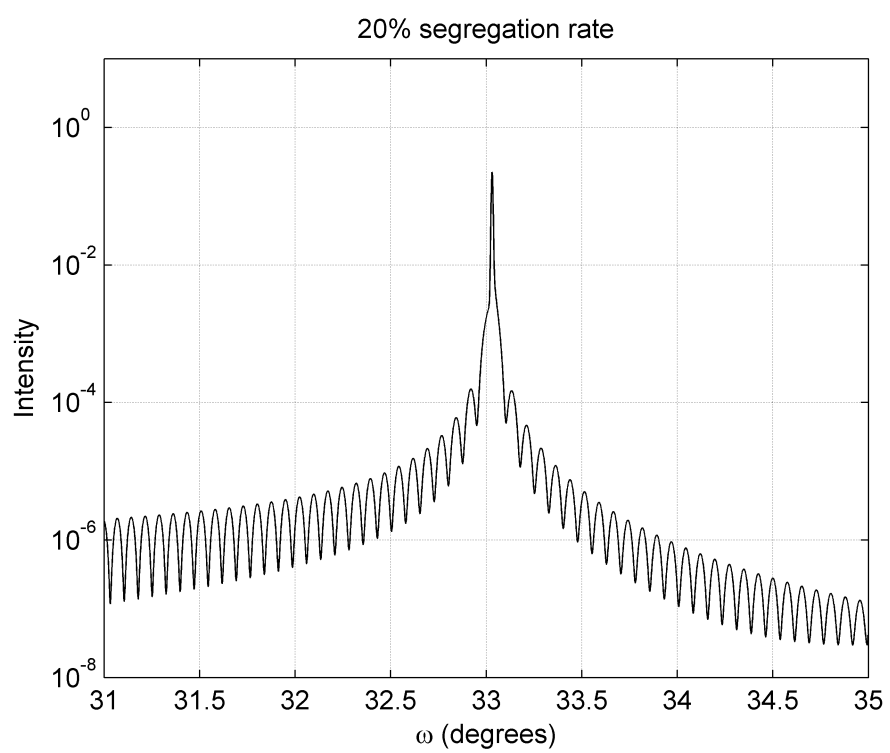


Figure 3.14

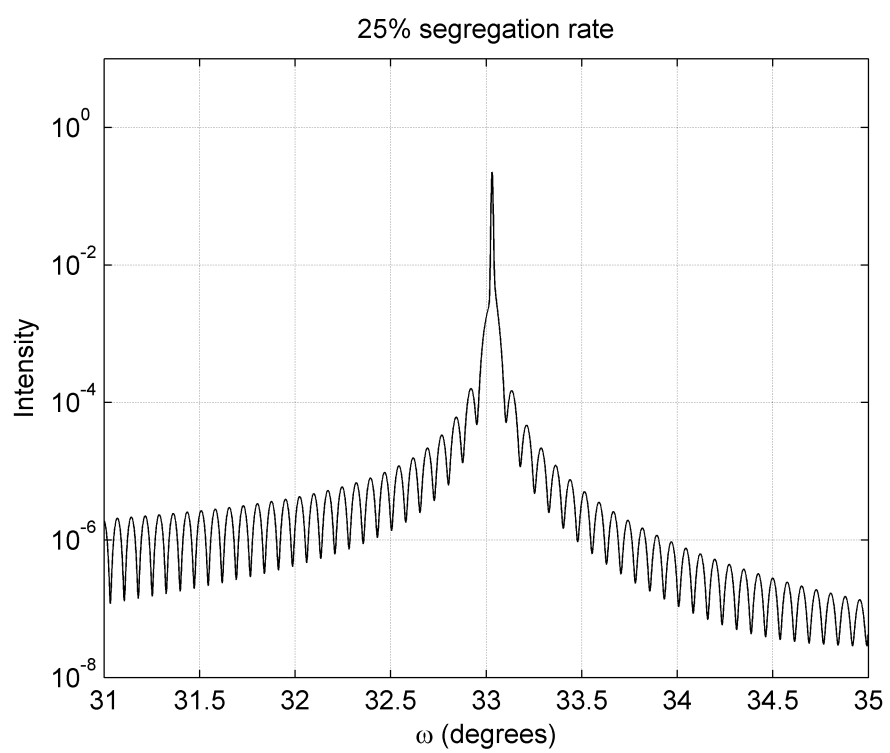


Figure 3.15

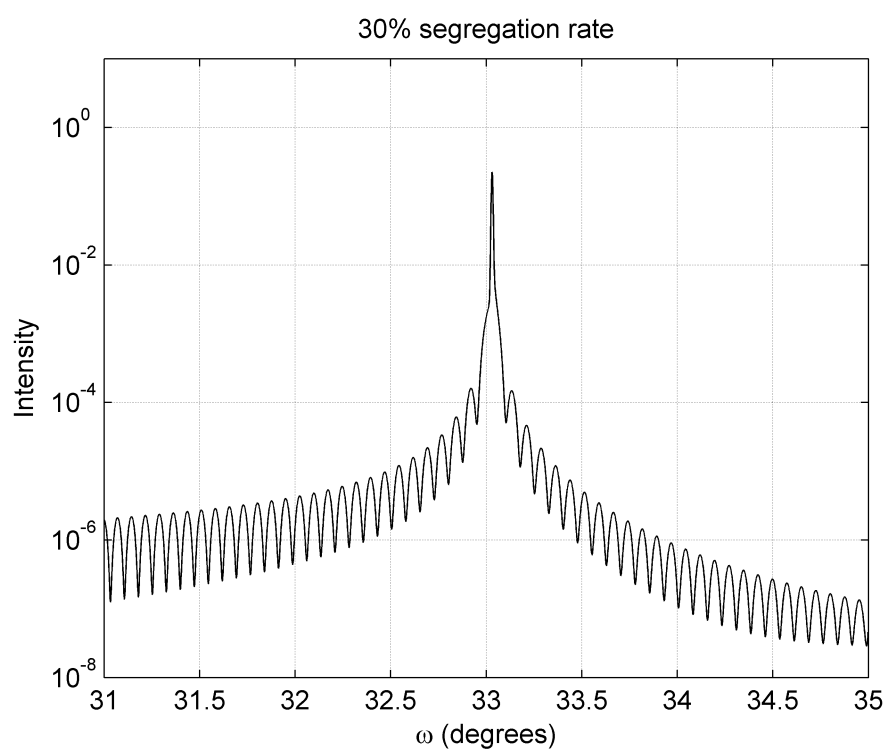


Figure 3.16

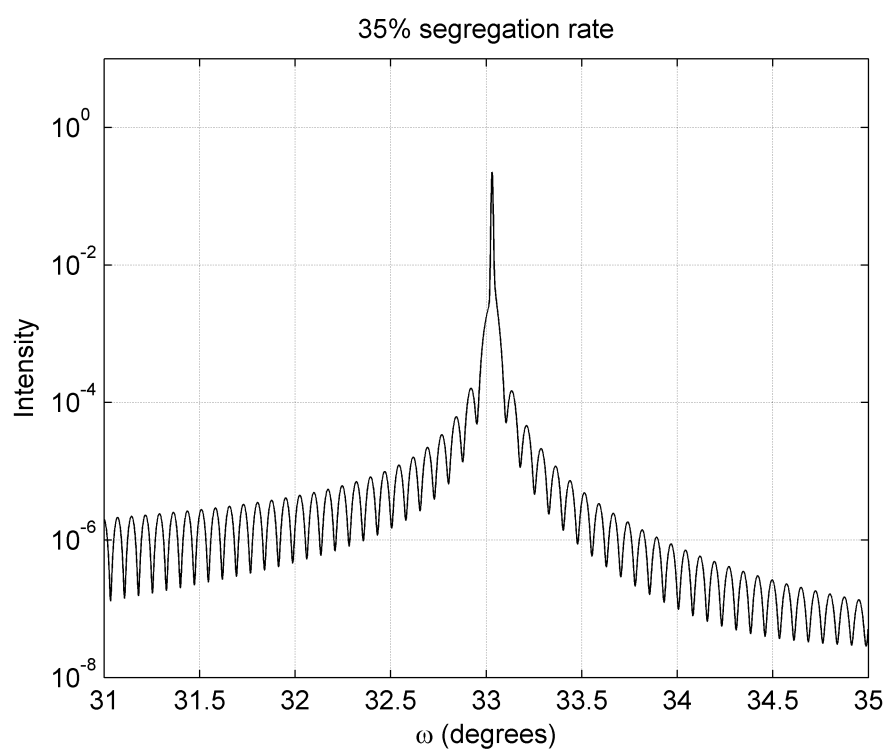


Figure 3.17

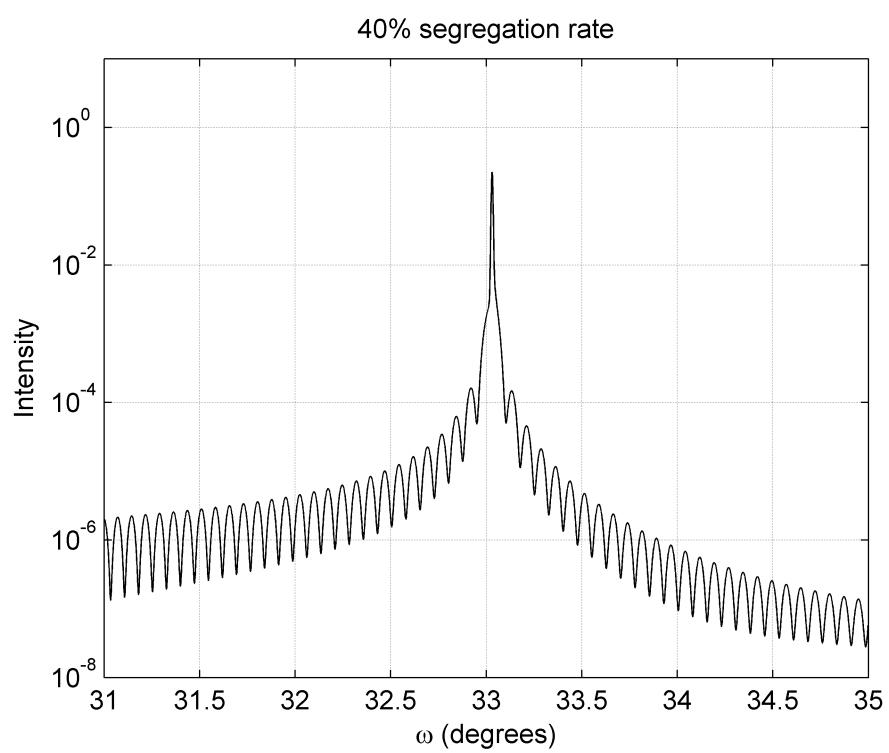


Figure 3.18

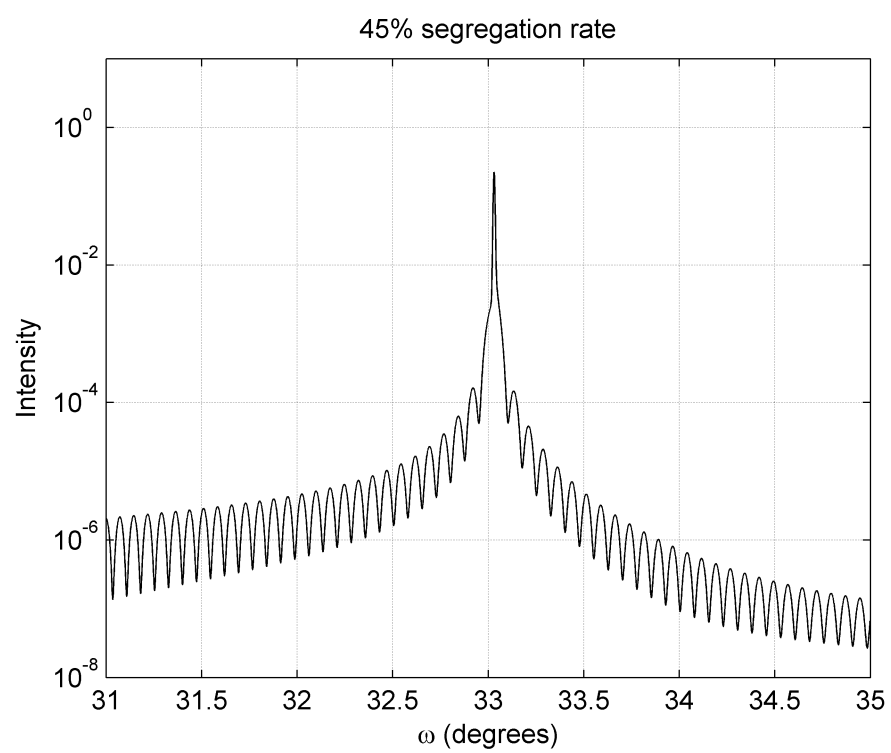


Figure 3.19

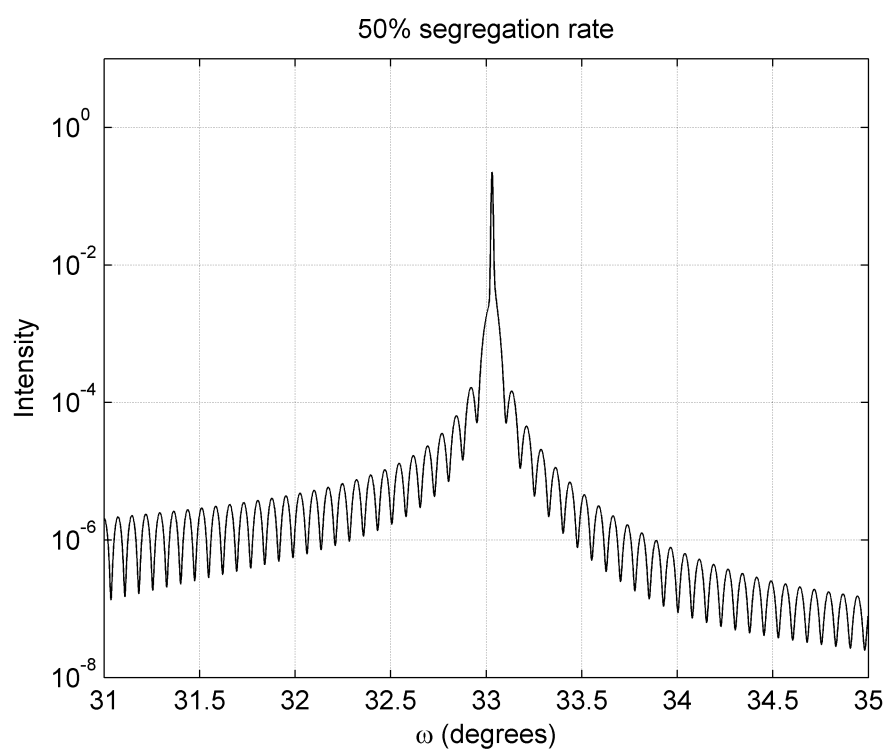


Figure 3.20

3.2 Case 2

Simulations with varying degrees of segregation are shown in figures 3.21–3.31

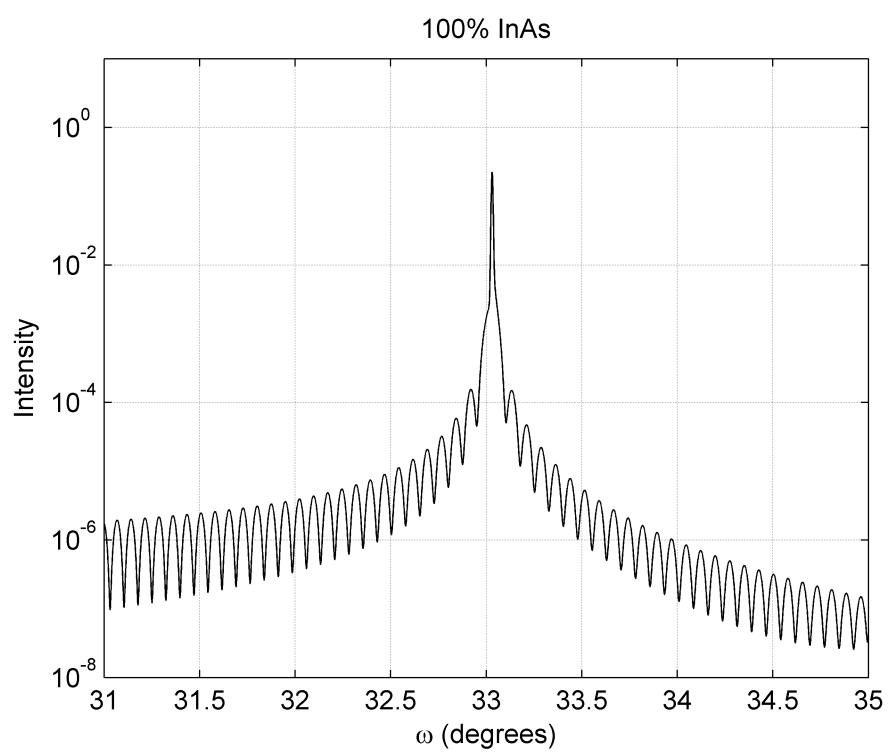


Figure 3.21

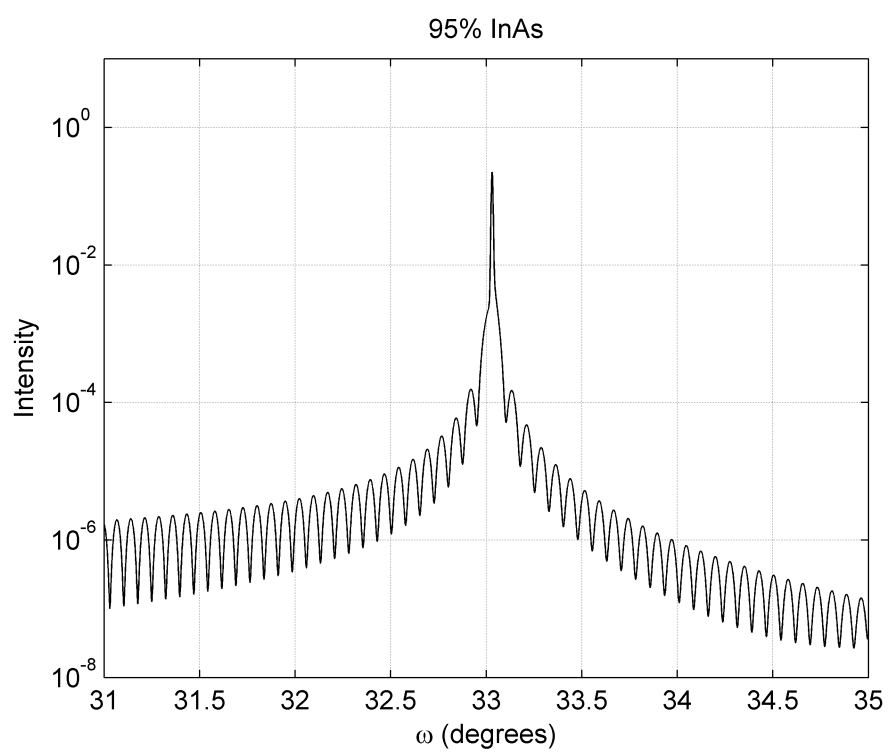


Figure 3.22

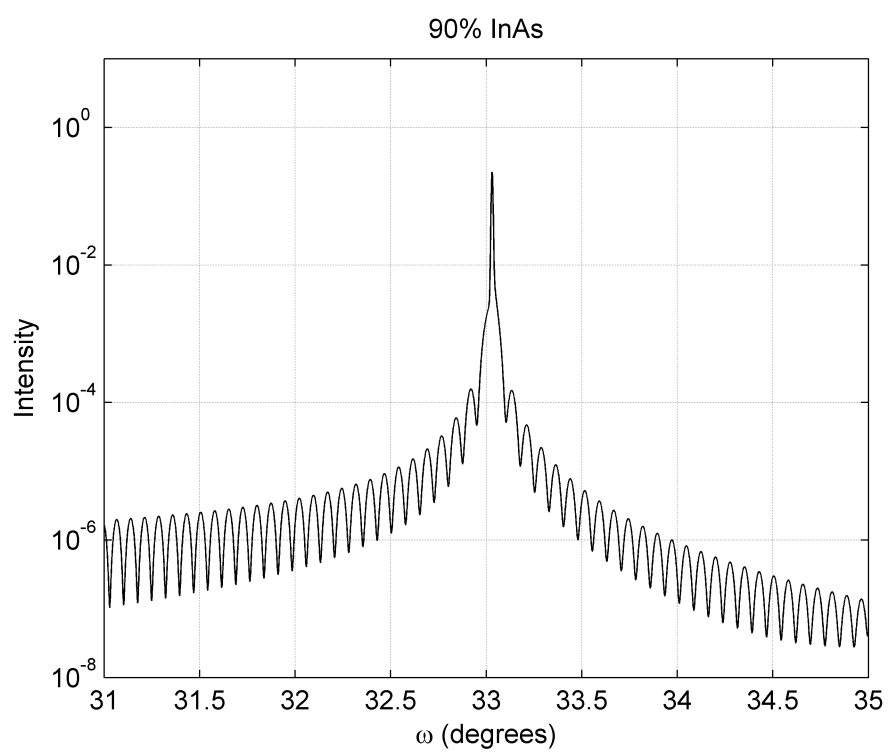


Figure 3.23

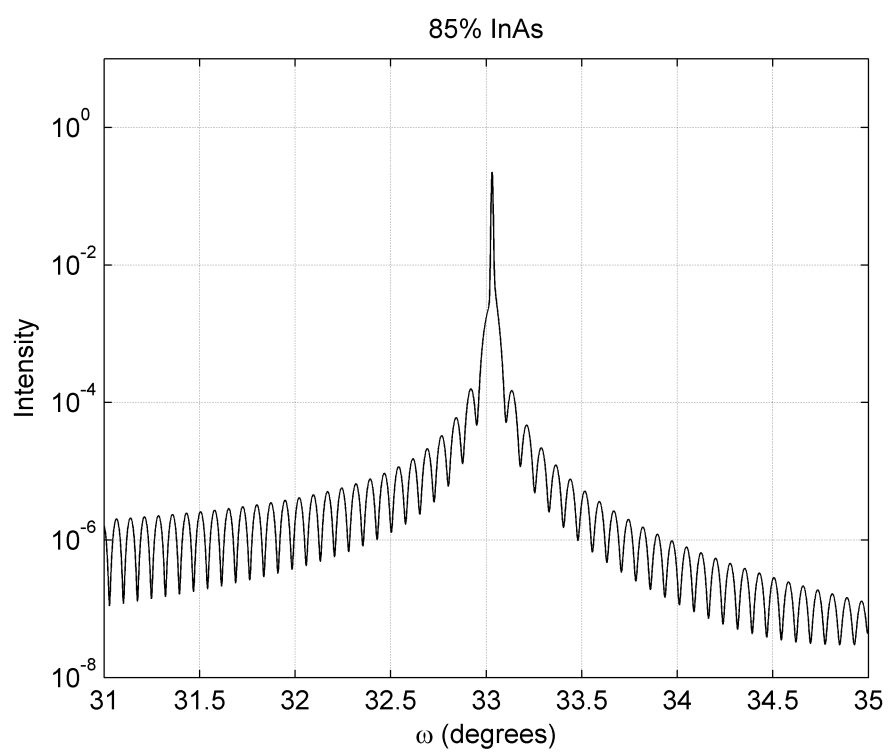


Figure 3.24

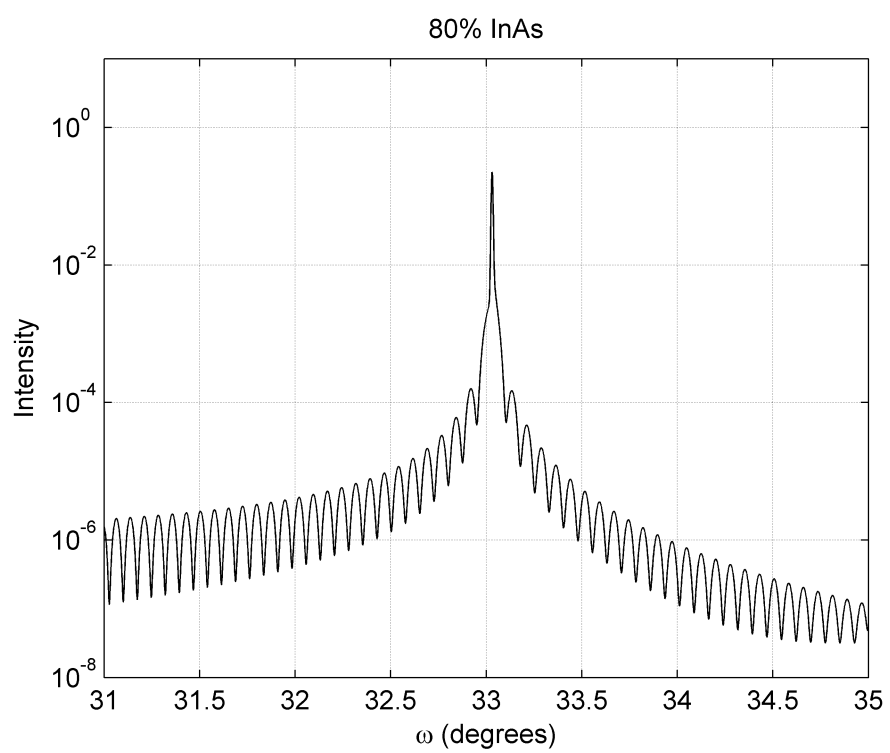


Figure 3.25

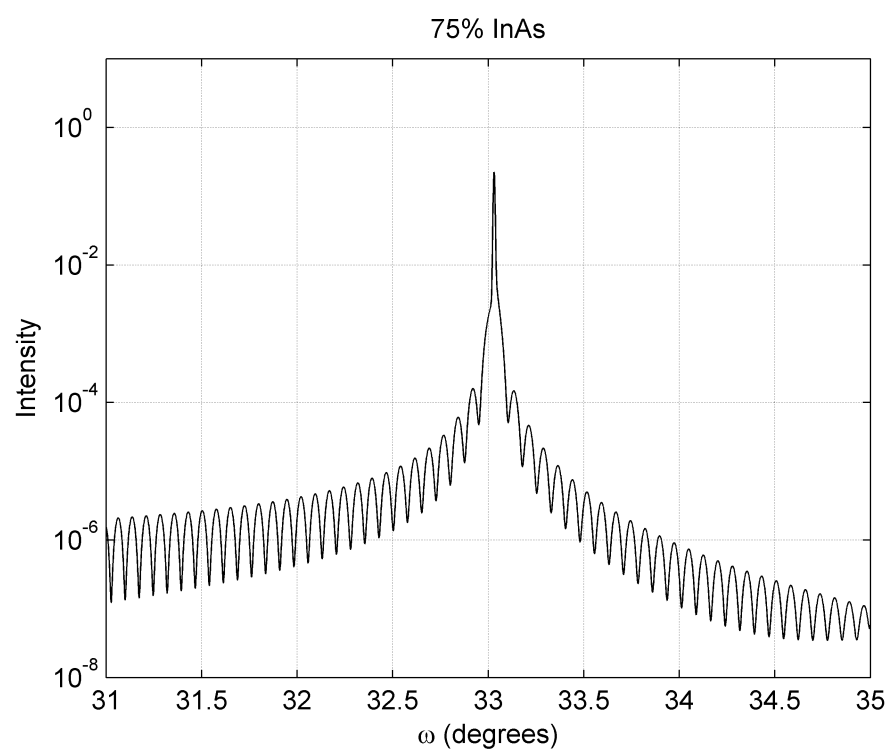


Figure 3.26

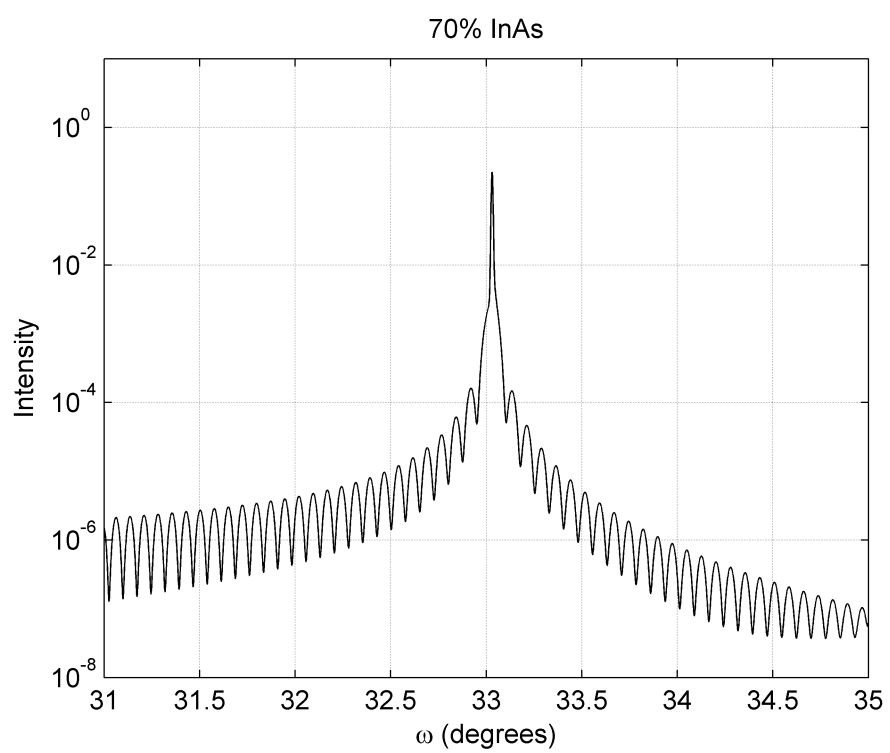


Figure 3.27

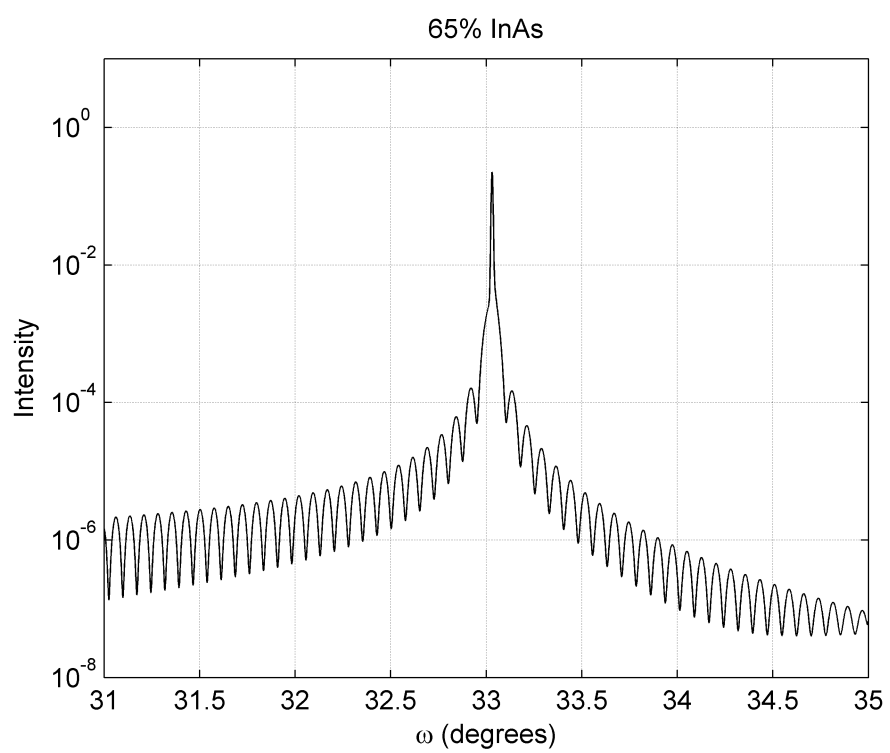


Figure 3.28

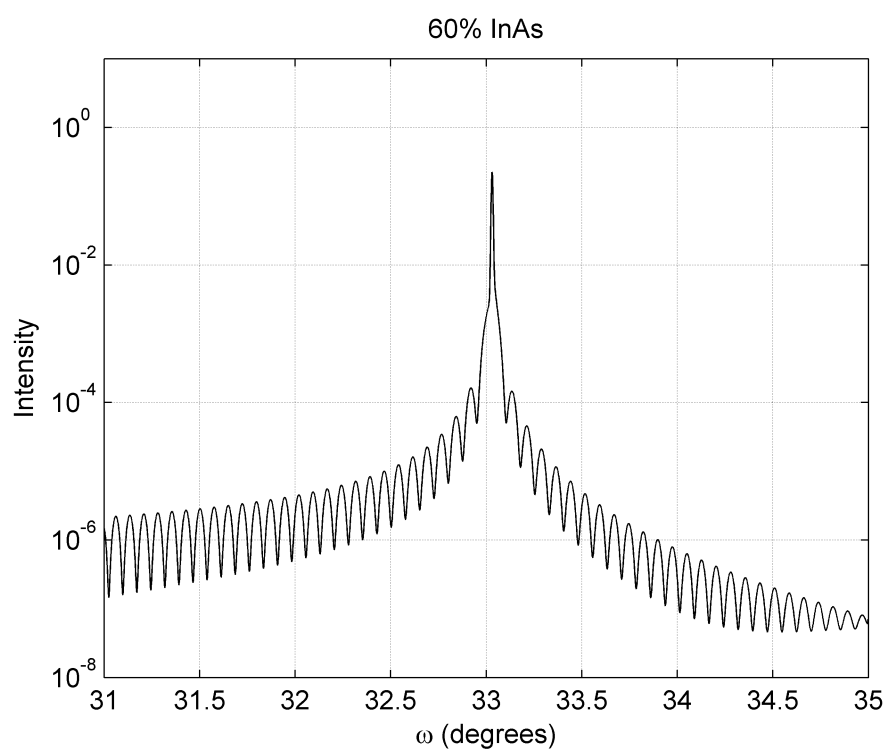


Figure 3.29

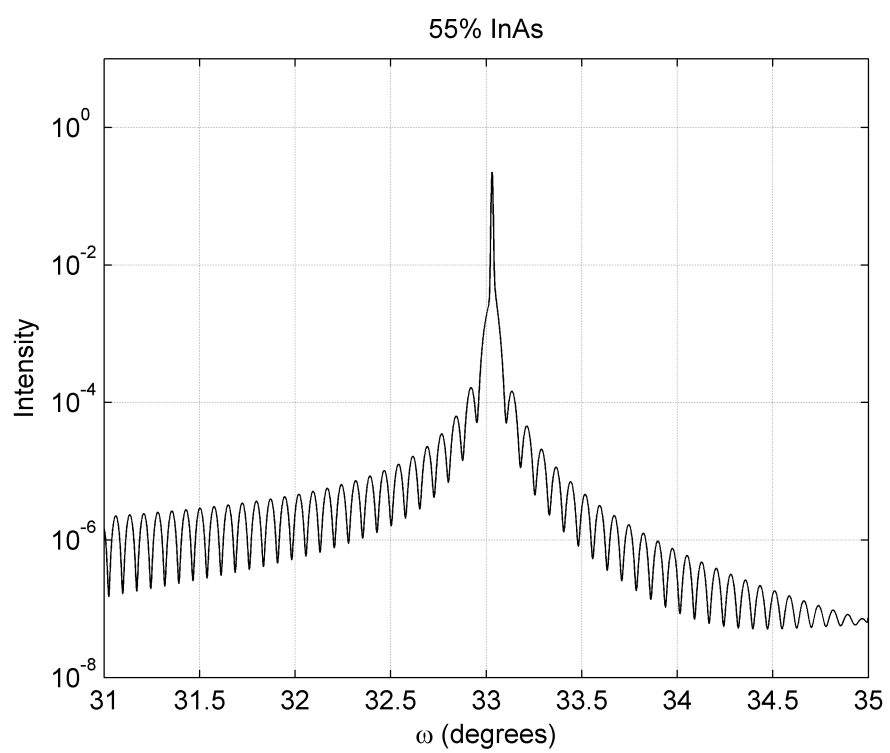


Figure 3.30

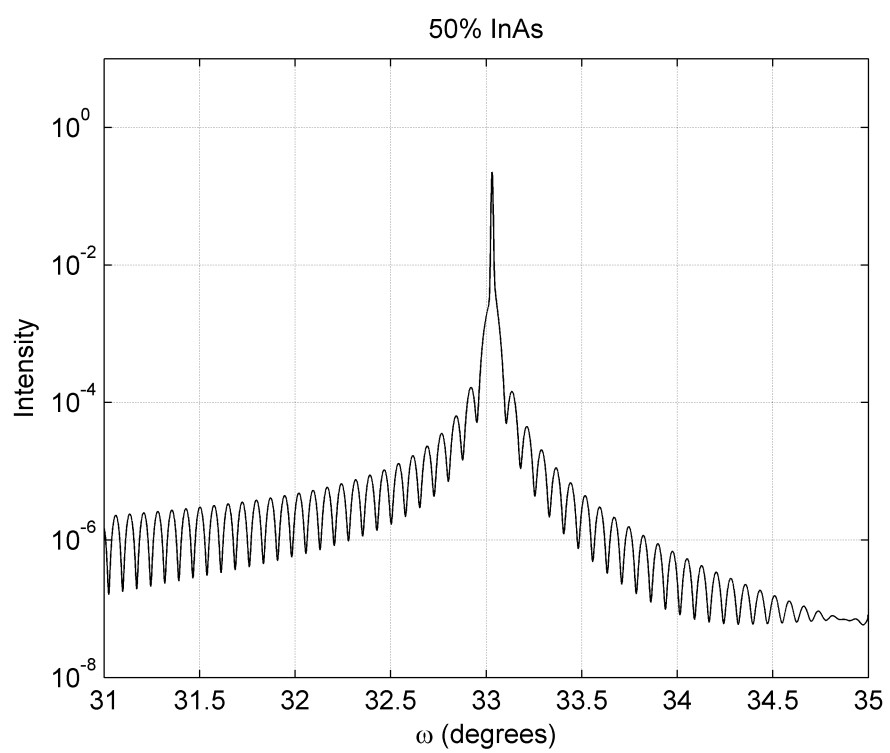


Figure 3.31

3.3 Case 3

Simulations with various InAs/GaAs distributions are shown in figures 3.32–3.35.

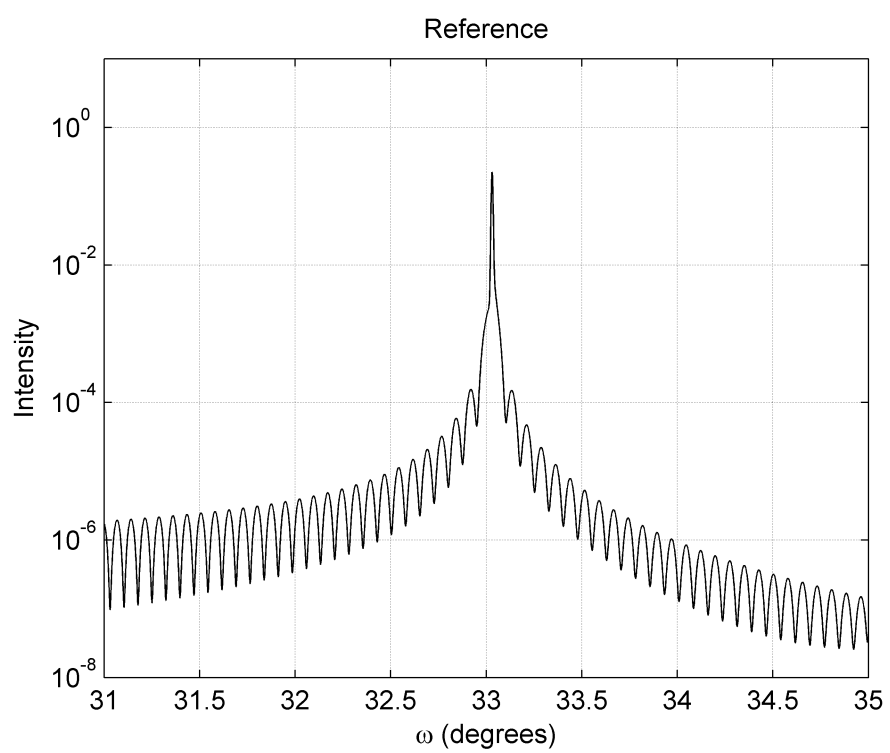


Figure 3.32

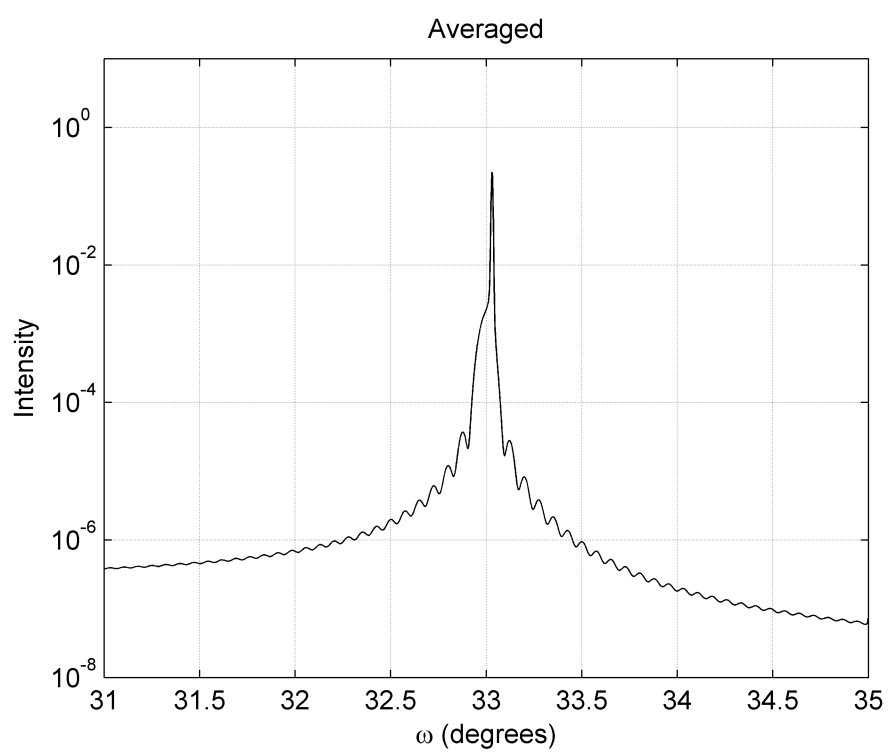


Figure 3.33

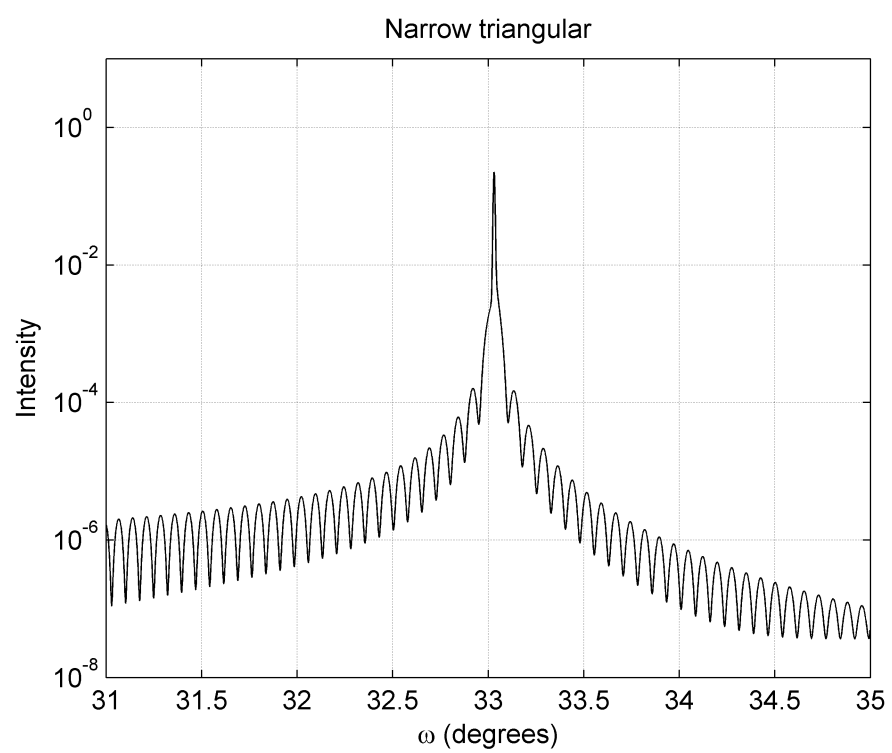


Figure 3.34

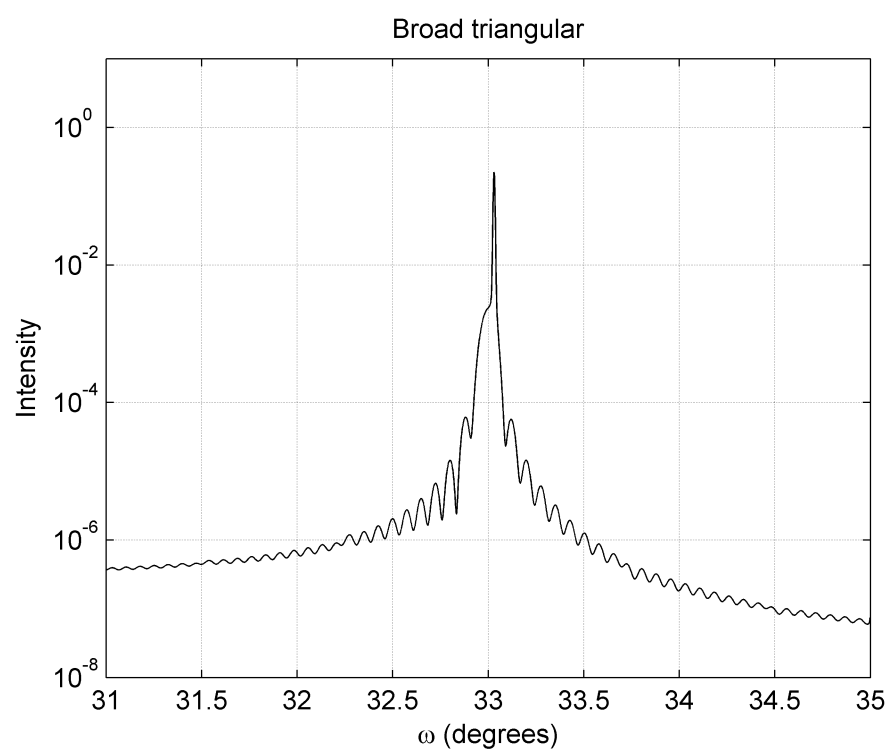


Figure 3.35

Chapter 4

Intermixing and segregation

Simulations with different cases of intermixing and segregation are shown in figures 4.1–4.11.

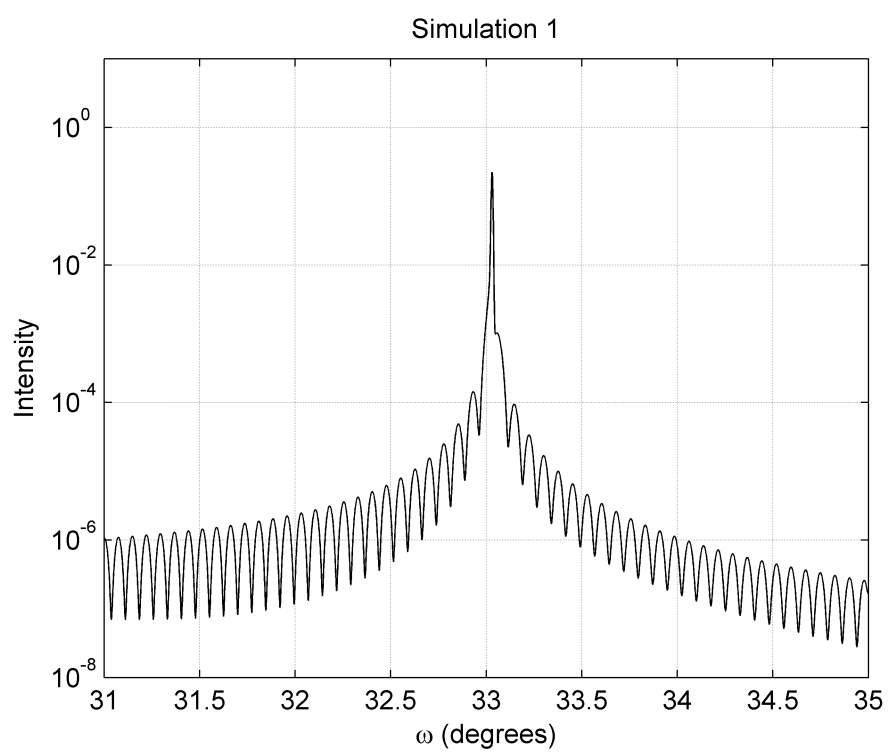


Figure 4.1

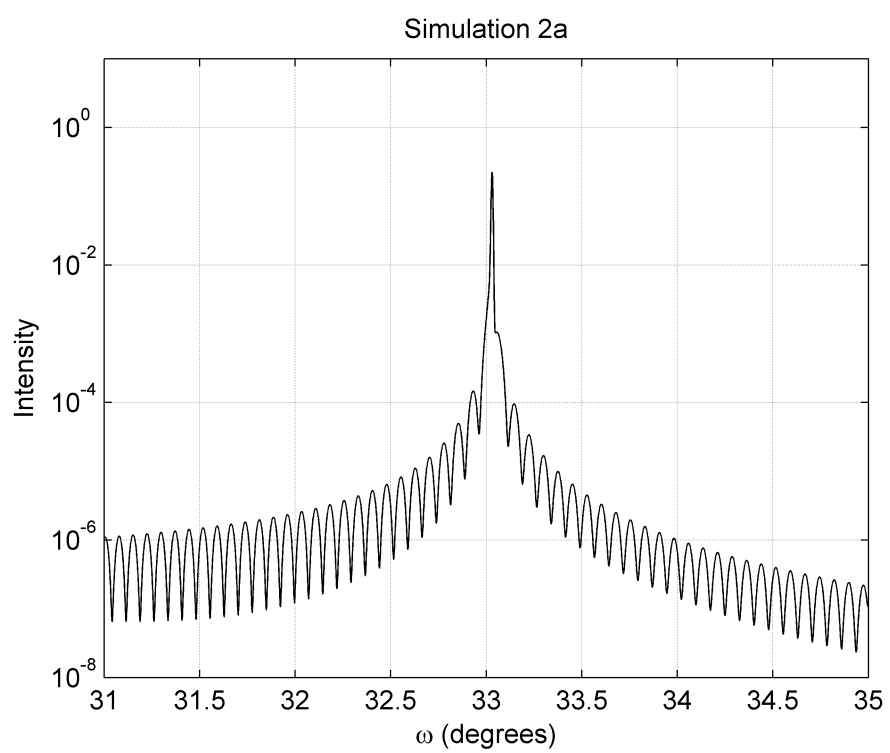


Figure 4.2

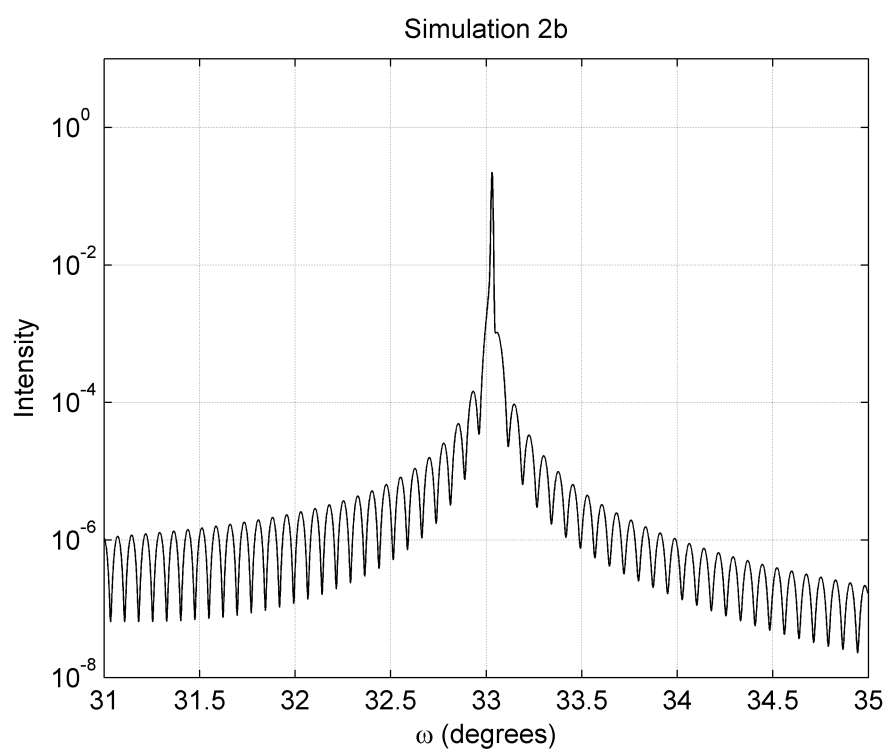


Figure 4.3

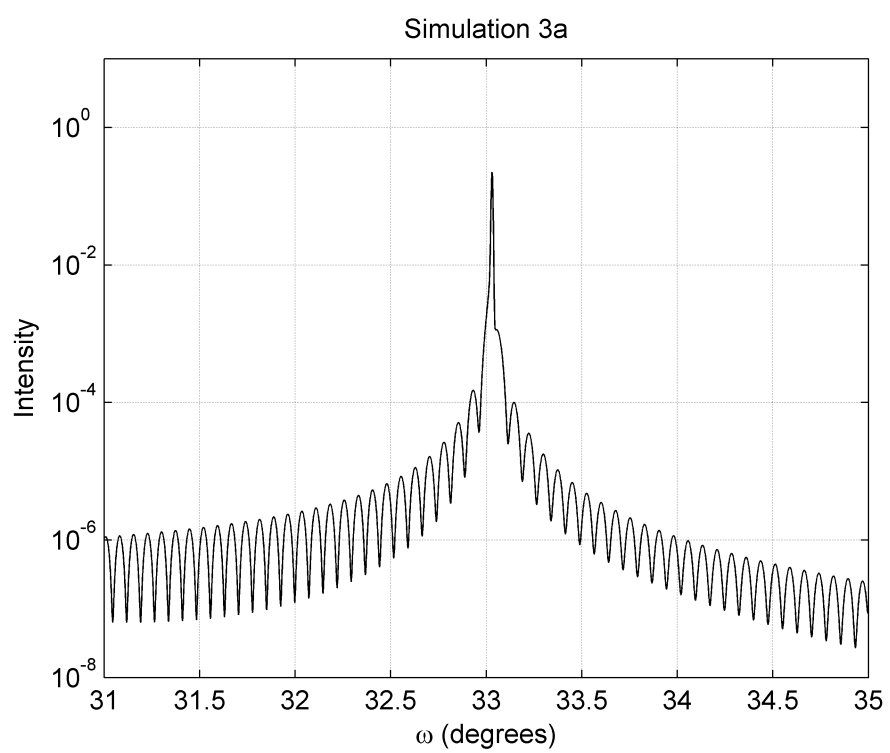


Figure 4.4

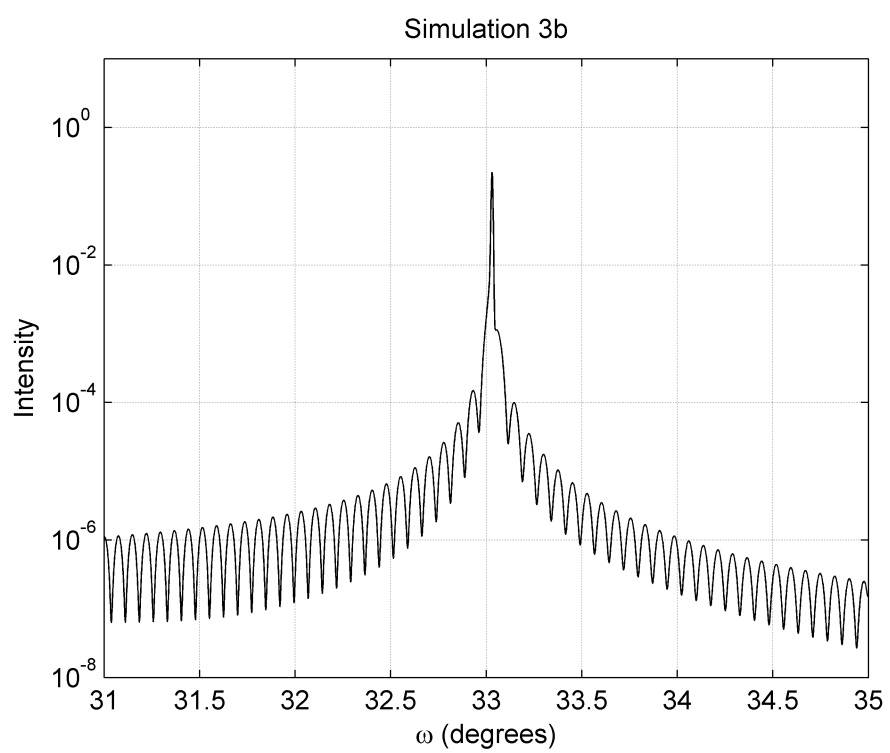


Figure 4.5

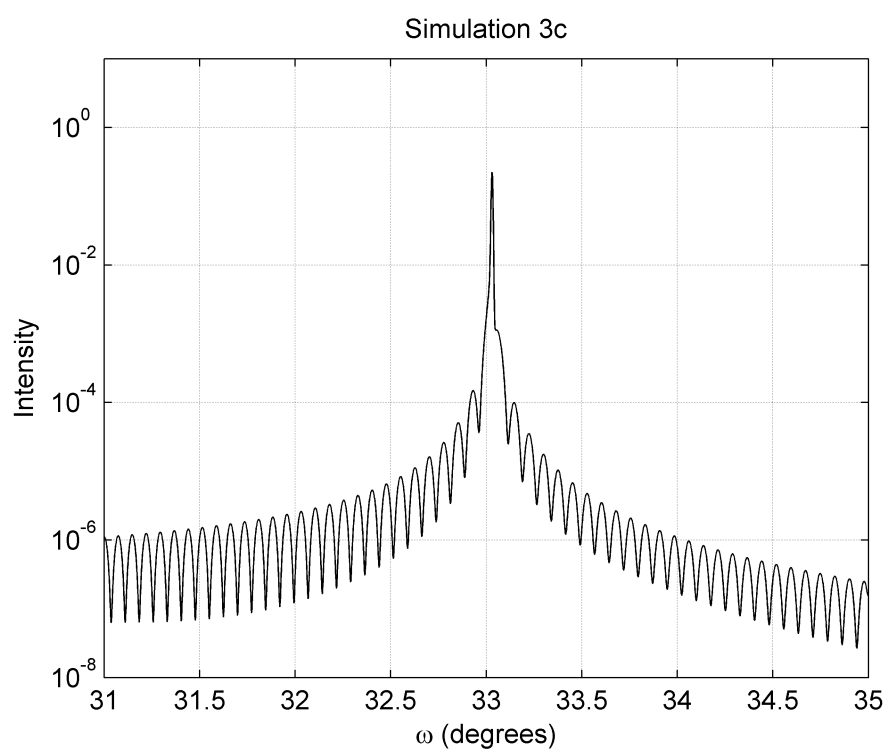


Figure 4.6

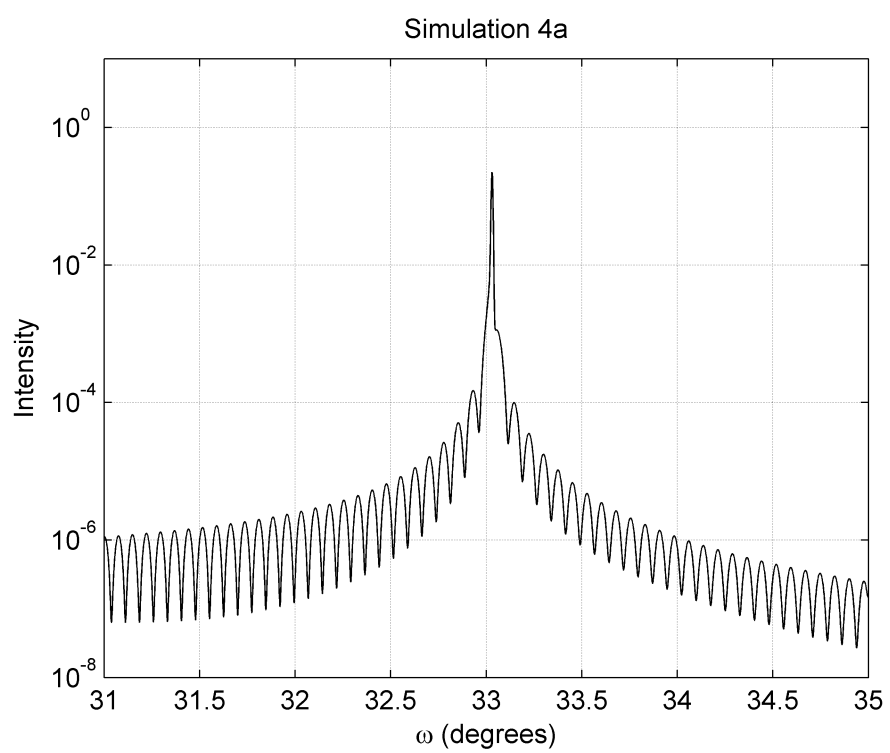


Figure 4.7

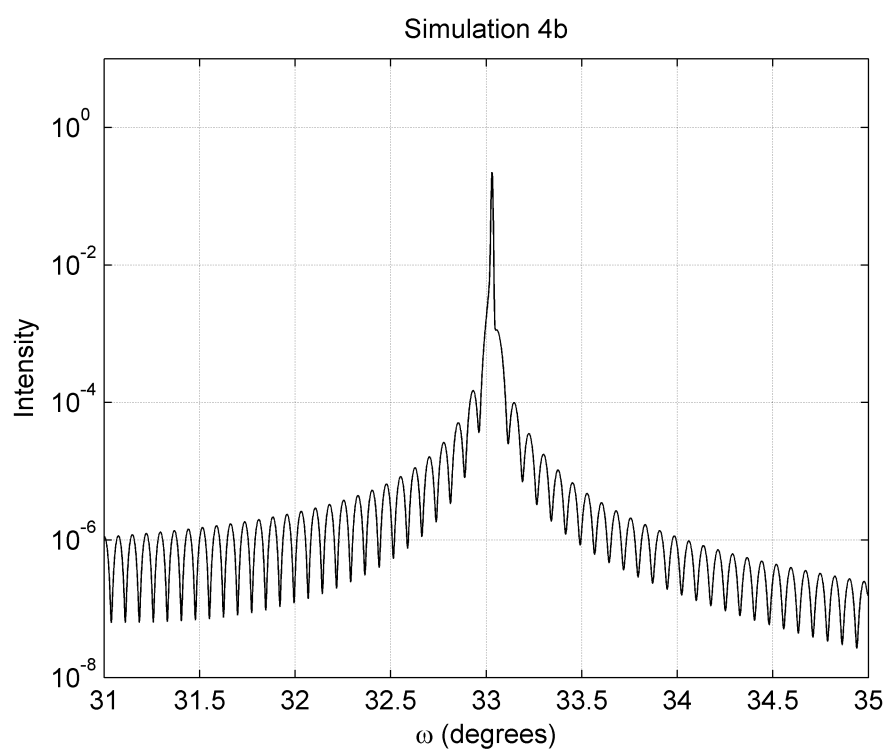


Figure 4.8

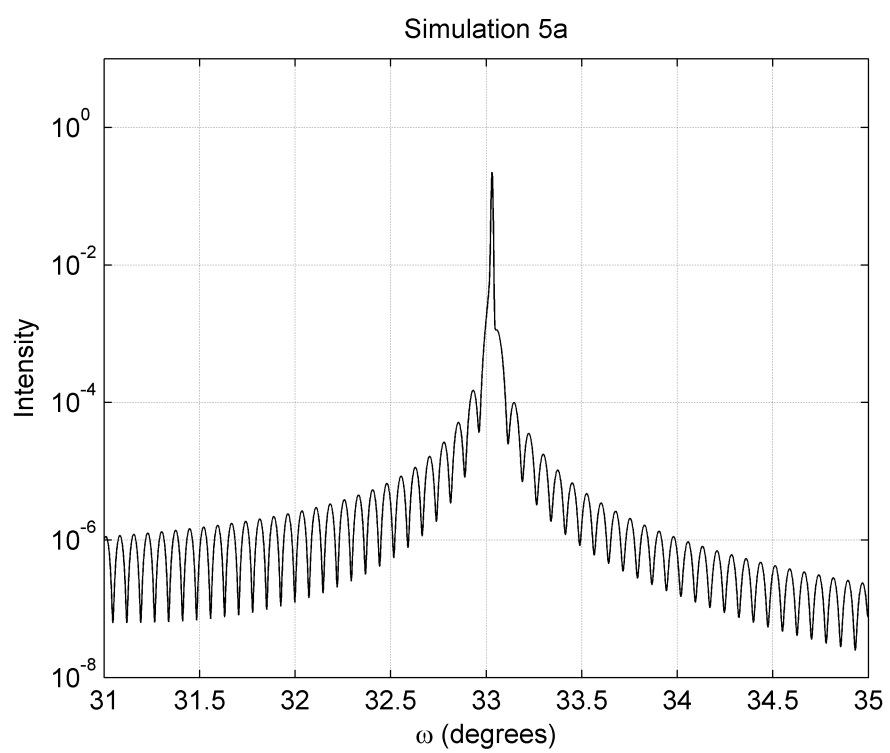


Figure 4.9

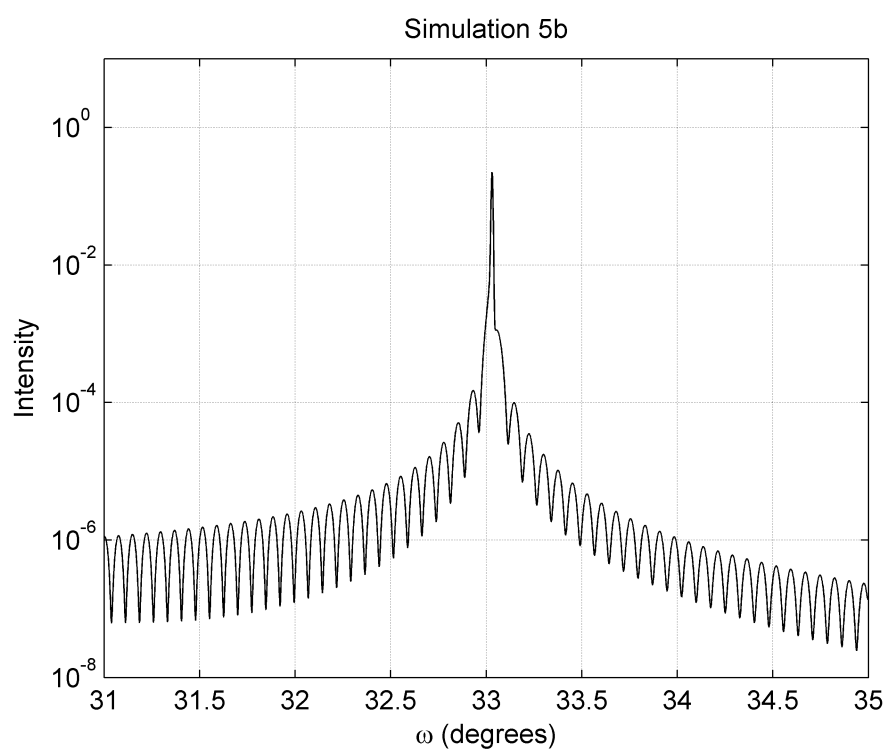


Figure 4.10

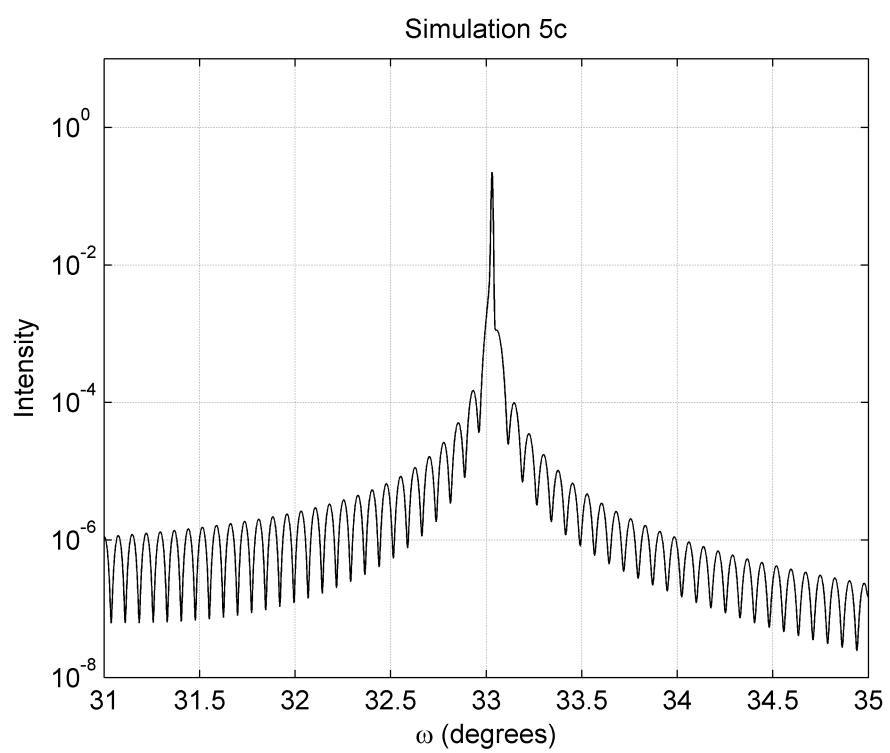


Figure 4.11